

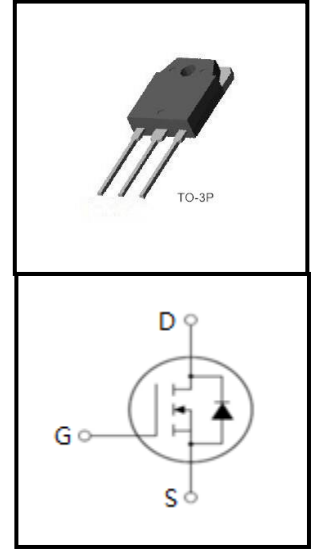
900V N-Channel MOSFET

FEATURES

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)



Device Marking and Package Information		
Device	Package	Marking
CS10N90V	TO-3P	CS10N90V

Absolute Maximum Ratings $T_C = 25^\circ\text{C}$, unless otherwise noted			
Parameter	Symbol	Value	Unit
		TO-3P	
Drain-Source Voltage ($V_{GS} = 0V$)	V_{DSS}	900	V
Continuous Drain Current	I_D	10	A
Pulsed Drain Current (note1)	I_{DM}	40	A
Gate-Source Voltage	V_{GSS}	± 30	V
Single Pulse Avalanche Energy (note2)	E_{AS}	460.8	mJ
Avalanche Current (note1)	I_{AS}	9.6	A
Repetitive Avalanche Energy (note1)	E_{AR}	276.5	mJ
Power Dissipation ($T_C = 25^\circ\text{C}$)	P_D	70	W
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55~+150	$^\circ\text{C}$

Thermal Resistance			
Parameter	Symbol	Value	Unit
		TO-3P	
Thermal Resistance, Junction-to-Case	R_{thJC}	2	K/W
Thermal Resistance, Junction-to-Ambient	R_{thJA}	50	

Specifications $T_J = 25^{\circ}\text{C}$, unless otherwise noted

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	900	--	--	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 900V, V _{GS} = 0V, T _J = 25°C	--	--	1	μA
Gate-Source Leakage	I _{GSS}	V _{GS} = ±30V	--	--	±100	nA
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	3.0	--	4.0	V
Drain-Source On-Resistance (Note3)	R _{DS(on)}	V _{GS} = 10V, I _D =5A	--	0.86	1.05	Ω
Dynamic						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 25V, f = 1.0MHz	--	2142	--	pF
Output Capacitance	C _{oss}		--	202	--	
Reverse Transfer Capacitance	C _{rss}		--	38	--	
Total Gate Charge	Q _g	V _{DD} = 720V, I _D = 10A, V _{GS} = 10V	--	88	--	nC
Gate-Source Charge	Q _{gs}		--	9.8	--	
Gate-Drain Charge	Q _{gd}		--	48.5	--	
Turn-on Delay Time	t _{d(on)}	V _{DD} = 450V, I _D = 10A, R _G = 25 Ω	--	50.5	--	ns
Turn-on Rise Time	t _r		--	38	--	
Turn-off Delay Time	t _{d(off)}		--	366	--	
Turn-off Fall Time	t _f		--	74	--	
Drain-Source Body Diode Characteristics						
Continuous Body Diode Current	I _S	T _C = 25 °C	--	--	10	A
Pulsed Diode Forward Current	I _{SM}		--	--	40	
Body Diode Voltage	V _{SD}	T _J = 25°C, I _{SD} = 5A, V _{GS} = 0V	--	--	1.4	V
Reverse Recovery Time	t _{rr}	V _{GS} = 0V, I _S = 10A, di _F /dt =100A /μs	--	653	--	ns
Reverse Recovery Charge	Q _{rr}		--	2.4	--	μC

Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. $L=10mH, V_{DD} = 50V, R_G = 25 \Omega$, Starting $T_J = 25^{\circ}\text{C}$
3. Pulse Test: Pulse width $\leq 300\mu s$, Duty Cycle $\leq 1\%$

Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 1. Output Characteristics ($T_J = 25^\circ\text{C}$)

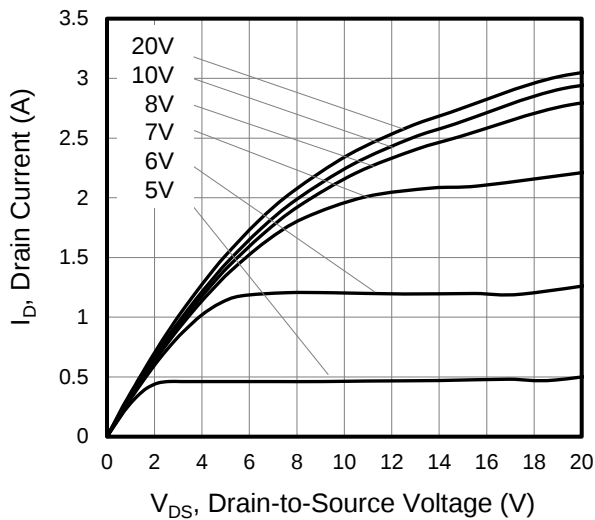


Figure 2. Body Diode Forward Voltage

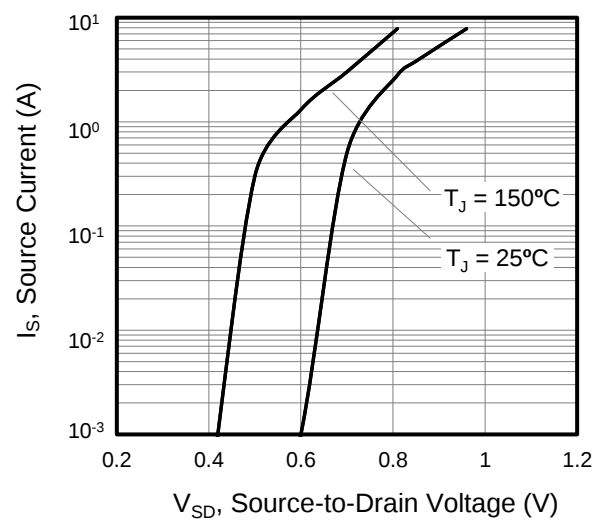


Figure 3. Drain Current vs. Temperature

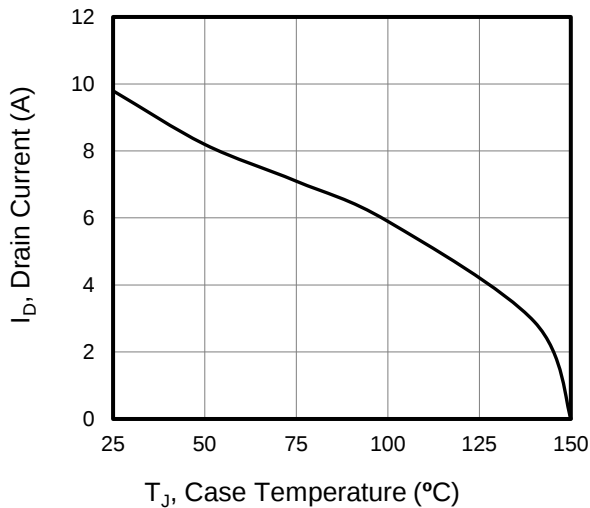


Figure 4. BV_{DSS} Variation vs. Temperature

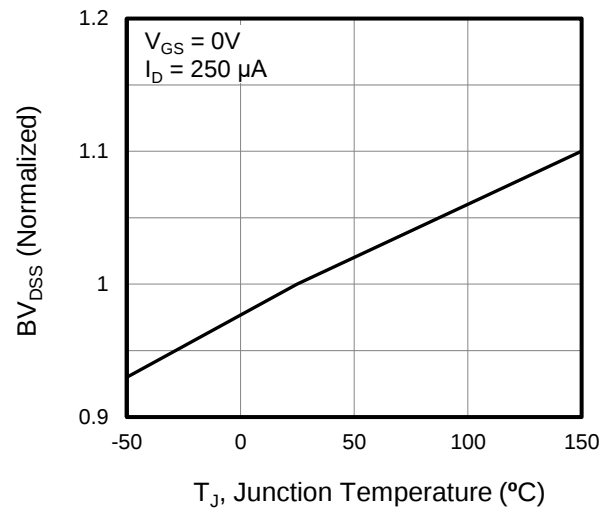


Figure 5. Transfer Characteristics

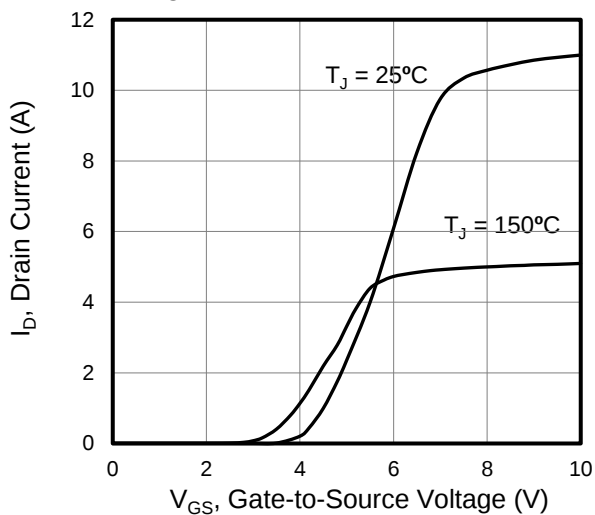
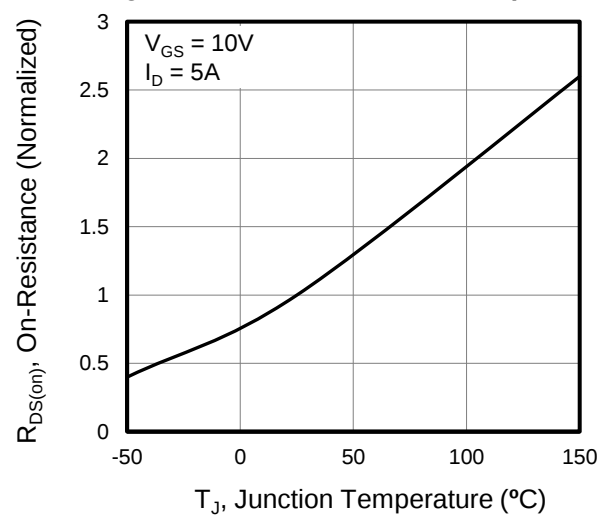


Figure 6. On-Resistance vs. Temperature



Typical Characteristics $T_j = 25^\circ\text{C}$, unless otherwise noted

Figure 7. Capacitance

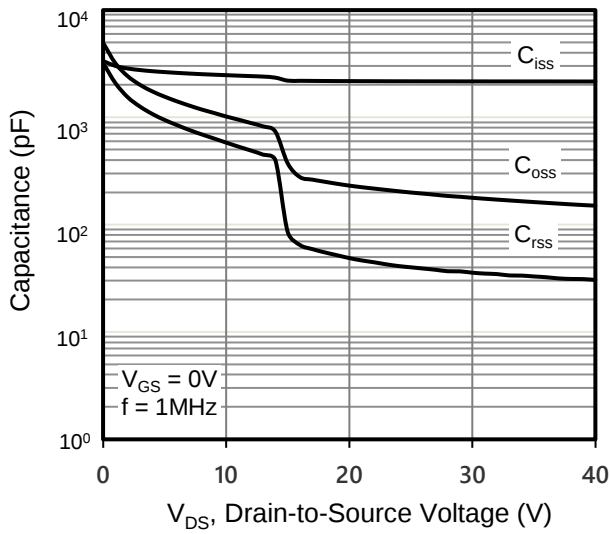


Figure 8. Gate Charge

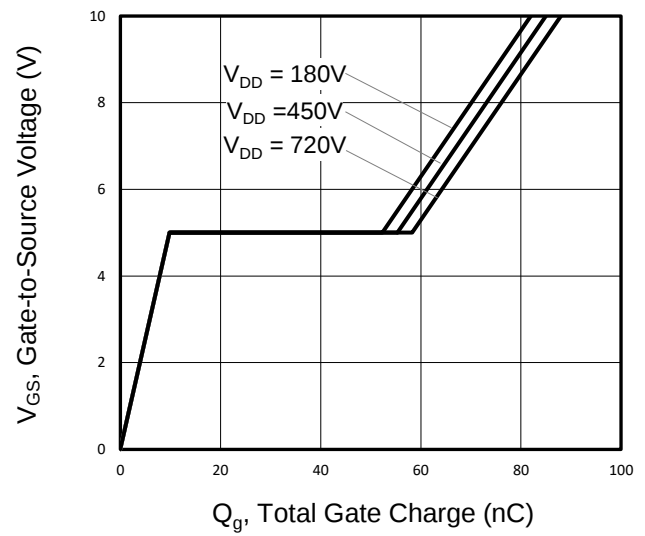


Figure 9. Transient Thermal Impedance

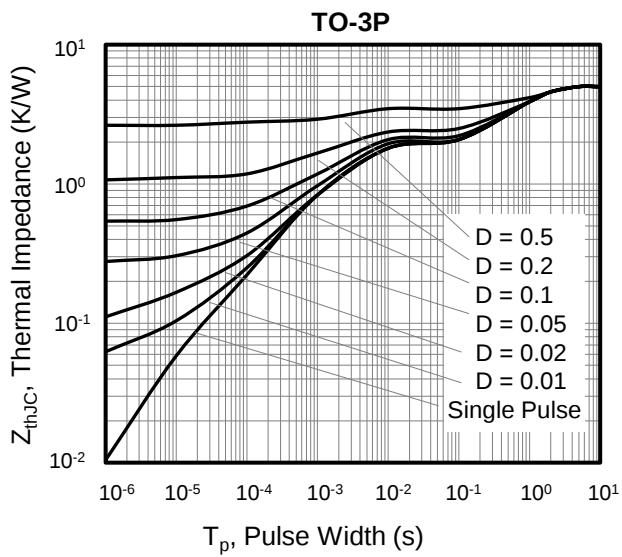


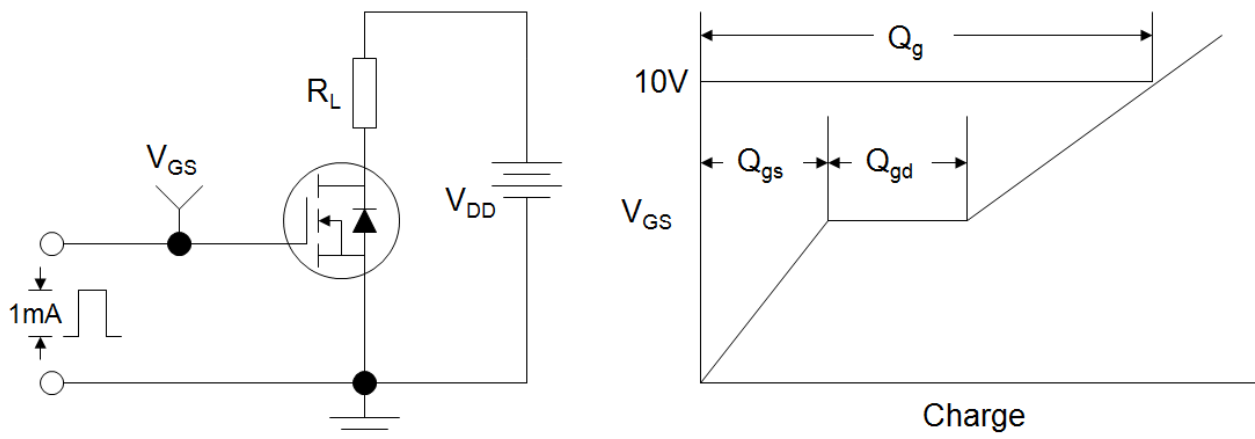
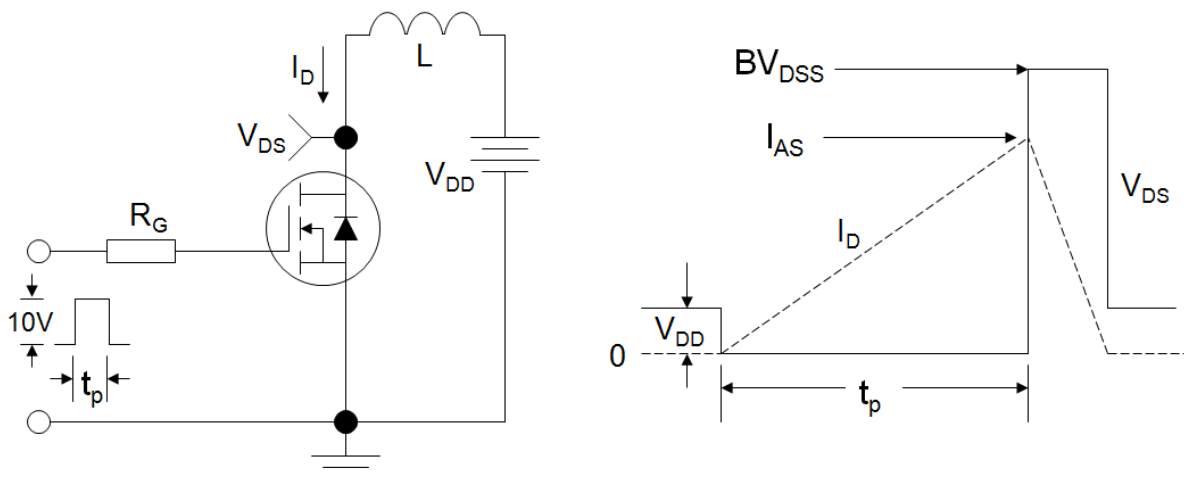
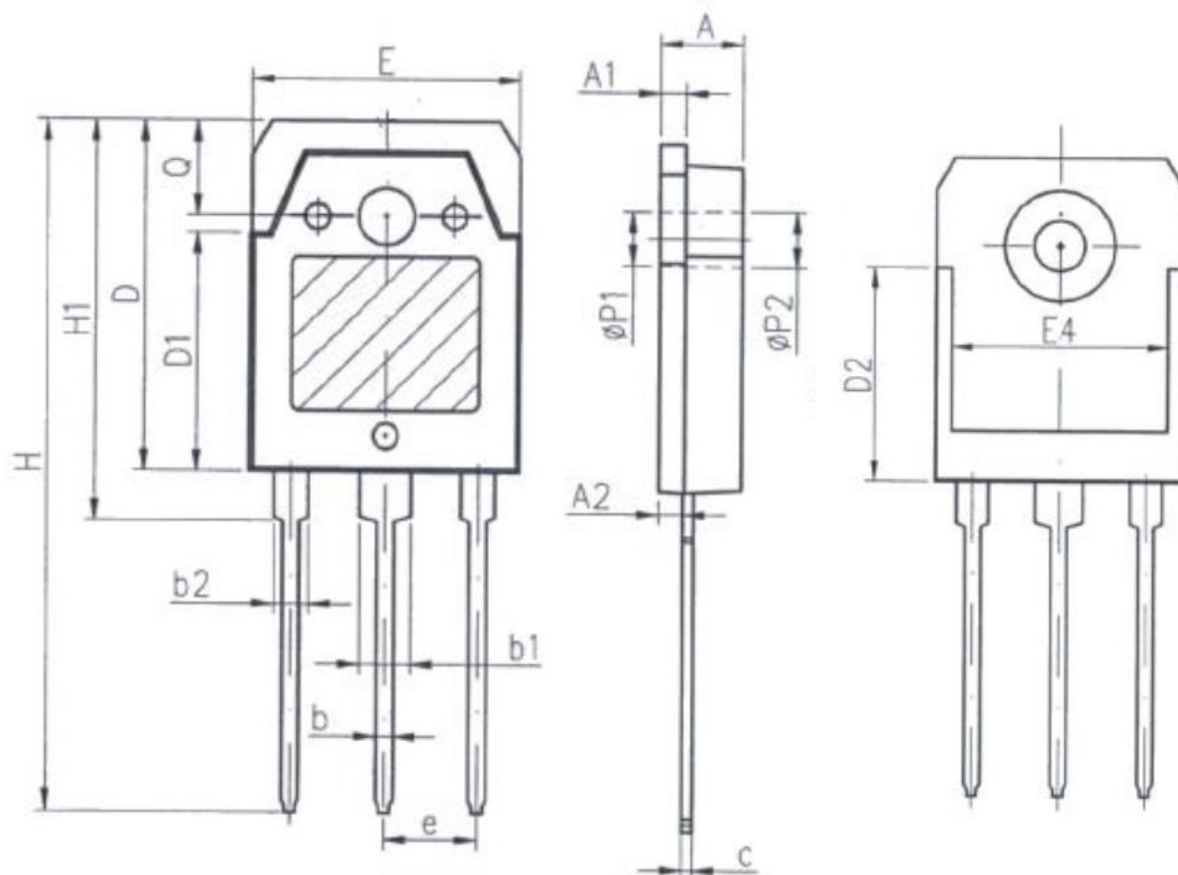
Figure A: Gate Charge Test Circuit and Waveform

Figure B: Resistive Switching Test Circuit and Waveform

Figure C: Unclamped Inductive Switching Test Circuit and Waveform


TO-3P



Unit:mm		
Symbol	Min.	Max.
A	4.6	5
A1	1.4	1.65
A2	1.18	1.58
b	0.8	1.2
b1	2.8	3.2
b2	1.8	2.2
c	0.5	0.75
D	19.6	20.2
D1	13.55	14.25
D2	12.9REF	
E	15.35	15.85
E4	12.6	-
e	5.45TYP	
H	40.1	40.9
H1	23.15	23.65
P1	3.2REF	
P2	3.5REF	

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