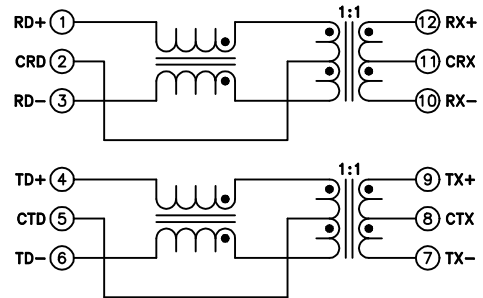
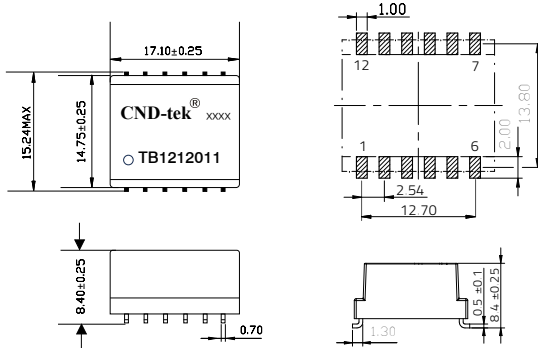


1. Compatible with various 10/100 Base-TX transceiver requiring 1:1 transmit and receive turns ratio
2. Compliant with IEEE 802.3af standard including baseline wander compensation specification of $350\mu\text{H}$ OCL when Biased at 24mA
3. Low profile Surface Mount Packaging designed for Hi-Temp Reflow Process
4. Single channel interface for 10/100Mbps Ethernet applications with CMC's tuned to Enhance EMC system performance
5. Suitable For End-span and Mid-span POE+ Applications 720mA Current Capability Per POE Port
6. Operating Temperature range: 0°C TO $+70^{\circ}\text{C}$
7. Storage temperature range: -25°C TO $+125^{\circ}\text{C}$



DIMENSIONS & SCHEMATICS

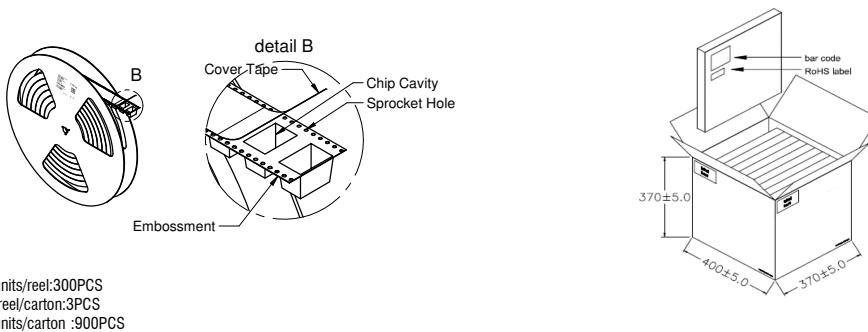


All 12 terminals must lie on a plane within .004mm of surface a after lead tinning

TECHNICAL SPECIFICATIONS

Inductance :	350 μH Min. @ 100 KHz, 100mV with 24mA DC Bias Lp(12-10)
Turns Ratio($\pm 2\%$):	350 μH Min. @ 100 KHz, 100mV with 24mA DC Bias Lp(7-9) 1CT:1CT,(1-3):(12-10) 1CT:1CT,(4-6):(9-7)
Insertion Loss:	-1.0 dB Max. @ 0.5M-100MHz
Return Loss:	-18dB Min. @ 0.5M-30MHz -15 dB Min. @ 30-45 MHz -13 dB Min.@ 45-60 MHz -10dB Min.@ 60-80 MHz -35 dB Min.@ 1-100 MHz
Cross Talk:	-33dB Min.@ 0.5M-100MHz
CMRR :	1500Vrms 1mA 60Second or 1800Vrms 1mA 3Second
Isolation HI-POT:	

PACKING SPECIFICATION



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