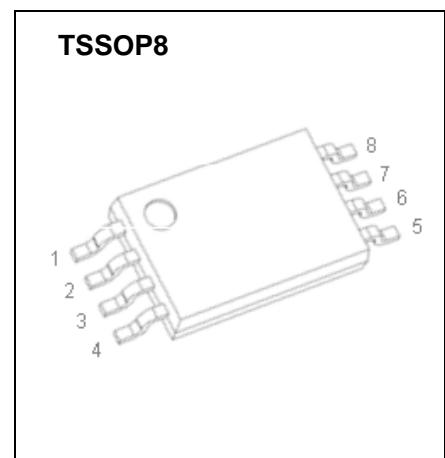


TSSOP8 Plastic-Encapsulate MOSFETS

CJS8810 Dual N-Channel MOSFET

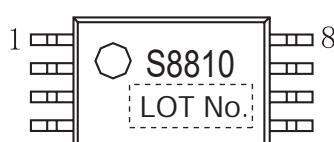
$V_{(BR)DSS}$	$R_{DS(on)}TYP$	I_D
20V	14.2mΩ@4.5V	7A
	15.4mΩ@3.8V	
	18.2mΩ@2.5V	
	26mΩ@1.8V	



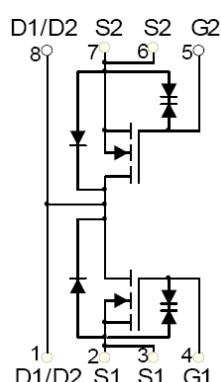
DESCRIPTION

The CJS8810 uses advanced trench technology to provide excellent $R_{DS(ON)}$ and low gate charge. It is ESD protected. This device is suitable for use as a uni-directional or bi-directional load switch, facilitated by its common-drain configuration.

MARKING:



Equivalent Circuit



MAXIMUM RATINGS ($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	20	V
Gate-Source Voltage	V_{GS}	±12	V
Continuous Drain Current	I_D	7	A
Pulsed Drain Current	I_{DM}^*	30	A
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	62.5	°C/W
Junction Temperature	T_j	150	°C
Storage Temperature	T_{stg}	-55~+150	°C
Lead Temperature for Soldering Purposes(1/8" from case for 10 s)	T_L	260	°C

*Repetitive rating: Pulse width limited by junction temperature.

MOSFET ELECTRICAL CHARACTERISTICS

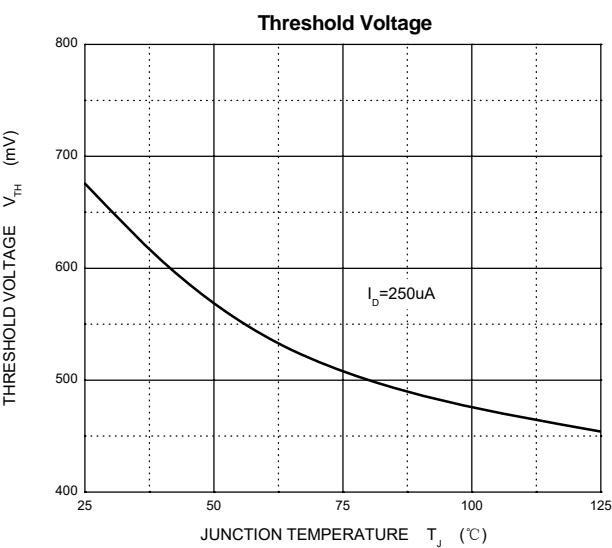
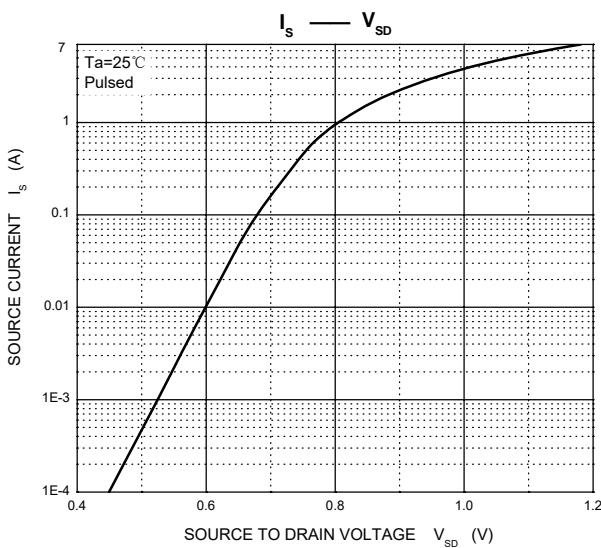
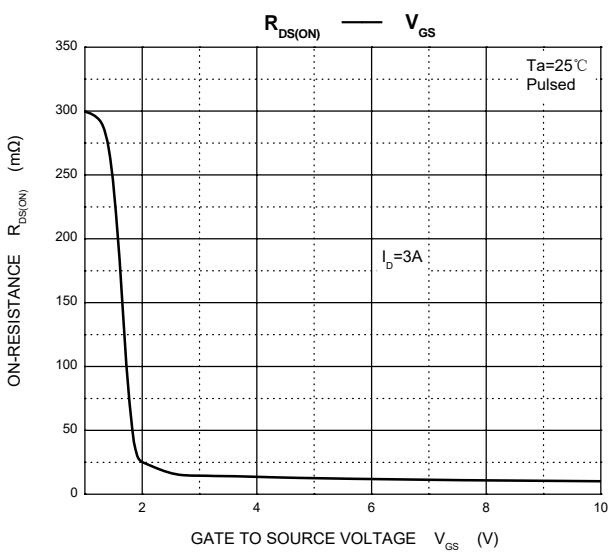
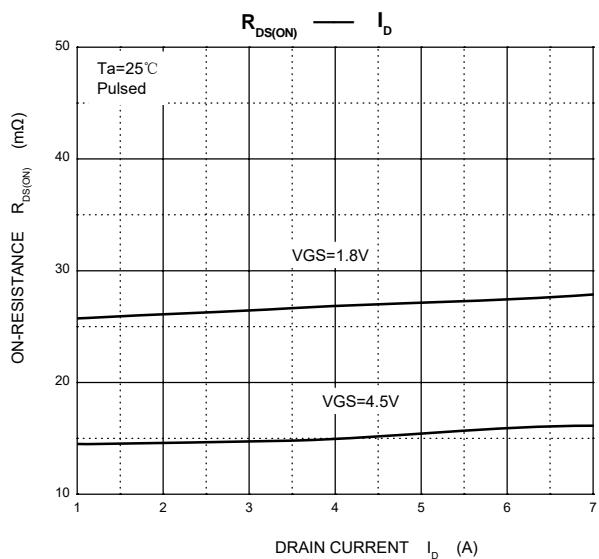
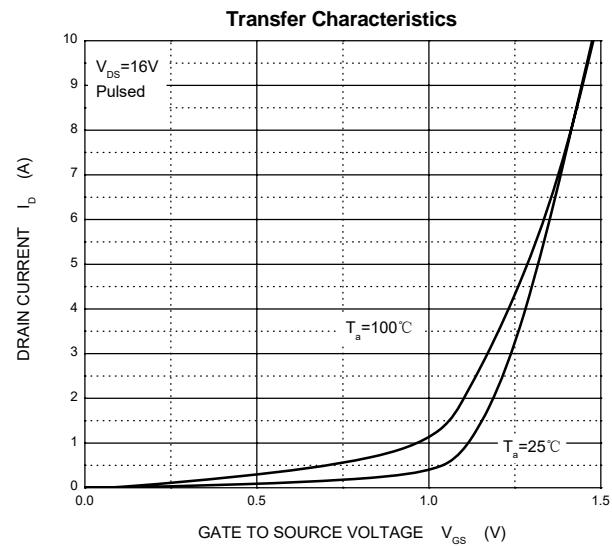
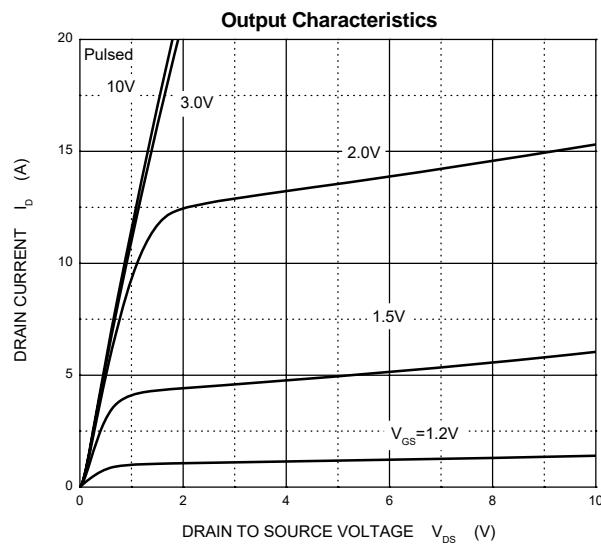
T_a=25 °C unless otherwise specified

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
STATIC PARAMETERS						
Drain-source breakdown voltage	V _(BR) DSS	V _{GS} = 0V, I _D = 250μA	20			V
Zero gate voltage drain current	I _{DSS}	V _{DS} = 16V, V _{GS} = 0V			1	μA
Gate-body leakage current	I _{GSS}	V _{GS} = ±4.5V, V _{DS} = 0V			±1	μA
		V _{GS} = ±8V, V _{DS} = 0V			±10	μA
Gate threshold voltage (note 1)	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	0.4		1	V
Drain-source on-resistance (note 1)	R _{DS(on)}	V _{GS} = 10V, I _D = 3A		14	17	mΩ
		V _{GS} = 4.5V, I _D = 3A		14.2	19	mΩ
		V _{GS} = 3.8V, I _D = 3A		15.4	21	mΩ
		V _{GS} = 2.5V, I _D = 3A		18.2	23	mΩ
		V _{GS} = 1.8V, I _D = 3A		26	32	mΩ
Forward transconductance (note 1)	g _{FS}	V _{DS} = 5V, I _D = 7A	9			S
Diode forward voltage(note 1)	V _{SD}	I _S = 1A, V _{GS} = 0V			1	V
DYNAMIC PARAMETERS (note 2)						
Input Capacitance	C _{iss}	V _{DS} = 10V, V _{GS} = 0V, f = 1MHz		1150		pF
Output Capacitance	C _{oss}			185		pF
Reverse Transfer Capacitance	C _{rss}			145		pF
Total gate charge	Q _g	V _{DS} = 10V, V _{GS} = 4.5V, I _D = 7A		15		nC
Gate-source charge	Q _{gs}			0.8		nC
Gate-drain charge	Q _{gd}			3.2		nC
SWITCHING PARAMETERS(note 2)						
Turn-on delay time	t _{d(on)}	V _{GS} = 5V, V _{DD} = 10V, R _L = 1.35Ω, R _{GEN} = 3Ω		6		ns
Turn-on rise time	t _r			13		ns
Turn-off delay time	t _{d(off)}			52		ns
Turn-off fall time	t _f			16		ns

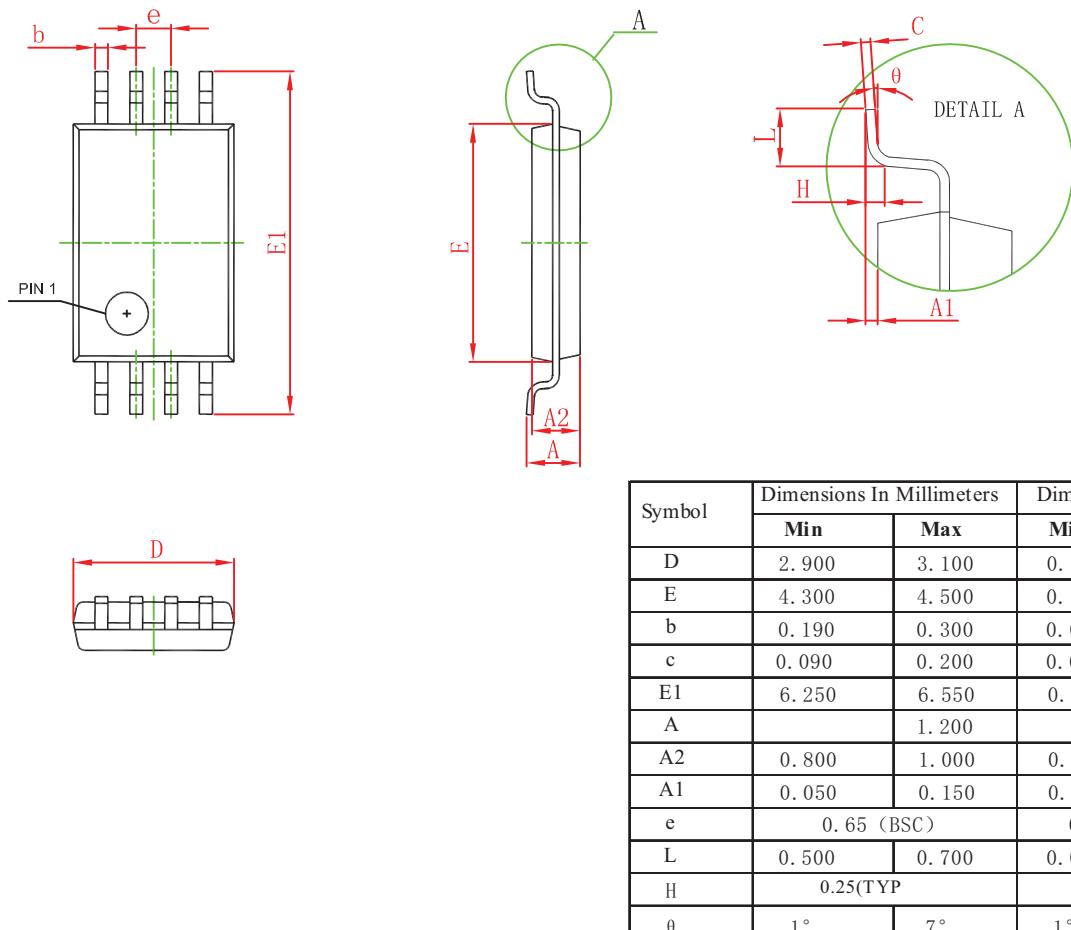
Notes :

1. Pulse Test : Pulse width≤300μs, duty cycle≤0.5%.
2. Guaranteed by design, not subject to production testing.

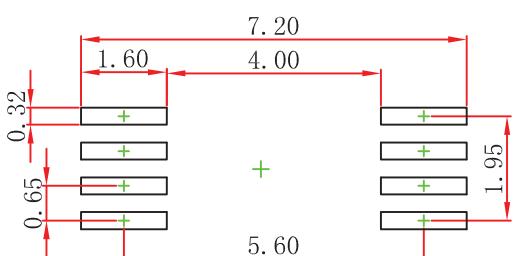
Typical Characteristics



TSSOP8 Package Outline Dimensions



TSSOP8 Suggested Pad Layout



Note:

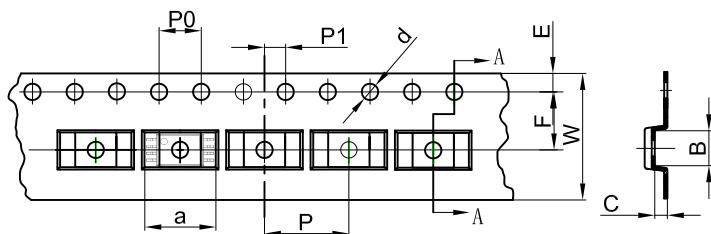
1. Controlling dimension: in millimeters.
2. General tolerance: $\pm 0.05\text{mm}$.
3. The pad layout is for reference purposes only.

NOTICE

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TSSOP8 Tape and Reel

TSSOP8 Embossed Carrier Tape



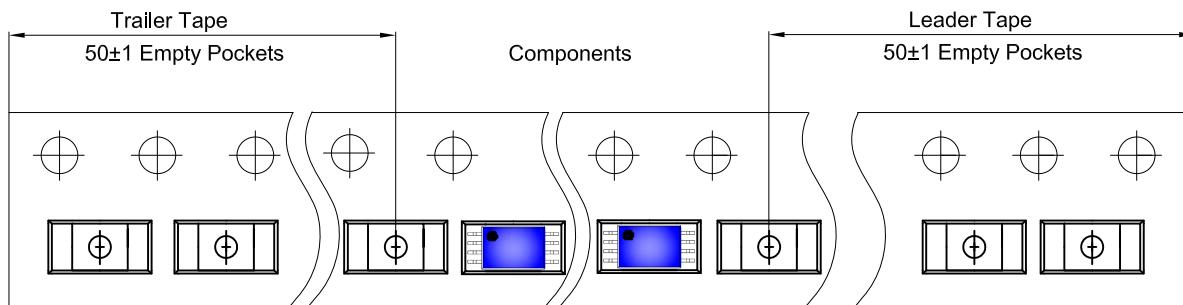
Packaging Description:

TSSOP8 parts are shipped in tape. The carrier tape is made from a dissipative (carbon filled) polycarbonate resin. The cover tape is a multilayer film (Heat Activated Adhesive in nature) primarily composed of polyester film, adhesive layer, sealant, and anti-static sprayed agent. These reeled parts in standard option are shipped with 3,000 units per 13" or 33cm diameter reel. The reels are clear in color and is made of polystyrene plastic (anti-static coated).

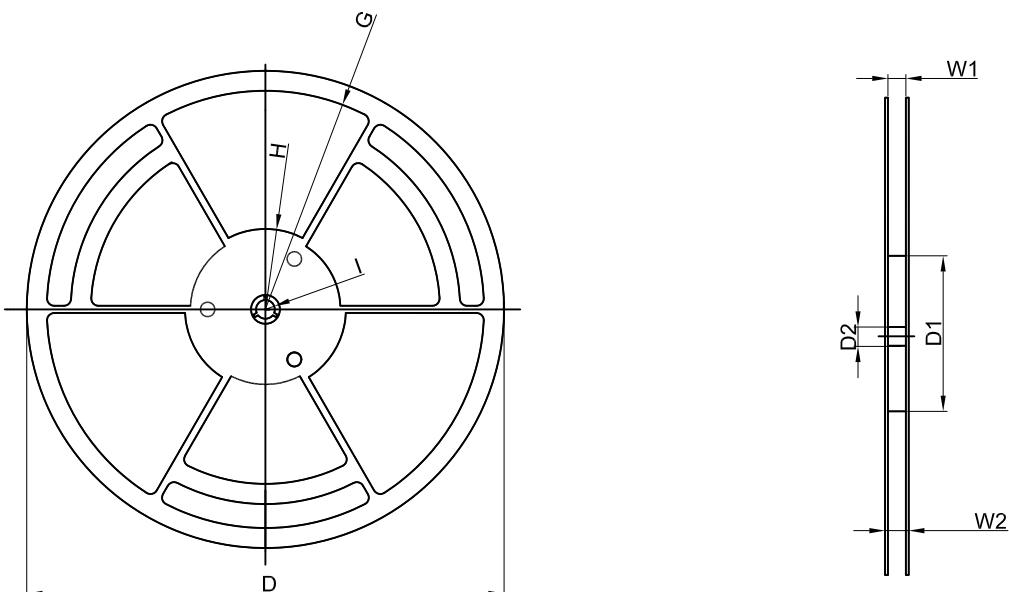
ALL DIM IN mm

Dimensions are in millimeter										
Pkg type	a	B	C	d	E	F	P0	P	P1	W
TSSOP8	6.76	3.30	1.20	Ø1.50	1.75	5.50	4.00	8.00	2.00	12.00

TSSOP8 Tape Leader and Trailer



TSSOP8 Reel



Dimensions are in millimeter								
Reel Option	D	D1	D2	G	H	I	W1	W2
13" Dia	Ø330.00	100.00	13.00	R151.00	R56.00	R6.50	12.40	17.60
REEL	Reel Size	Box	Box Size(mm)	Carton	Carton Size(mm)	G.W.(kg)		
3,000 pcs	13 inch	3,000 pcs	336×336×48	24,000 pcs	445×355×365			

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