

SMT GaAs HBT MMIC Divide-by-4, 0.05 - 4 GHz

Typical Applications

Prescaler for DC to C Band PLL Applications:

- UNII, Point-to-Point & VSAT Radios
- 802.11a & HiperLAN WLAN
- Fiber Optic
- Wireless infrastracture (W-CDMA, TD-SCDMA, WiMax, GSM, PCS, DCS, DECT)
- Cellular Infrastructure
- Satellites / VSATs
- Test Equipment/Instrumentation

Functional Diagram



Features

Ultra Low SSB Phase Noise: -150 dBc/Hz Single-Ended I/O's Output Power: -2 dBm Single DC Supply: +3V @ 53 mA

General Description

The ADMV2101 is a low noise Divide-by-4 Static Divider utilizing InGaP GaAs HBT technology in ultra small surface mount MSOP8 plastic package. This device operates from DC (with a square wave input) to 4 GHz input frequency with a single +3V DC supply. Single-ended inputs and outputs reduce component count and cost. The low additive SSB phase noise of -150 dBc/Hz at 100 kHz offset helps the user maintain good system noise performance.

Electrical Specifications, $T_{A} = +25^{\circ}$ C, 50 Ohm System, Vcc= +3Vdc ± 5%

Parameter	Conditions	Min.	Тур.	Max.	Units
Input Frequency ^{[1], [2]}	Sinewave	0.05		4	GHz
Input Power Range	Fin = 1GHz - 4GHz	-10		10	dBm
Output Power	Fin = 4GHz	-5.0	-2.8		
Reverse Leakage	RF Output Terminated, Fin= 2 GHz, Pin= 0 dBm		-20		dBm
SSB Phase Noise (100 kHz offset)	Pin= 0 dBm, Fin= 4 GHz		-150		dBc/Hz
Output Transition Time	Pin= 0 dBm, Fout= 882 MHz		120		ps
Supply Current (Icc)	Vcc= +3.0V		55	71	mA

Divider will operate down to DC levels. Square-wave input required below 200MHz.
For stable operation without an input sgnal, refer to Analog Devices Application Note, "Frequency Divider Operation & Compensation with No linput Signal."

4



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Input Sensitivity Window, T= 25 °C



Output Power vs. Temperature, Pin = 0 dBm



Output Harmonic Content, Pin= 0 dBm, T= 25 °C



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Input Sensitivity Window vs. Temperature







Reverse Leakage, Pin= 0 dBm, T= 25 °C



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Output Voltage Waveform, Pin= 0 dBm, Fout= 882 MHz, T= 25 °C



Absolute Maximum Ratings

RF Input Power (Vcc = +3V)	15 dBm
Nominal +3V Supply to GND	-0.3V to +3.5V
Max Peak Flow Temperature	260 °C
Storage Temperature	-65 to +125 °C
ESD Rating	FICDM - Class IV, HBM - Class 0

Reliability Information

Junction Temperature to Maintain 1 Million Hour MTTF	135 °C
Nominal Junction Temperature $(T = 85 \degree C)$	99 °C
Thermal Resistance (Junction to GND Paddle, 3V Supply)	83 °C/W
Operating Temperature	-40 to +85 °C

DC blocking capacitors are required at RF input and RF output ports. Choose value for lowest frequency of operation.



ELECTROSTATIC SENSITIVE DEVICE OBSERVE HANDLING PRECAUTIONS

Typical Supply Current vs. Vcc

Vcc (V)	Icc (mA)
2.70	45
3.0	55
3.30	66

Note: Divider will operate over full voltage range shown above

4

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Outline Drawing



COMPLIANT TO JEDEC STANDARDS MO-187-AA-T

Package Information

Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking
ADMV2101	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL3 ^[1]	#V 1B

[1] Max peak reflow temperature of 260 °C



v00.0417

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Pin Description

Pin Number	Function	Description	Interface Schematic
1	VCC	Main supply voltage, 3.0Vdc ± 0.3V	Vcc ○ 8pF 57∩ 57∩
2	IN	RF input; must use external DC block	
3, 6, 8	NC	No connection or ground. No internal bond.	
4, 5	GND	Ground. Must be connected to RF and DC ground.	GND =
7	OUT	RF output; must use external DC block	500 0 OUT
Exposed Paddle	GND	Ground. Must be connected to RF and DC ground.	

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