

2.4GHz Chip Antenna



AANI-CH-0133

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1.6 x 0.8 x 0.4 mm
RoHS/RoHS II Compliant
MSL Level = 1

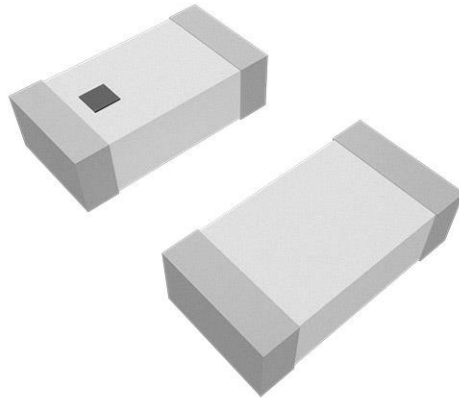
Features

- Miniaturized in Size: 1.6 x 0.8 x 0.4 mm
- Low Return Loss of: < -9.3 dB
- Peak Gain: 1.1 dBi
- Average Total Efficiency: -3.1 dB (49%)
- Surface Mount (SMD)
- Integration: PCB Corner Mounting

Applications

- Bluetooth®/ ZigBee® / Thread/ Matter/Wi-Fi®
- Bluetooth®/ Wi-Fi® Modules
- Consumer Hearables & Wearables
- Medical Wearables & Tele-Medicine
- Smart Home & Smart Building
- Asset Tracking & Telematics
- Smart Metering
- Intelligent Lighting
- Wireless Remote Control

Product Image



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Electrical Specification

Parameter	Specification	Unit
Operating Frequency Range	2400 – 2500	MHz
Return Loss	< -9.3	dB
VSWR	< 2.0	:1
Polarization	Linear	-
Peak Gain	1.1	dBi
Minimum Total Efficiency	-3.7 (43)	dB (%)
Average Total Efficiency	-3.1 (49)	dB (%)
Maximum Total Efficiency	-2.9 (52)	dB (%)
Impedance	50	Ω
Radiation Pattern	Omni-directional	-
Input Power	< 2	W

Note: All measurements were performed using the evaluation board in a free-space environment. Actual performance may vary depending on factors such as the ground plane, specific application, and surrounding environment.

Mechanical Specification

Parameter	Specification
Antenna Dimension	1.6 x 0.8 x 0.4 mm
Evaluation board Dimension	46 x 20 mm
Recommended Ground Clearance for Antenna	5.0 x 3.0 mm
Mounting Type	Surface Mount
Mounting Location	PCB Corner Mounting
Material(s)	Ceramic

Environmental Specification

Parameter	Specification
Operating and Storage Temperature (individual chip without packing)	-40°C ~ +85°C
Packaging Storage Temperature	-10°C ~ +40°C
Packaging Storage Relative Humidity	70% (Max.)

Ordering Information

Part Number	Description
AANI-CH-0133	Antenna Component on Cut Tape
AANI-CH-0133-T	Antenna Component on Tape & Reel
AANI-CH-0133-EVB	Evaluation Kit



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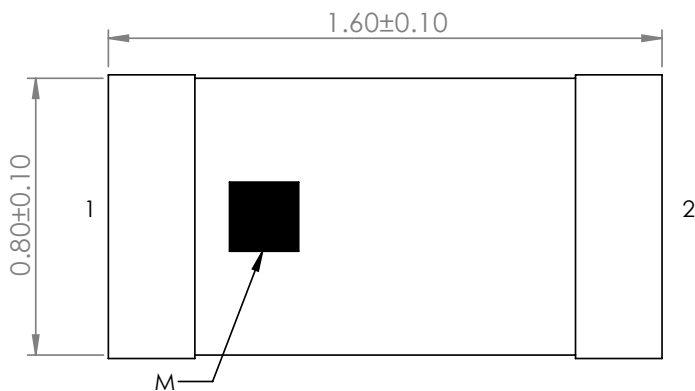


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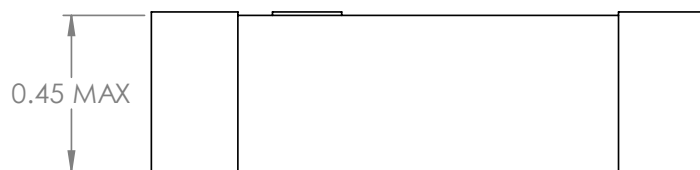
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Product Dimensions and Terminal Configuration

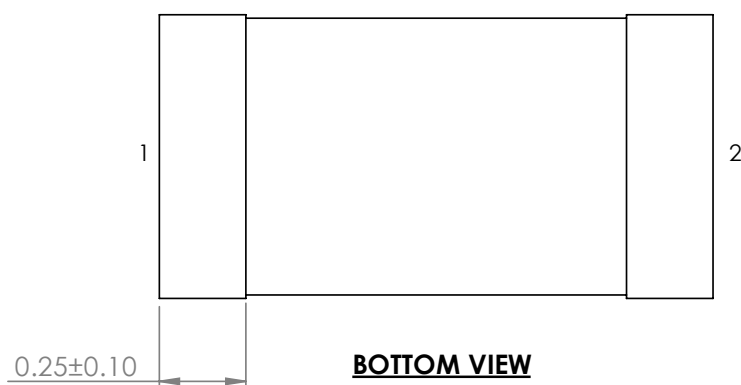


TOP VIEW

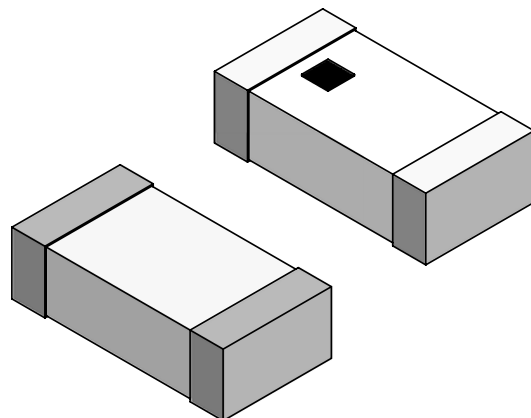
Pin #	Function
1	Feeding Point
2	NC
M	MARK



FRONT VIEW



BOTTOM VIEW



Unit: mm

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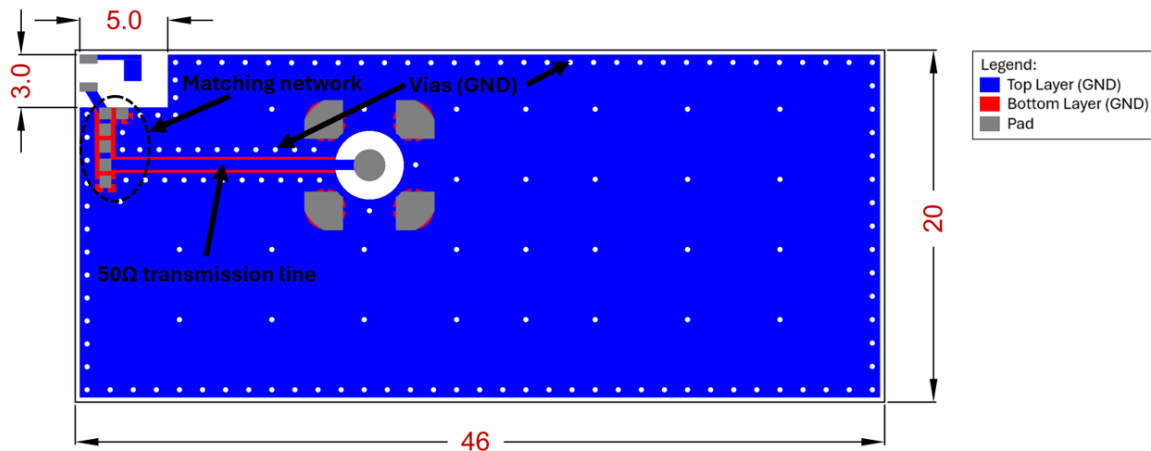
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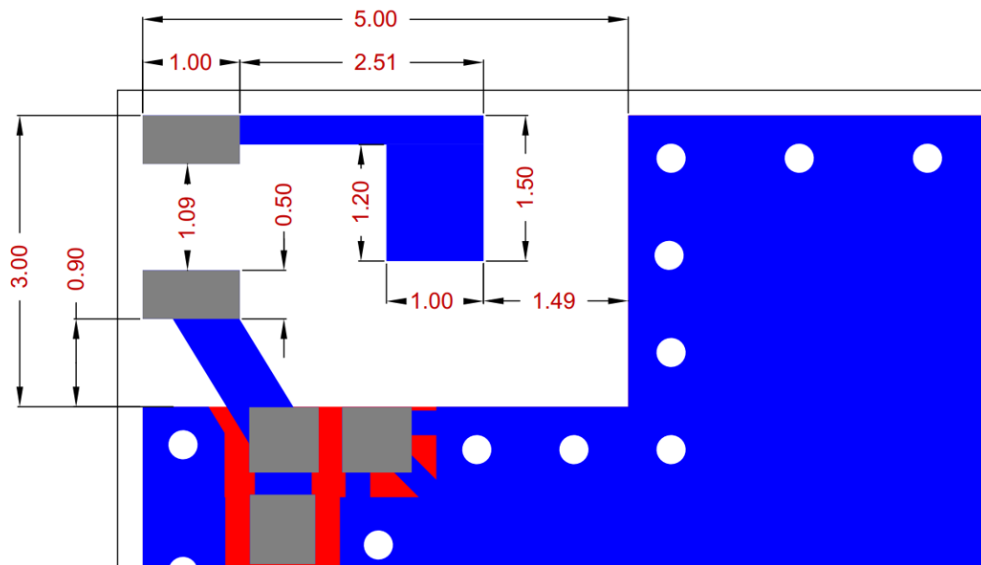
1.6 x 0.8 x 0.4 mm
RoHS/RoHS II Compliant
MSL Level = 1

Recommended PCB layout

If there are several layers in the PCB, there is an advantage to add vias for smooth interconnection of the ground areas to avoid splits in the ground plane. It is also important that the ground clearance is respected through **all layers** of the PCB. It is recommended to implement a matching network to optimize the antenna impedance in your application.



Detailed view of antenna footprint and copper clearance:



Unit: mm

Transmission Line

The transmission line should be kept as short as possible and be designed to have a characteristic impedance of 50Ω. Abracon recommends using a Co-Planar Waveguide with Ground (CPWG), which dimensions can be derived by any trusted calculator, using the correct input for PCB materials and layer stack-up.



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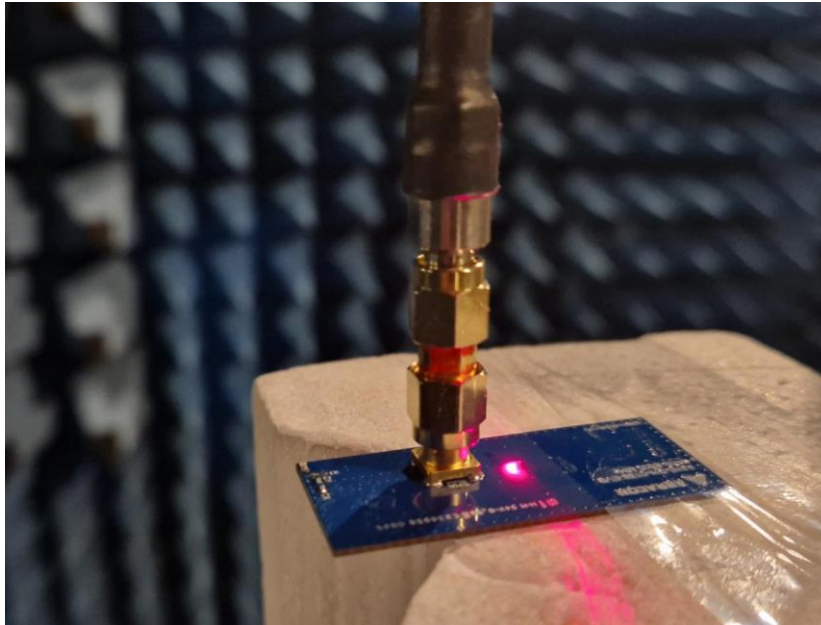
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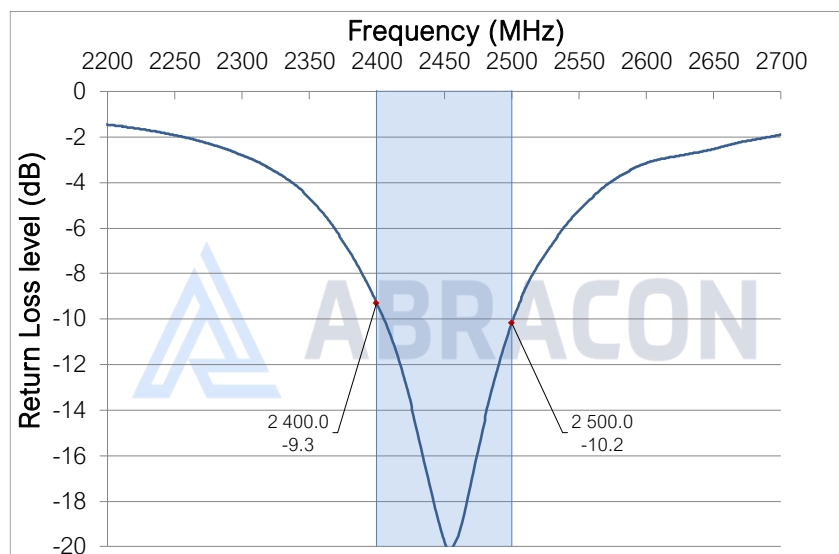
1.6 x 0.8 x 0.4 mm
RoHS/RoHS II Compliant
MSL Level = 1

Measurement Setup

The radiation measurements were all done in an anechoic chamber with the antenna implemented on its evaluation board (Abracon AANI-CH-0133-EVB) that has a PCB size of 46 x 20 mm:



Reflection Characteristics – Return Loss



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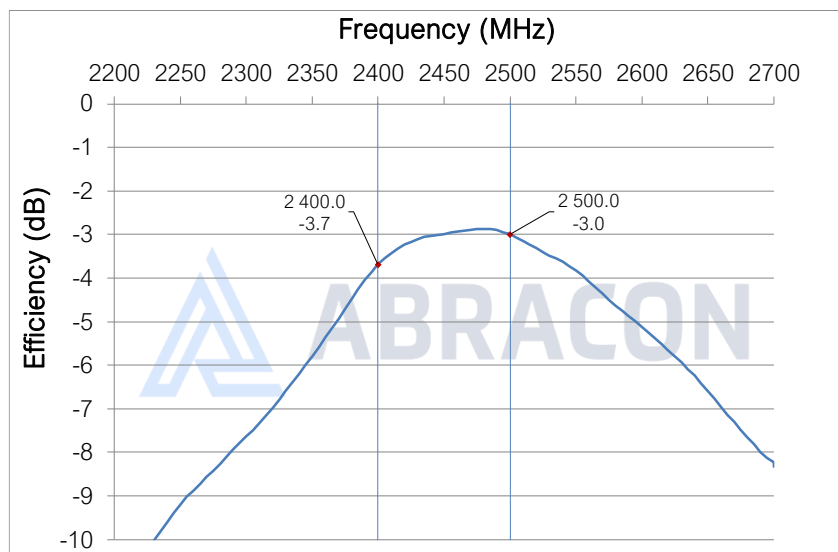


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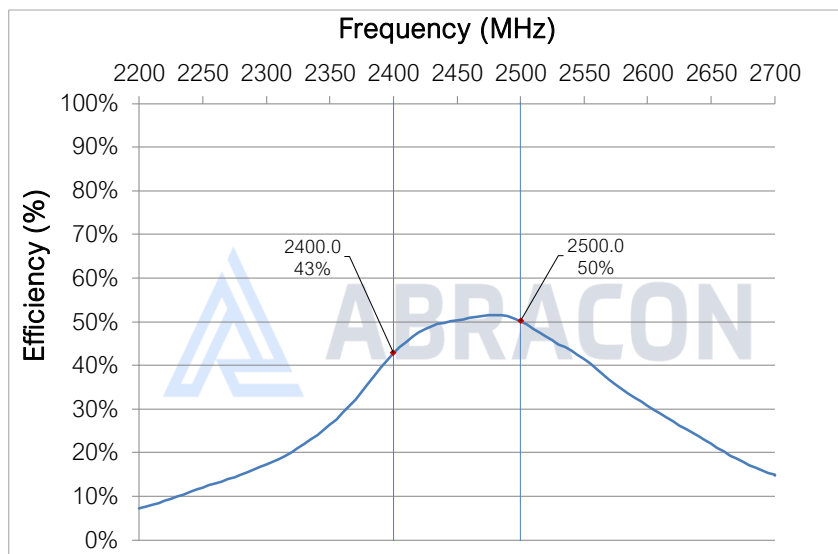


1.6 x 0.8 x 0.4 mm
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Radiation Characteristics – Total Efficiency (dB)



Radiation Characteristics – Total Efficiency (%)



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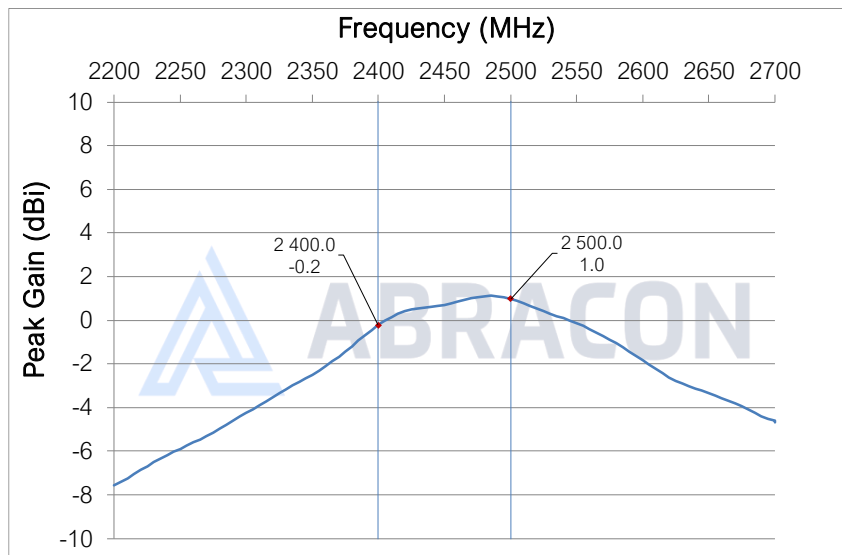


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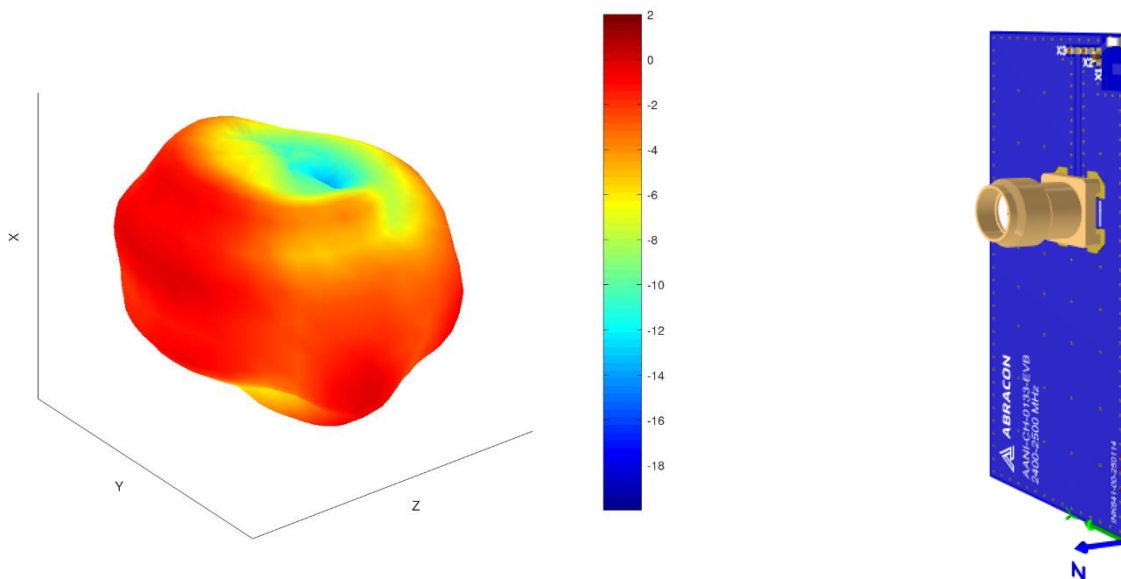


1.6 x 0.8 x 0.4 mm
RoHS/RoHS II Compliant
MSL Level = 1

Radiation Characteristics – Maximum Gain



Radiation Characteristics – 3D Pattern @ 2450 MHz



Unit: dBi



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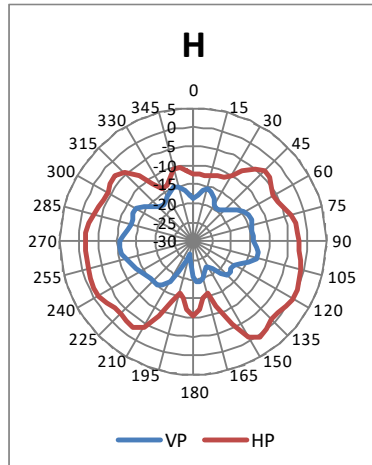
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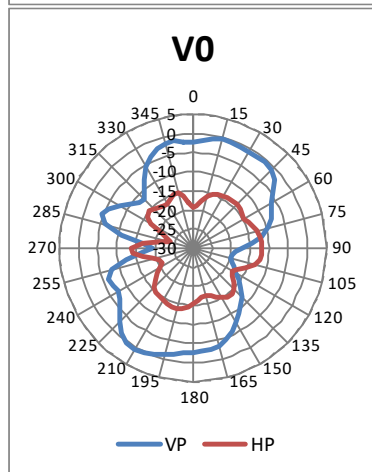
1.6 x 0.8 x 0.4 mm
RoHS/RoHS II Compliant
MSL Level = 1

Radiation Characteristics – 2D Pattern @ 2450 MHz

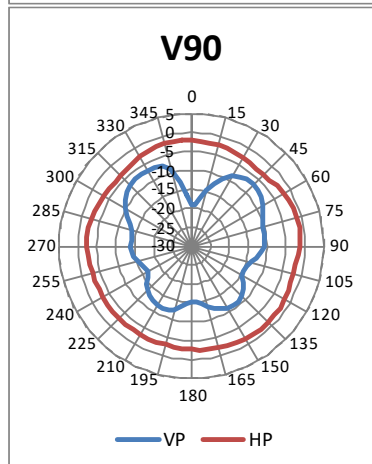
XY-plane:



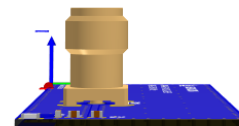
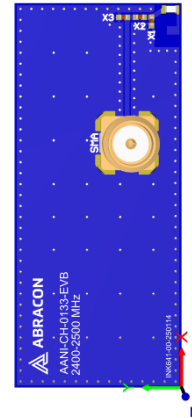
XZ-plane:



YZ-plane:



VP: Vertical Polarization
HP: Horizontal Polarization



Unit: dBi



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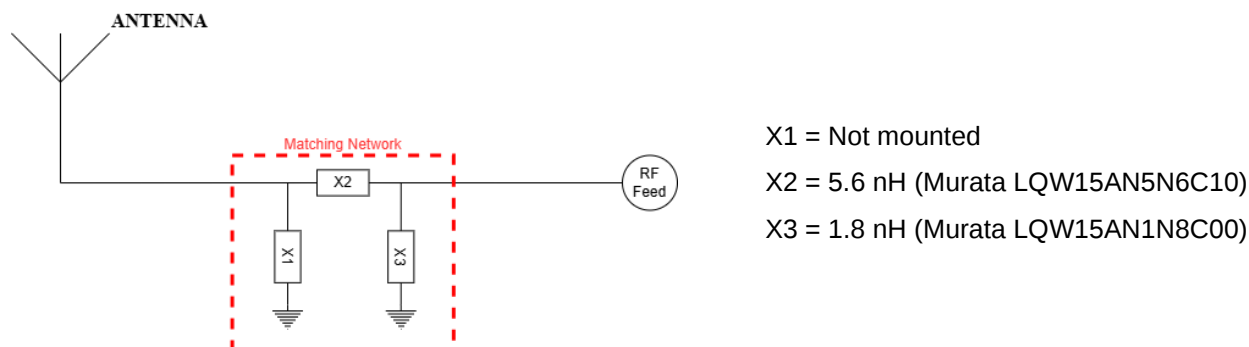
Evaluation Board Outline & Matching Circuit

The evaluation board (Abracon AANI-CH-0133-EVB) is developed to simplify antenna testing and evaluation. It has an arbitrary size of 46 x 20 mm and includes an SMA connector. The purpose is to give a reference design for an optimal antenna implementation. The evaluation board can also be used to test other implementations by cutting and soldering the PCB into any device.



The evaluation board has a matching circuit implemented next to the antenna. This is aimed to enable optimization possibilities for the user. The component positions are sized for 0402 (1005 metric) SMD components.

The antenna requires a matching circuit to fine-tune the resonant frequency and achieve optimal balance. The evaluation board is pre-tuned for optimal performance in the 2.4–2.5 GHz range using the components listed below (equivalents may be used):



However, it is common that the resonant frequency will shift during implementation in an arbitrary device. Therefore, this matching may be changed with other values/components/brands for compensation of such effects. This is further described in the General Implementation Guidelines section below.



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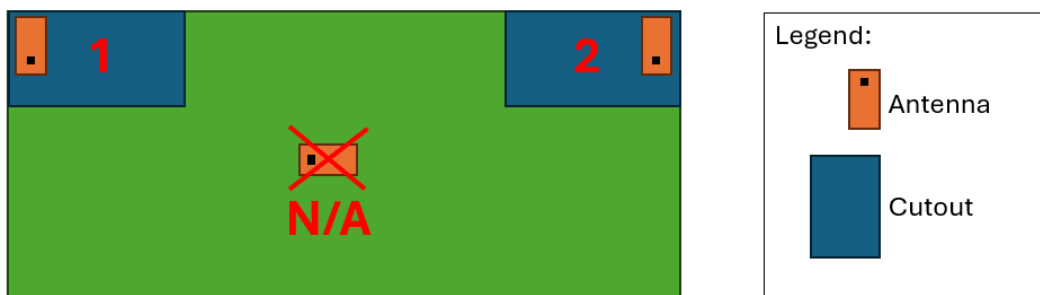


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General Implementation Guidelines

The antenna can be positioned in different ways, although there are some positions which are more beneficial. The illustration below shows a typical PCB with examples on two different antenna positions:

- The optimal position is usually option 1 or 2
- The antenna must be placed along the PCB edge, i.e., it cannot be placed in the middle (see “N/A”).



The rectangular copper cutout in the footprint must extend through all layers of the PCB stack-up, ensuring there is **no copper on any layer in this area**. Additionally, a robust via structure around the cutout and along the edge of the ground plane is highly recommended for optimal performance.

It is important to note that plastic and metal parts in close proximity to antennas may significantly affect antenna tuning and performance. For instance, a plastic housing above the antenna often causes the resonant frequency to shift downward. Since such effects are challenging to predict without detailed design information, it is recommended to measure the antenna performance in the final device after implementation. To compensate for potential frequency shifts, implementing a matching network on the antenna feed is advisable.

Another general consideration for surface-mounted antennas relates to PCB population. Electrical components placed near the antenna may impact its tuning and radiation performance. To mitigate this, components in the surrounding area should be positioned below a topographical slope. This slope should begin at the PCB level near the antenna's designated keep-out zone and gradually increase in height as distance from the antenna grows.

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Reflow Profile [JEDEC J-STD-020]

Solder paste: Sn/3.0Ag/0.5Cu

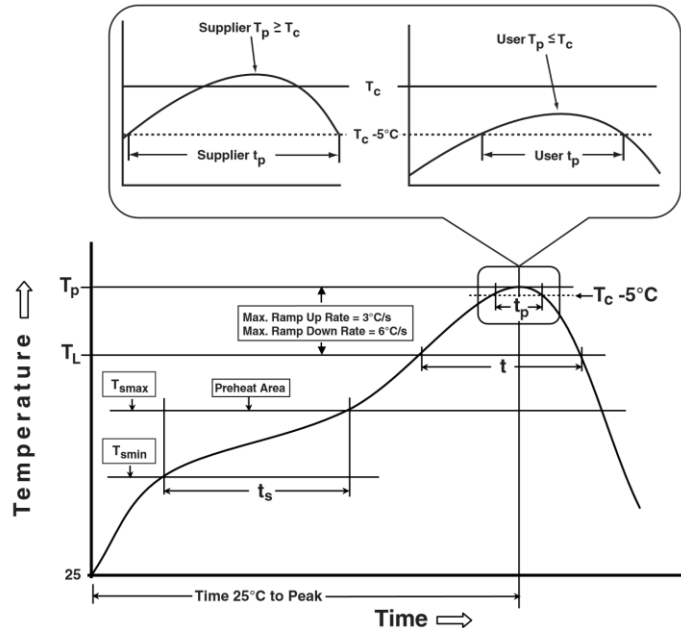


Table 1

SnPb Eutectic Process
Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ ≥ 350
<2.5mm	235°C	220°C
≥ 2.5mm	220°C	220°C

Table 2

Pb-Free Process
Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
<1.6mm	260°C	260°C	260°C
1.6mm - 2.5mm	260°C	250°C	245°C
>2.5mm	250°C	245°C	245°C

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat / soak		
Temperature minimum (T _{smmin})	100°C	150°C
Temperature maximum (T _{smmax})	150°C	200°C
Time (T _{smmin} to T _{smmax}) (t _s)	60 – 120 sec.	60 – 90 sec.
Average ramp-up rate (T _{smmax} to T _p)	3°C/sec. max	3°C/sec. max
Liquidous temperature (T _L)	183°C	217°C
Time at Liquidous (T _L)	60 – 150 sec.	60 – 150 sec.
Peak package body temperature (T _p)*	See Table 1	See Table 2
Time (T _p)* within 5°C of the specified classification temperature (T _c)	20 sec.	10 sec.
Ramp-down rate (T _p to T _{smmax})	6°C/sec. max	6°C/sec. max
Time 25°C to peak temperature	6 min. max	8 min. max
Reflow cycles	2 max	2 max

*Tolerance for peak profile temperature (T_p) is defined as a supplier minimum and a user maximum.

**Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.

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