

V_{DRM} = 5200 V
 $I_{T(AV)M}$ = 3720 A
 $I_{T(RMS)}$ = 5840 A
 I_{TSM} = $63.0 \cdot 10^3$ A
 V_{TO} = 0.96 V
 r_T = 0.194 mΩ

Phase Control Thyristor

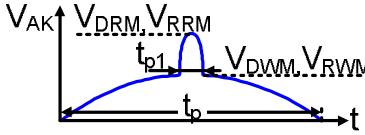
5STP 34Q5200

Doc. No. 5SYA1052-07 Apr. 20

- Patented free-floating silicon technology
- Low on-state and switching losses
- Designed for traction, energy and industrial applications
- Optimum power handling capability
- Interdigitated amplifying gate

Blocking

Maximum rated values¹⁾

Parameter	Symbol	Conditions	5STP 34Q5200		Unit
Max. surge peak forward and reverse blocking voltage	V_{DSM} , V_{RSM}	$t_p = 10$ ms, $f = 5$ Hz $T_{vj} = 5 \dots 125$ °C, Note 1	5200		V
Max repetitive peak forward and reverse blocking voltage	V_{DRM} , V_{RRM}	$f = 50$ Hz, $t_p = 10$ ms, $t_{p1} = 250$ µs, $T_{vj} = 5 \dots 125$ °C, Note 1, Note 2	5200		V
Max crest working forward and reverse voltages	V_{DWM} , V_{RWM}		3470		V
Critical rate of rise of commutating voltage	dv/dt_{crit}	Exp. to $0.67 \cdot V_{DRM}$, $T_{vj} = 125$ °C	2000		V/µs

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
Forward leakage current	I_{DRM}	V_{DRM} , $T_{vj} = 125$ °C			500	mA
Reverse leakage current	I_{RRM}	V_{RRM} , $T_{vj} = 125$ °C			500	mA

Note 1: Voltage de-rating factor of 0.11% per °C is applicable for T_{vj} below +5 °C.

Note 2: Recommended minimum ratio of V_{DRM} / V_{DWM} or $V_{RRM} / V_{RWM} = 2$. See App. Note 5SYA 2051.

Mechanical data

Maximum rated values¹⁾

Parameter	Symbol	Conditions	min	typ	max	Unit
Mounting force	F_M		81	90	108	kN
Acceleration	a	Device unclamped			50	m/s ²
Acceleration	a	Device clamped			100	m/s ²

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
Weight	m				2.1	kg
Housing thickness	H	$F_M = 90$ kN, $T_a = 25$ °C	26.25		26.70	mm
Surface creepage distance	D _s		36			mm
Air strike distance	D _a		15			mm

1) Maximum rated values indicate limits beyond which damage to the device may occur

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On-state**Maximum rated values¹⁾**

Parameter	Symbol	Conditions	min	typ	max	Unit
Average on-state current	I _{T(AV)M}	Half sine wave, T _c = 70 °C			3720	A
RMS on-state current	I _{T(RMS)}				5840	A
Peak non-repetitive surge current	I _{TSM}	t _p = 10 ms, T _{vj} = 125 °C, sine half wave,			63.0·10 ³	A
Limiting load integral	I ² t	V _D = V _R = 0 V, after surge			19.8·10 ⁶	A ² s
Peak non-repetitive surge current	I _{TSM}	t _p = 10 ms, T _{vj} = 125 °C, sine half wave,			45.5·10 ³	A
Limiting load integral	I ² t	V _R = 0.6·V _{RRM} , after surge			10.3·10 ⁶	A ² s

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
On-state voltage	V _T	I _T = 3000 A, T _{vj} = 125 °C		1.47	1.54	V
Threshold voltage	V _(TO)				0.96	V
Slope resistance	r _T	I _T = 2300 A - 7000 A, T _{vj} = 125 °C			0.194	mΩ
Holding current	I _H	T _{vj} = 25 °C			125	mA
		T _{vj} = 125 °C			75	mA
Latching current	I _L	T _{vj} = 25 °C			500	mA
		T _{vj} = 125 °C			250	mA

Switching**Maximum rated values¹⁾**

Parameter	Symbol	Conditions	min	typ	max	Unit
Critical rate of rise of on-state current	di/dt _{crit}	T _{vj} = 125 °C, I _T = 3000 A, V _D ≤ 0.67·V _{DRM} , I _{GM} = 2 A, t _r = 0.5 μs	Cont. f = 50 Hz			250 A/μs
			Cont. f = 1 Hz			1000 A/μs
Circuit-commutated turn-off time	t _q	T _{vj} = 125 °C, I _T = 2000 A, V _R = 200 V, di _T /dt = -1.5 A/μs, V _D ≤ 0.67·V _{DRM} , dv _D /dt = 20 V/μs			700	μs

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
Reverse recovery charge	Q _{rr}	T _{vj} = 125 °C, I _T = 2000 A,	3400	4770	5200	μAs
Reverse recovery current	I _{RM}	V _R = 200 V, di _T /dt = -1.5 A/μs	60	77	95	A
Gate turn-on delay time	t _{gd}	T _{vj} = 25 °C, V _D = 0.4·V _{RM} , I _{GM} = 2 A, t _r = 0.5 μs			3	μs

Triggering

Maximum rated values¹⁾

Parameter	Symbol	Conditions	min	typ	max	Unit
Peak forward gate voltage	V _{FGM}				12	V
Peak forward gate current	I _{FGM}				10	A
Peak reverse gate voltage	V _{RGM}				10	V
Average gate power loss	P _{G(AV)}				see Fig. 7	W

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
Gate-trigger voltage	V _{GT}	T _{vj} = 25 °C			2.6	V
Gate-trigger current	I _{GT}	T _{vj} = 25 °C			400	mA
Gate non-trigger voltage	V _{GD}	V _D = 0.4 · V _{DRM} , T _{vjmax} = 125 °C			0.3	V
Gate non-trigger current	I _{GD}	V _D = 0.4 · V _{DRM} , T _{vjmax} = 125 °C			10	mA

Thermal

Maximum rated values¹⁾

Parameter	Symbol	Conditions	min	typ	max	Unit
Operating junction temperature range	T _{vj}				125	°C
Storage temperature range	T _{stg}		-40		140	°C

Characteristic values

Parameter	Symbol	Conditions	min	typ	max	Unit
Thermal resistance junction to case	R _{th(j-c)}	Double-side cooled F _m = 81... 108 kN			5	K/kW
	R _{th(j-c)A}	Anode-side cooled F _m = 81... 108 kN			10	K/kW
	R _{th(j-c)C}	Cathode-side cooled F _m = 81... 108 kN			10	K/kW
Thermal resistance case to heatsink	R _{th(c-h)}	Double-side cooled F _m = 81... 108 kN			1	K/kW
	R _{th(c-h)}	Single-side cooled F _m = 81... 108 kN			2	K/kW

Analytical function for transient thermal impedance:

$$Z_{th(j-c)}(t) = \sum_{i=1}^n R_i (1 - e^{-t/\tau_i})$$

i	1	2	3	4
R _i (K/kW)	3.563	0.905	0.514	0.018
τ _i (s)	0.4587	0.0526	0.0055	0.0007

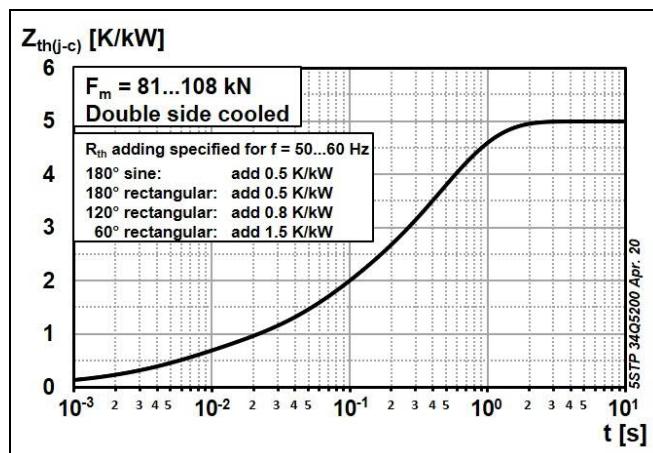


Fig. 1 Transient thermal impedance (junction-to-case) vs. time

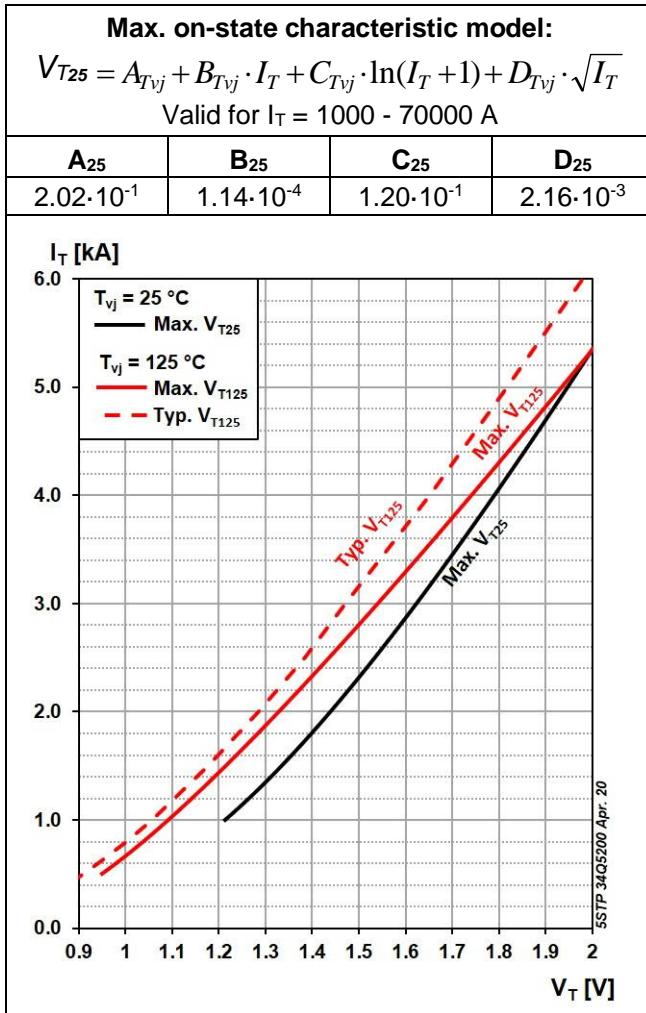


Fig. 2 On-state voltage characteristics

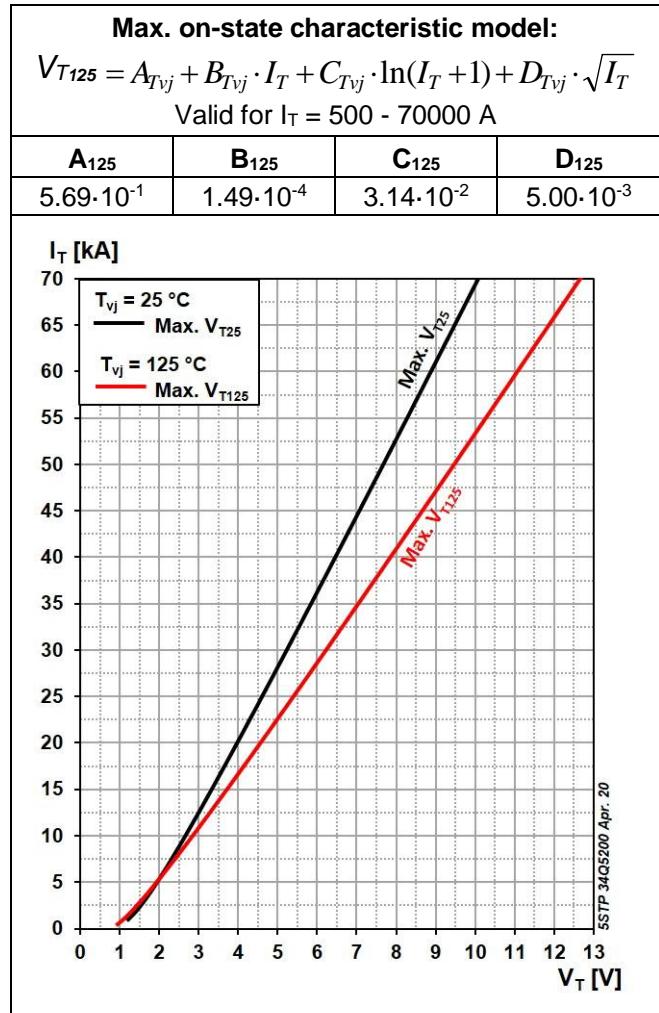


Fig. 3 On-state voltage characteristics

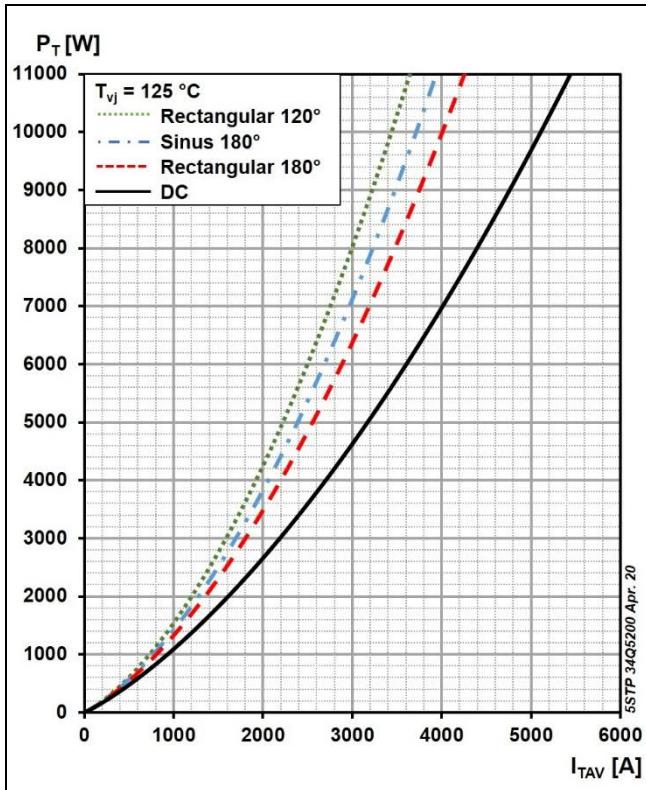


Fig. 4 On-state power dissipation vs. mean on-state current, turn-on losses excluded

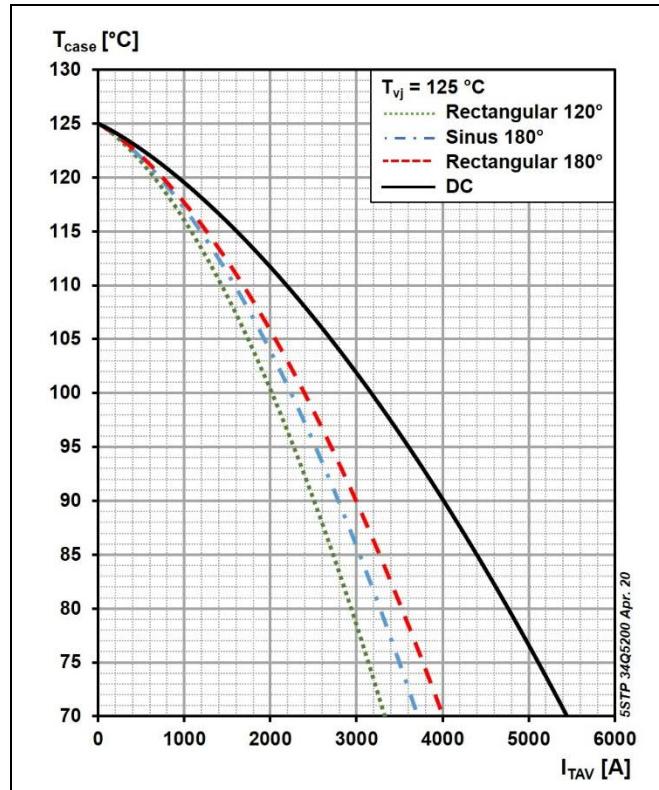
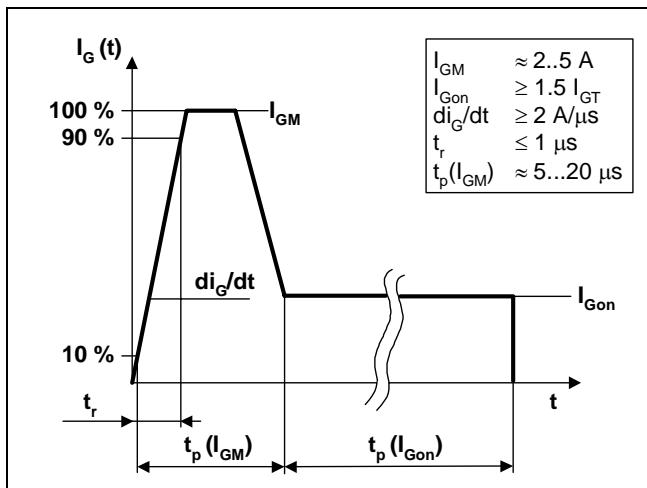
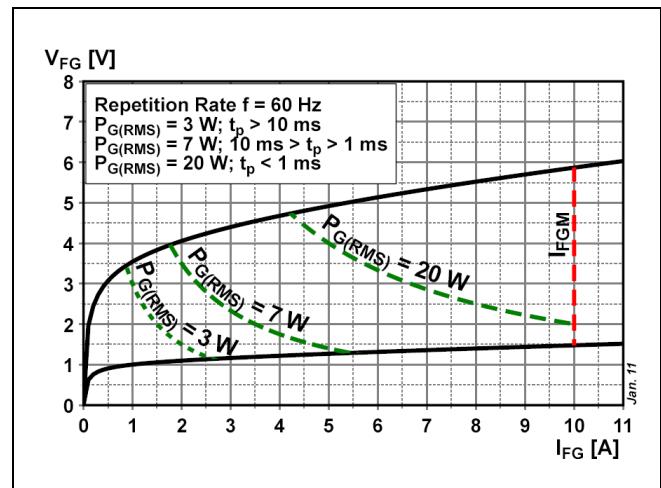
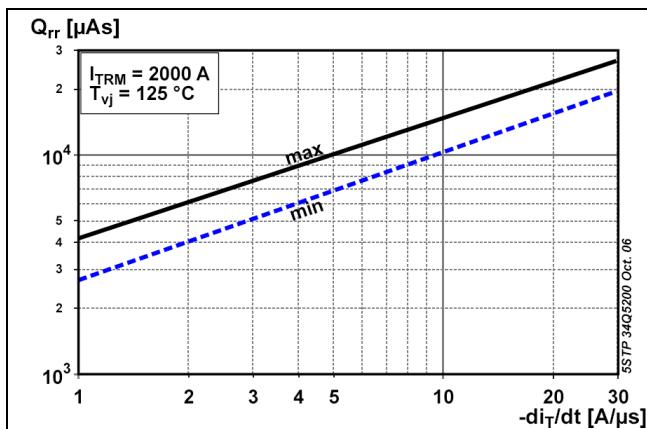
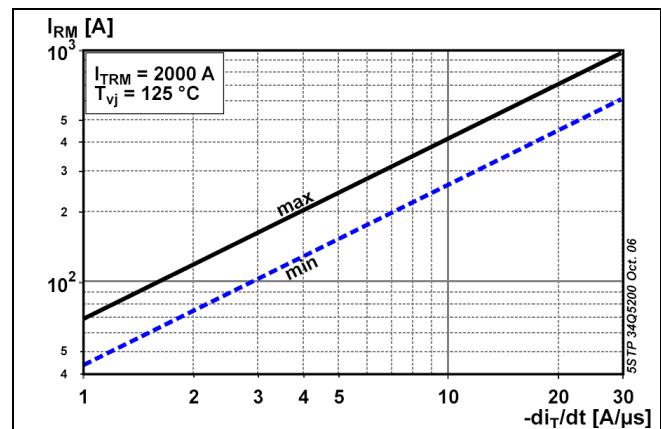


Fig. 5 Max. permissible case temperature vs. mean on-state current, switching losses ignored

**Fig. 6** Recommended gate current waveform**Fig. 7** Max. peak gate power loss**Fig. 8** Reverse recovery charge vs. decay rate of on-state current**Fig. 9** Peak reverse recovery current vs. decay rate of on-state current

Turn-on and Turn-off losses

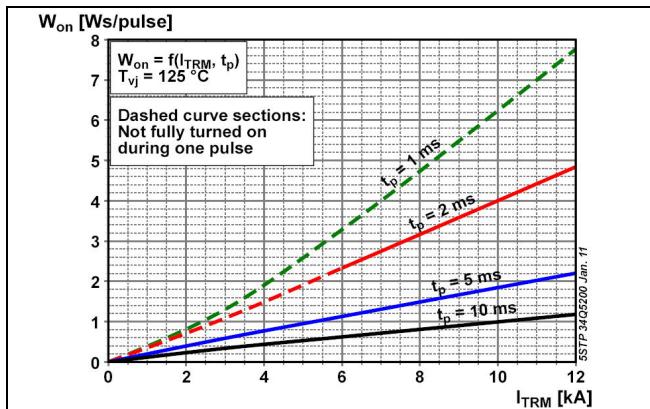


Fig. 10 Turn-on energy, half sinusoidal waves

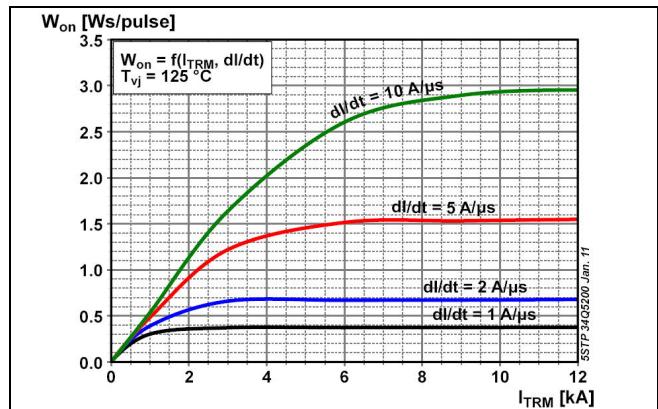


Fig. 11 Turn-on energy, rectangular waves

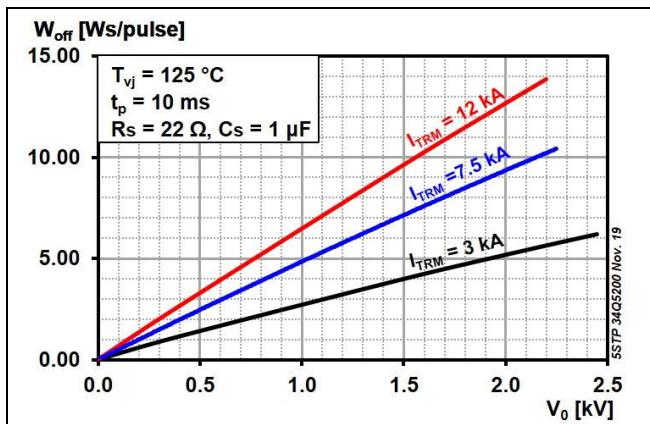


Fig. 12 Typical turn-off energy, half sinusoidal waves

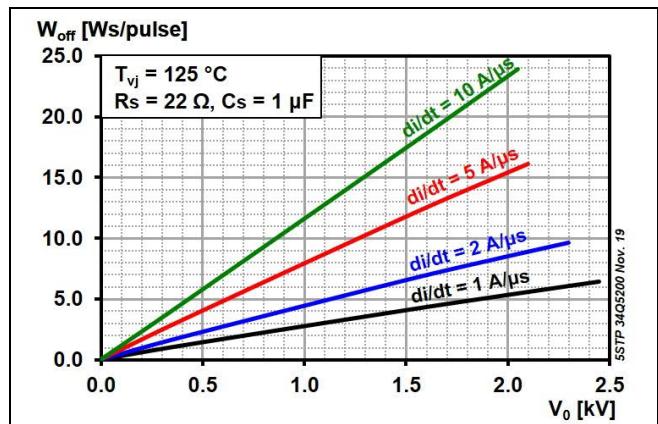


Fig. 13 Typical turn-off energy, rectangular waves

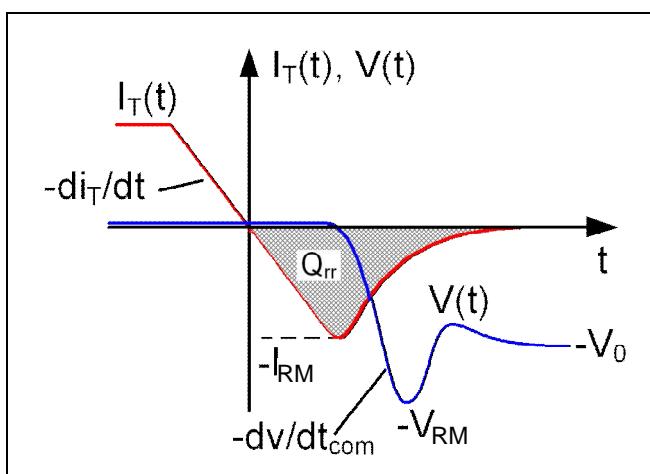


Fig. 14 Current and voltage waveforms at turn-off

Total power loss for repetitive waveforms:

$$P_{TOT} = P_T + W_{on} \cdot f + W_{off} \cdot f$$

where

$$P_T = \frac{1}{T} \int_0^T I_T \cdot V_T(I_T) dt$$

Fig. 15 Relationships for power loss

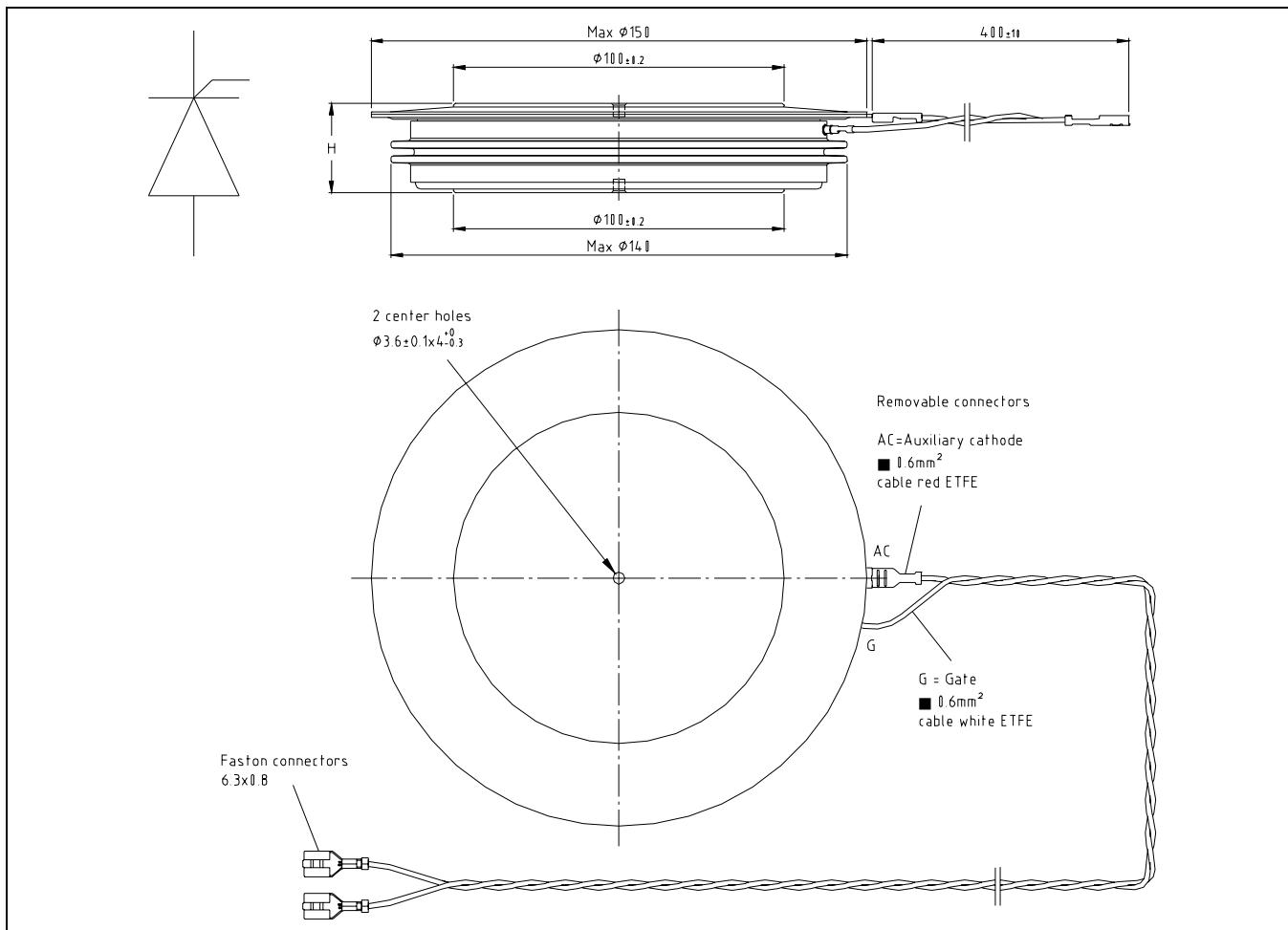


Fig. 16 Device Outline Drawing

Related documents:

-
- | | |
|-----------|---|
| 5SYA 2020 | Design of RC-Snubbers for Phase Control Applications |
| 5SYA 2049 | Voltage definitions for phase control and bi-directionally controlled thyristors |
| 5SYA 2051 | Voltage ratings of high power semiconductors |
| 5SYA 2034 | Gate-drive recommendations for phase control and bi-directionally controlled thyristors |
| 5SYA 2036 | Recommendations regarding mechanical clamping of Press-Pack High Power Semiconductors |
| 5SYA 2102 | Surge currents for Phase Control Thyristors |
| 5SZK 9118 | General Environmental Conditions for High Power Semiconductors |

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