



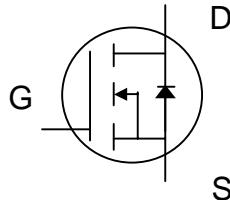
## N-channel Enhancement-mode Power MOSFET

**Simple Drive Requirement**

**Good Thermal Performance**

**Low On-resistance**

**RoHS-compliant, halogen-free**

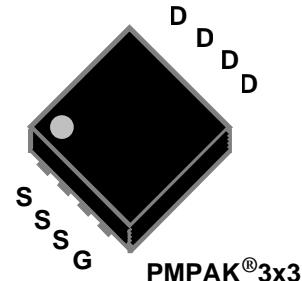


$BV_{DSS}$	30V
$R_{DS(ON)}$	9mΩ
$I_D$	15.5A

## Description

Advanced Power MOSFETs from APEC provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The PMPAK®3x3 package is specially designed for DC-DC converter applications, with a small foot print that offers a backside heat sink and a low 1.0mm package profile.



## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$ at $T_A=25^\circ\text{C}$	Continuous Drain Current <sup>3</sup>	15.5	A
$I_D$ at $T_A=70^\circ\text{C}$	Continuous Drain Current <sup>3</sup>	12.4	A
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	60	A
$P_D$ at $T_A=25^\circ\text{C}$	Total Power Dissipation	3.57	W
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

## Thermal Data

Symbol	Parameter	Value	Units
$R_{thj-c}$	Maximum Thermal Resistance, Junction-case <sup>3</sup>	5	°C/W
$R_{thj-a}$	Maximum Thermal Resistance, Junction-ambient <sup>3</sup>	35	°C/W

## Ordering Information

**AP4034GYT-HF-3TR**

**RoHS-compliant halogen-free PMPAK®3x3, shipped on tape and reel (3000pcs/reel)**

PMPAK® is a registered trademark of Advanced Power Electronics Corp.



**Electrical Specifications at  $T_j=25^\circ\text{C}$  (unless otherwise specified)**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_{\text{D}}=250\mu\text{A}$	30	-	-	V
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{\text{GS}}=10\text{V}$ , $I_{\text{D}}=12\text{A}$	-	6.6	9.0	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$ , $I_{\text{D}}=8\text{A}$	-	10	13.5	$\text{m}\Omega$
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$ , $I_{\text{D}}=250\mu\text{A}$	1.0	1.4	3	V
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=10\text{V}$ , $I_{\text{D}}=12\text{A}$	-	24	-	S
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=24\text{V}$ , $V_{\text{GS}}=0\text{V}$	-	-	10	$\text{uA}$
$I_{\text{GSS}}$	Gate-Source Leakage	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$	-	-	$\pm 100$	nA
$Q_g$	Total Gate Charge	$I_{\text{D}}=12\text{A}$	-	15	24	nC
$Q_{\text{gs}}$	Gate-Source Charge	$V_{\text{DS}}=15\text{V}$	-	4	-	nC
$Q_{\text{gd}}$	Gate-Drain ("Miller") Charge		-	7	-	nC
$t_{\text{d}(\text{on})}$	Turn-on Delay Time	$V_{\text{DS}}=15\text{V}$	-	10	-	ns
$t_r$	Rise Time		-	5	-	ns
$t_{\text{d}(\text{off})}$	Turn-off Delay Time	$R_G=3.3\Omega$	-	27	-	ns
$t_f$	Fall Time		-	7	-	ns
$C_{\text{iss}}$	Input Capacitance	$V_{\text{GS}}=0\text{V}$	-	1700	2720	pF
$C_{\text{oss}}$	Output Capacitance		-	185	-	pF
$C_{\text{rss}}$	Reverse Transfer Capacitance	$f=1.0\text{MHz}$	-	160	-	pF
$R_g$	Gate Resistance	$f=1.0\text{MHz}$	-	1.1	2.2	$\Omega$

## Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$V_{\text{SD}}$	Forward On Voltage <sup>2</sup>	$I_{\text{S}}=2.9\text{A}$ , $V_{\text{GS}}=0\text{V}$	-	-	1.2	V
$t_{\text{rr}}$	Reverse Recovery Time	$I_{\text{S}}=12\text{A}$ , $V_{\text{GS}}=0\text{V}$ , $dI/dt=100\text{A}/\mu\text{s}$	-	21	-	ns
$Q_{\text{rr}}$	Reverse Recovery Charge		-	12	-	nC

### Notes:

1. Pulse width limited by maximum junction temperature
2. Pulse test
3. Surface mounted on 1 in<sup>2</sup> 2oz copper pad of FR4 board,  $\tau \leq 10\text{sec}$ ,  $160^\circ\text{C}/\text{W}$  when mounted on min. copper pad.

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

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## Typical Electrical Characteristics

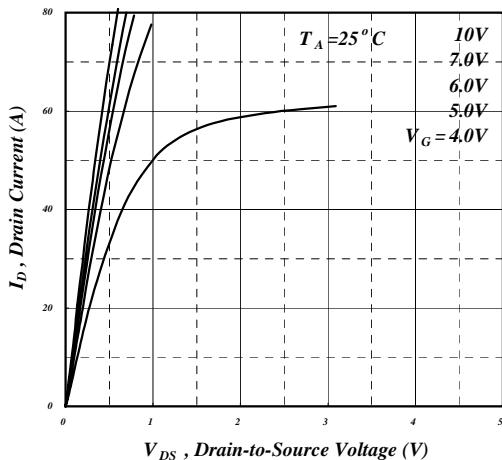


Fig 1. Typical Output Characteristics

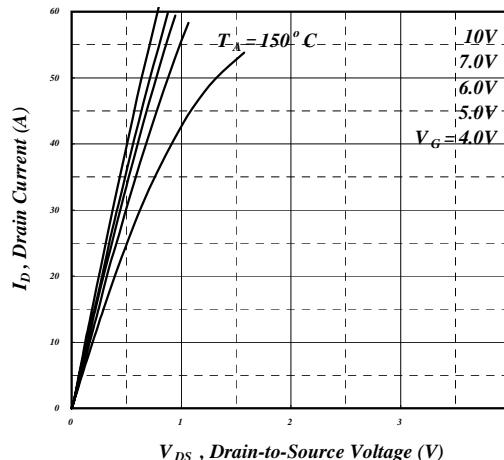


Fig 2. Typical Output Characteristics

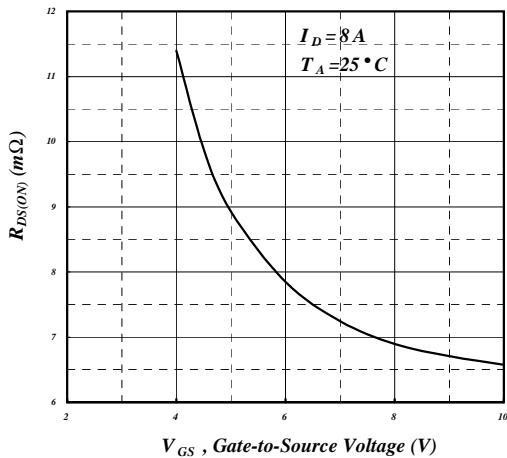


Fig 3. On-Resistance v.s. Gate Voltage

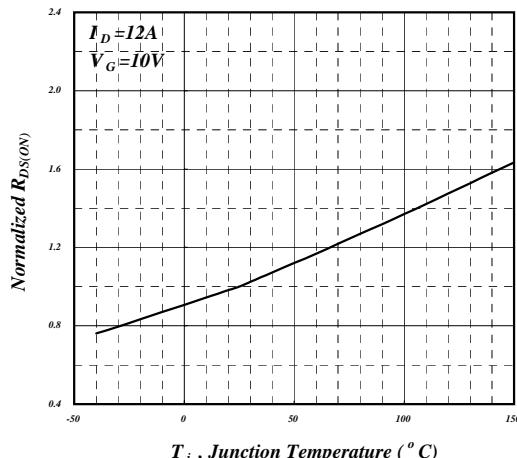


Fig 4. Normalized On-Resistance  
vs. Junction Temperature

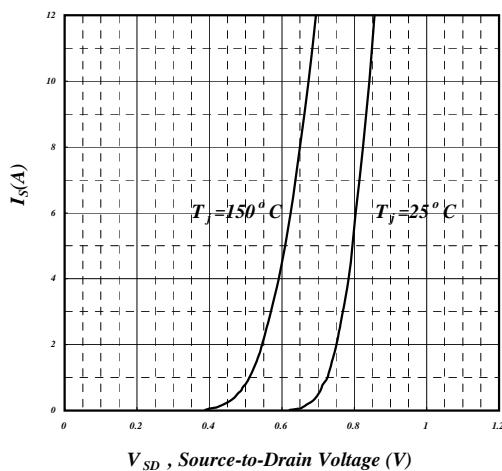


Fig 5. Forward Characteristic of  
Reverse Diode

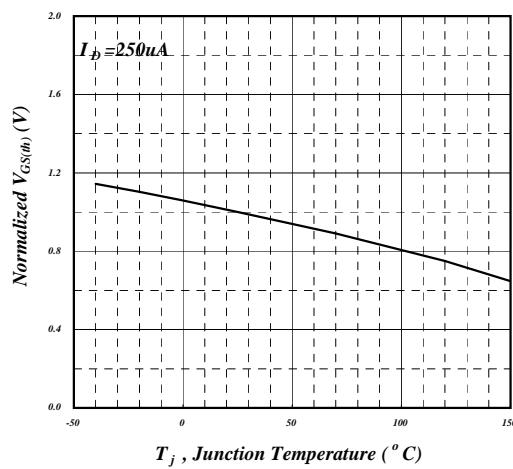


Fig 6. Gate Threshold Voltage v.s.  
Junction Temperature



## Typical Electrical Characteristics (cont.)

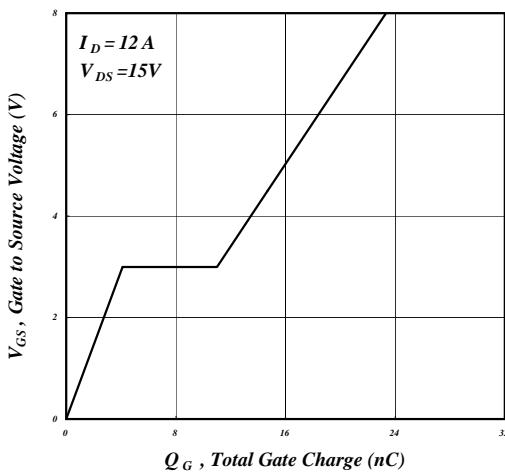


Fig 7. Gate Charge Characteristics

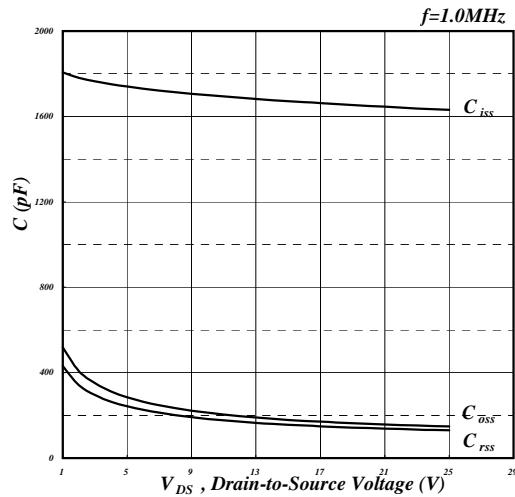


Fig 8. Typical Capacitance Characteristics

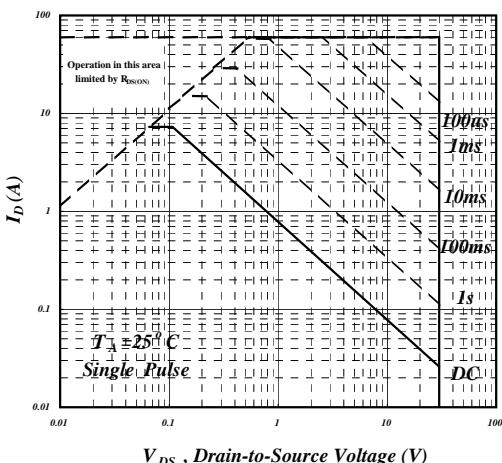


Fig 9. Maximum Safe Operating Area

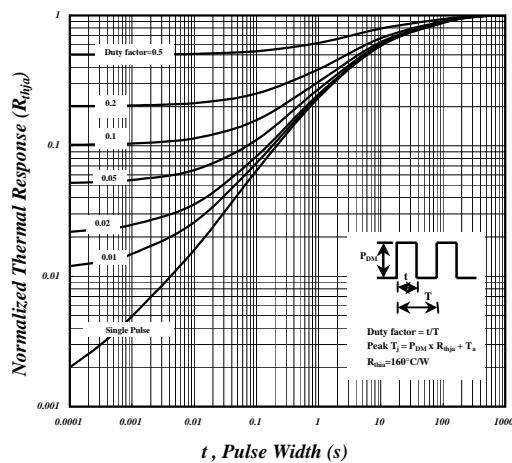


Fig 10. Effective Transient Thermal Impedance

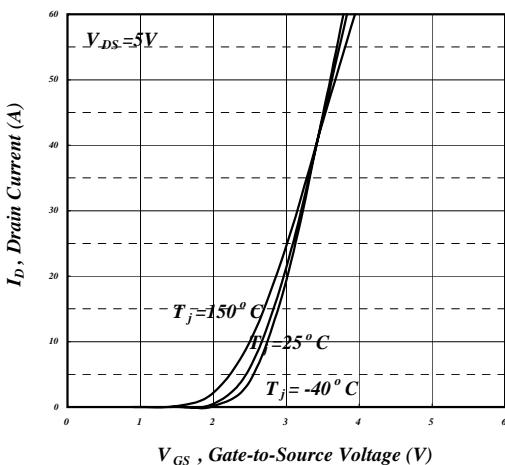


Fig 11. Transfer Characteristics

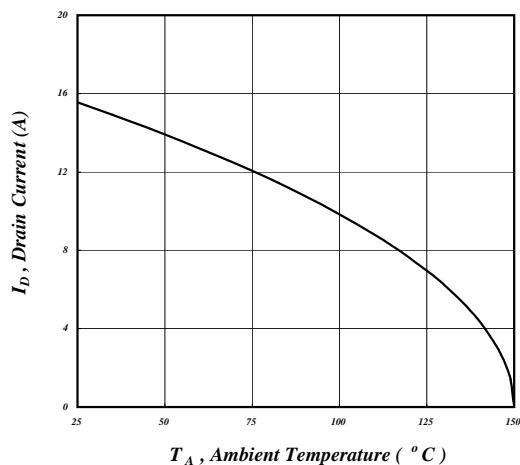
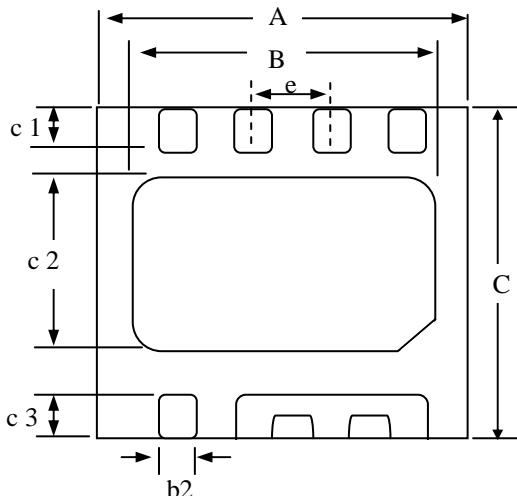


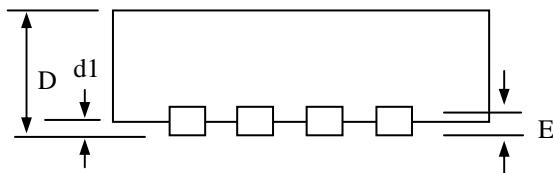
Fig 12. Maximum Continuous Drain Current vs. Ambient Temperature



## Package Dimensions: PMPAK®3x3

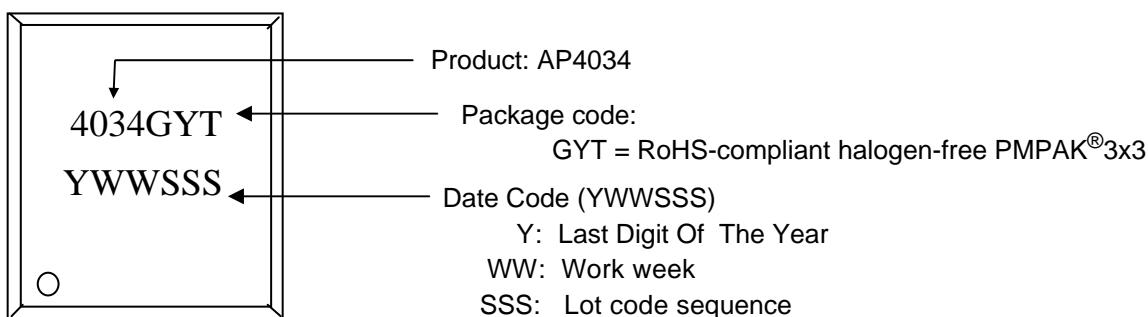


SYMBOLS	Millimeters		
	MIN	NOM	MAX
A	2.95	3.00	3.05
B	2.35	2.40	2.45
e	0.65 (ref.)		
b2	0.30	0.35	0.40
C	2.95	3.00	3.05
c1	0.37	0.42	0.47
c2	1.65	1.70	1.75
c3	0.37	0.42	0.47
D	0.80	0.85	0.95
d1	0.00	-	0.05
E	0.178	0.203	0.228



1. All dimensions are in millimeters.
2. Dimensions do not include mold protrusions.

## Marking Information:



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