

P-Channel Enhancement Mode Power MOSFET

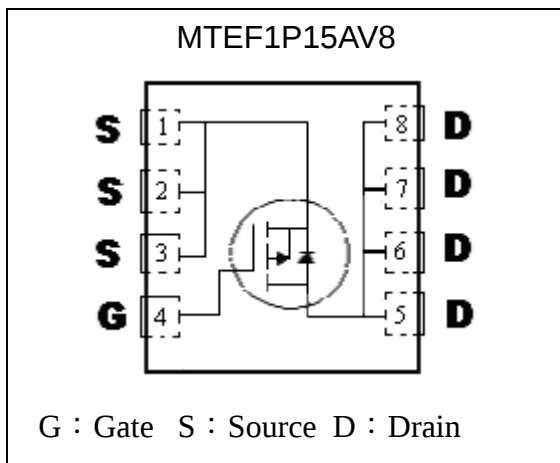
MTEF1P15AV8

BV_{DSS}	-150V
I_D	-5.4A @ $V_{GS}=-10V$, $T_C=25^{\circ}C$
$R_{DS(on)(Typ)}$	0.52Ω @ $V_{GS}=-10V$, $I_D=-1.4A$
	0.56Ω @ $V_{GS}=-6V$, $I_D=-1A$

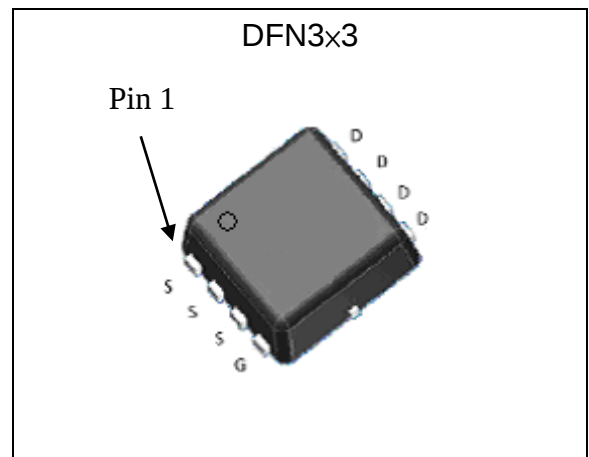
Features

- Simple drive requirement
- Low on-resistance
- Fast switching speed
- Pb-free lead plating and halogen-free package

Equivalent Circuit

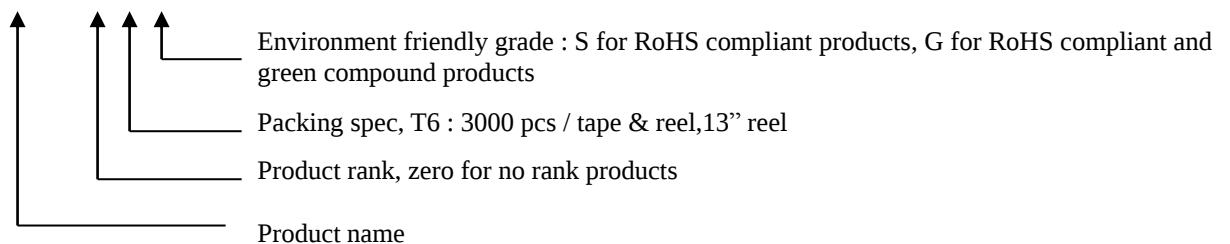


Outline



Ordering Information

Device	Package	Shipping
MTEF1P15AV8-0-T6-G	DFN3x3 (Pb-free lead plating and halogen-free package)	3000 pcs / tape & reel



**Absolute Maximum Ratings** (T_C=25°C, unless otherwise noted)

Parameter		Symbol	Limits	Unit
Drain-Source Voltage		V _{DS}	-150	V
Gate-Source Voltage		V _{GS}	±30	
Continuous Drain Current @ T _C =25°C, V _{GS} =-10V		I _D	-5.4	A
Continuous Drain Current @ T _C =100°C, V _{GS} =-10V			-3.4	
Continuous Drain Current @ T _A =25°C, V _{GS} =-10V *3			-1.3	
Continuous Drain Current @ T _A =70°C, V _{GS} =-10V *3			-1.0	
Pulsed Drain Current *1, 2		I _{DM}	-16	
Continuous Source-Drain Diode Current	T _C =25°C	I _S	-5.4	
Avalanche Current		I _{AS}	-10	
Avalanche Energy @ L=1mH, I _D =-10A, V _{GS} =-10V *4		E _{AS}	50	mJ
Maximum Power Dissipation	T _C =25°C	P _D	42	W
	T _C =70°C		27	
	T _A =25°C *3		2.1	
	T _A =70°C *3		1.3	
Operating Junction and Storage Temperature Range		T _j , T _{stg}	-55~+150	°C

Thermal Data

Parameter	Symbol	Typ	Maximum	Unit
Thermal Resistance, Junction-to-ambient *3	R _{th,j-a}	50	60	°C/W
Thermal Resistance, Junction-to-case	R _{th,j-c}	2.5	3	

- Note : 1. Pulse width limited by maximum junction temperature.
2. Duty cycle ≤ 1%.
3. Surface mounted on 1 in² copper pad of FR-4 board; 135°C/W when mounted on minimum pad of 2 oz. copper.
4. 100% tested by conditions of L=1mH, V_{GS}=-10V, I_{AS}=-7A, V_{DD}=-25V

Characteristics (T_C=25°C, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	-150	-	-	V	V _{GS} =0V, I _D =-250μA
ΔBV _{DSS} /ΔT _J	-	-106	-	mV/°C	I _D =-250μA
ΔV _{GS(th)} /ΔT _J	-	6	-		
V _{GS(th)}	-2	-	-4	V	V _{DS} =V _{GS} , I _D =-250μA
I _{GSS}	-	-	±100	nA	V _{GS} =±30V, V _{DS} =0V
I _{DSS}	-	-	-1	μA	V _{DS} =-120V, V _{GS} =0V
	-	-	-10		V _{DS} =-120V, V _{GS} =0V, T _J =55°C
R _{DS(ON)} *1	-	0.52	0.68	Ω	V _{GS} =-10V, I _D =-1.4A
	-	0.56	0.79		V _{GS} =-6V, I _D =-1A
G _{FS} *1	-	2.8	-	S	V _{DS} =-10V, I _D =-1.4A

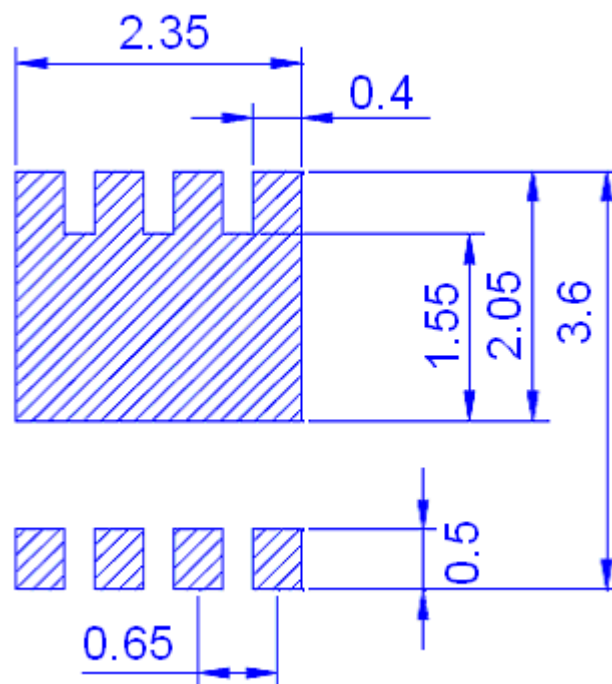
Dynamic					
Qg *1, 2	-	10.5		nC	V _{DS} =-75V, I _D =-5.4A, V _{GS} =-10V
Qgs *1, 2	-	3.1	-		
Qgd *1, 2	-	2.9	-		
t _{d(ON)} *1, 2	-	9.8	-	ns	V _{DS} =-75V, I _D =-1A, V _{GS} =-10V, R _G =6Ω
tr *1, 2	-	16.6	-		
t _{d(OFF)} *1, 2	-	31.4	-		
t _f *1, 2	-	28.4	-		
C _{iss}	-	571	-	pF	V _{DS} =-30V, V _{GS} =0V, f=1MHz
C _{oss}	-	29	-		
C _{rss}	-	17	-		
Source-Drain Diode					
I _S *1	-	-	-5.4	A	T _C =25°C
I _{SM} *3	-	-	-16		
V _{SD} *1	-	-0.77	-1.2	V	I _F =-1A, V _{GS} =0V
t _{rr}	-	28	-	ns	I _F =-1A, dI _F /dt=100A/μs
Q _{rr}	-	33	-	nC	

Note : *1.Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

*2.Independent of operating temperature

*3.Pulse width limited by maximum junction temperature.

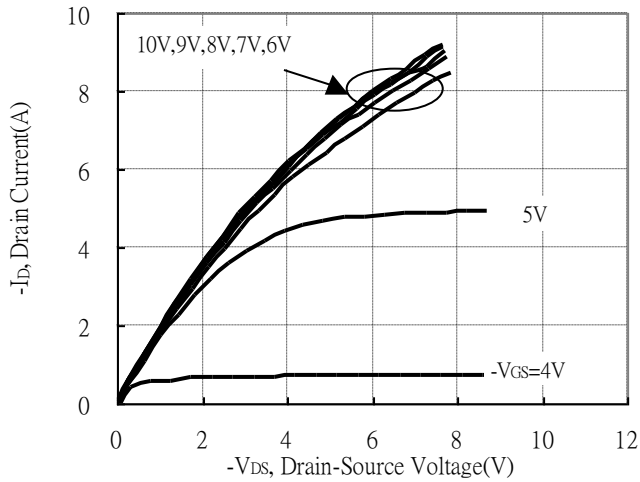
Recommended Soldering Footprint



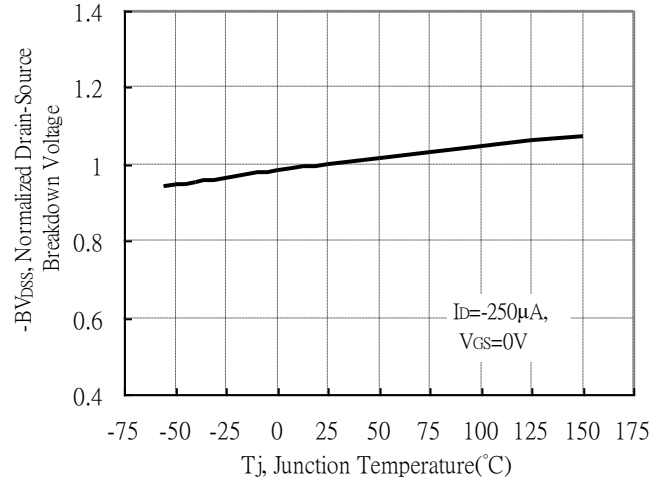
unit : mm

Typical Characteristics

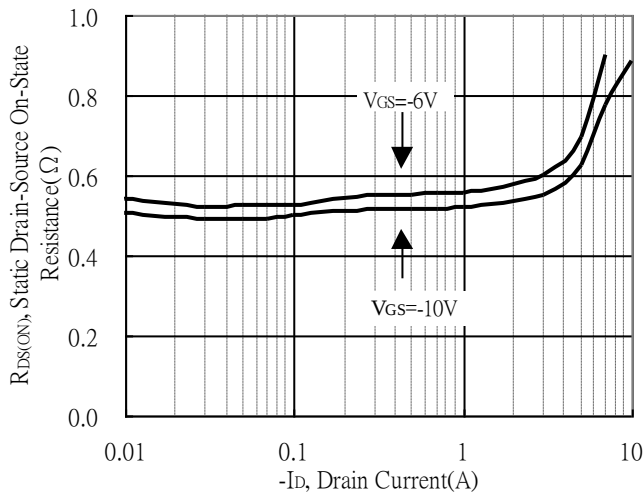
Typical Output Characteristics



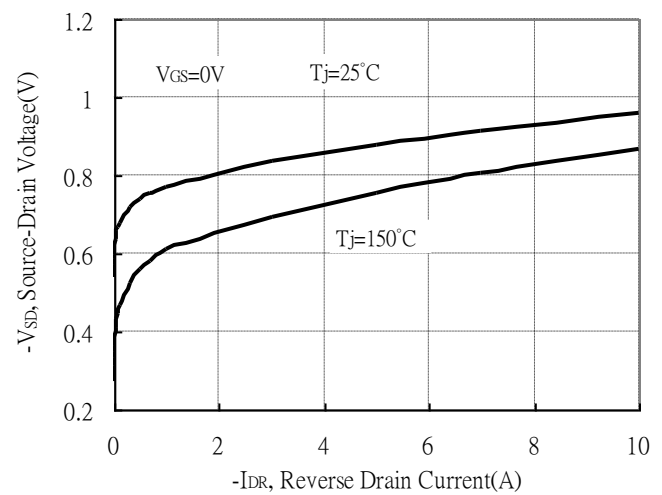
Brekdown Voltage vs Ambient Temperature



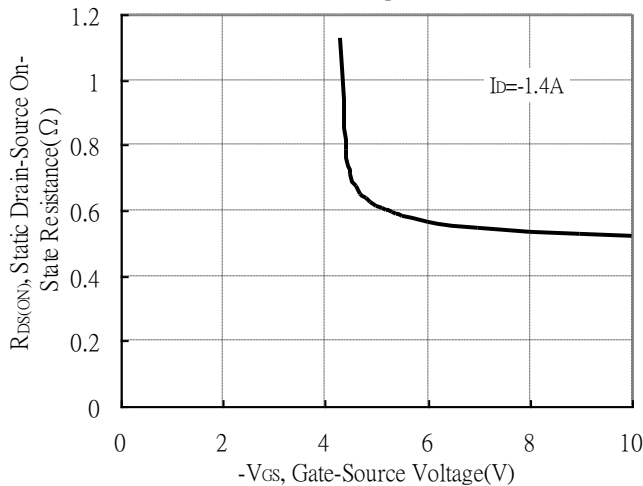
Static Drain-Source On-State resistance vs Drain Current



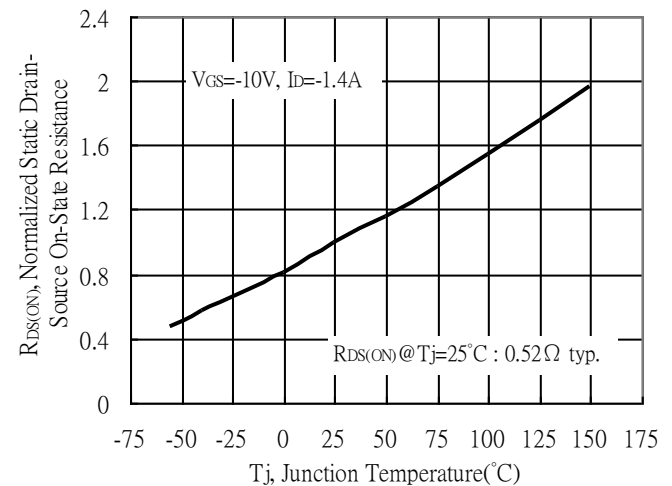
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

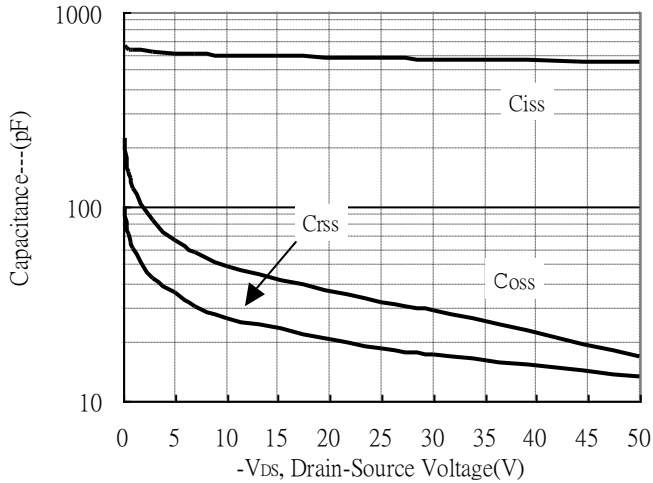


Drain-Source On-State Resistance vs Junction Temperature

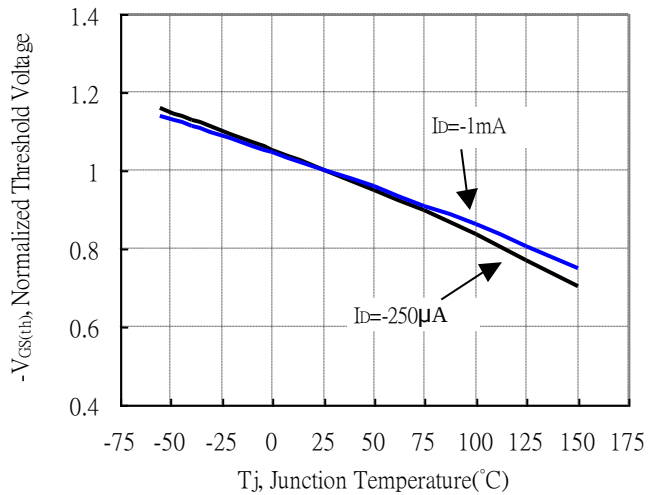


Typical Characteristics(Cont.)

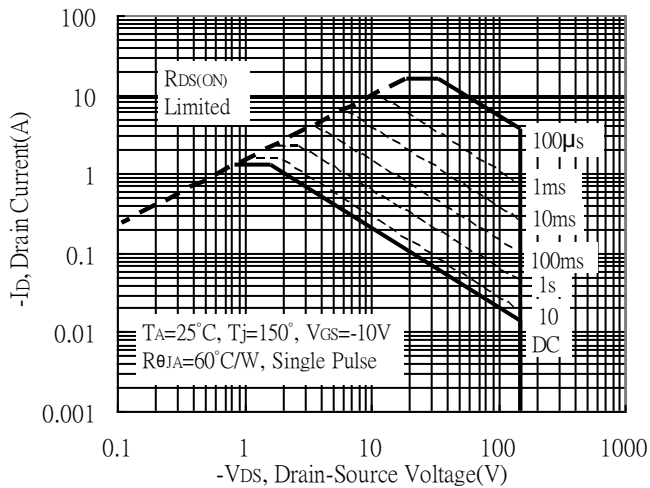
Capacitance vs Drain-to-Source Voltage



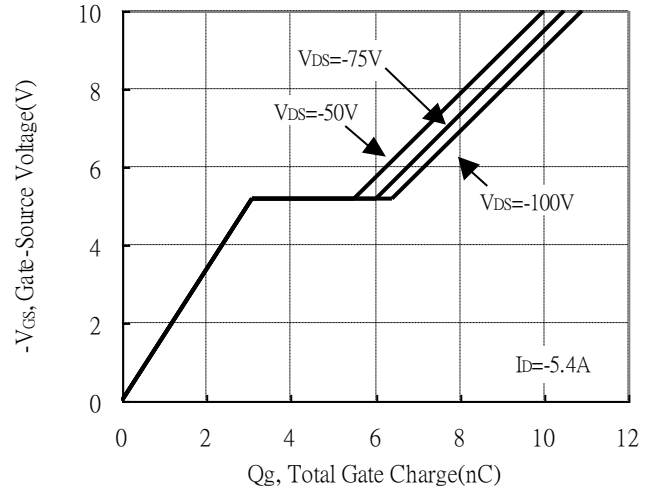
Threshold Voltage vs Junction Temperature



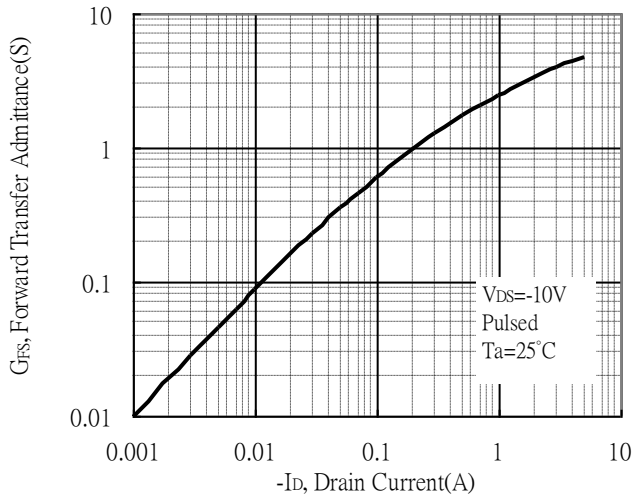
Maximum Safe Operating Area



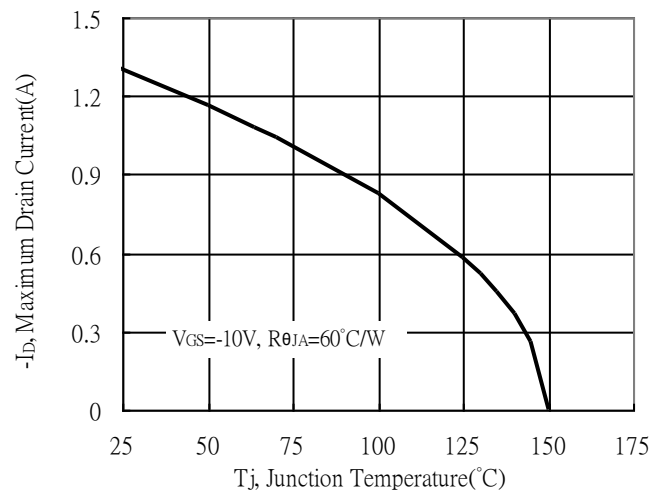
Gate Charge Characteristics



Forward Transfer Admittance vs Drain Current

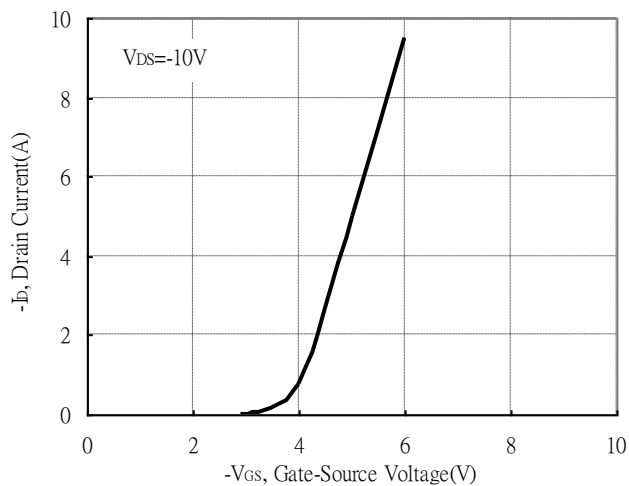


Maximum Drain Current vs Junction Temperature

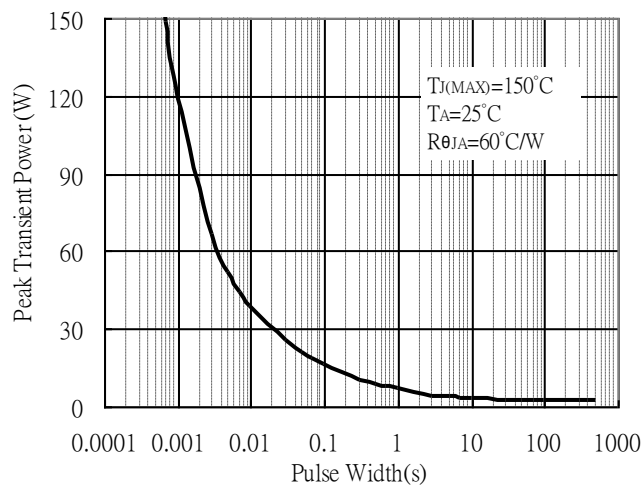


Typical Characteristics(Cont.)

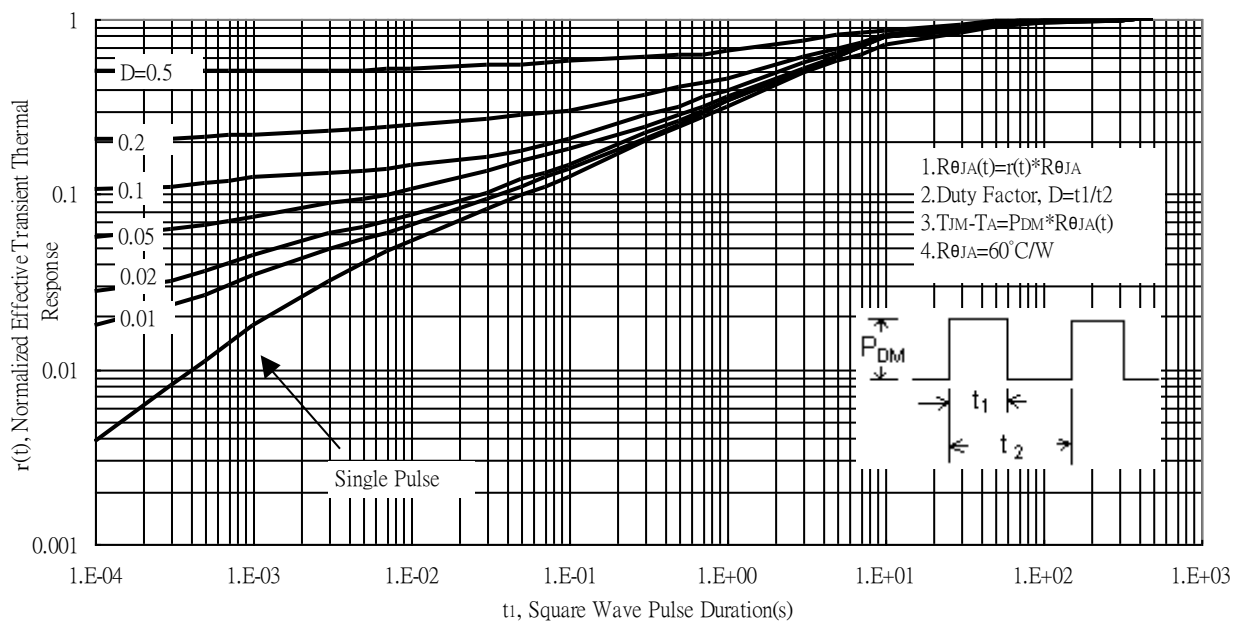
Typical Transfer Characteristics



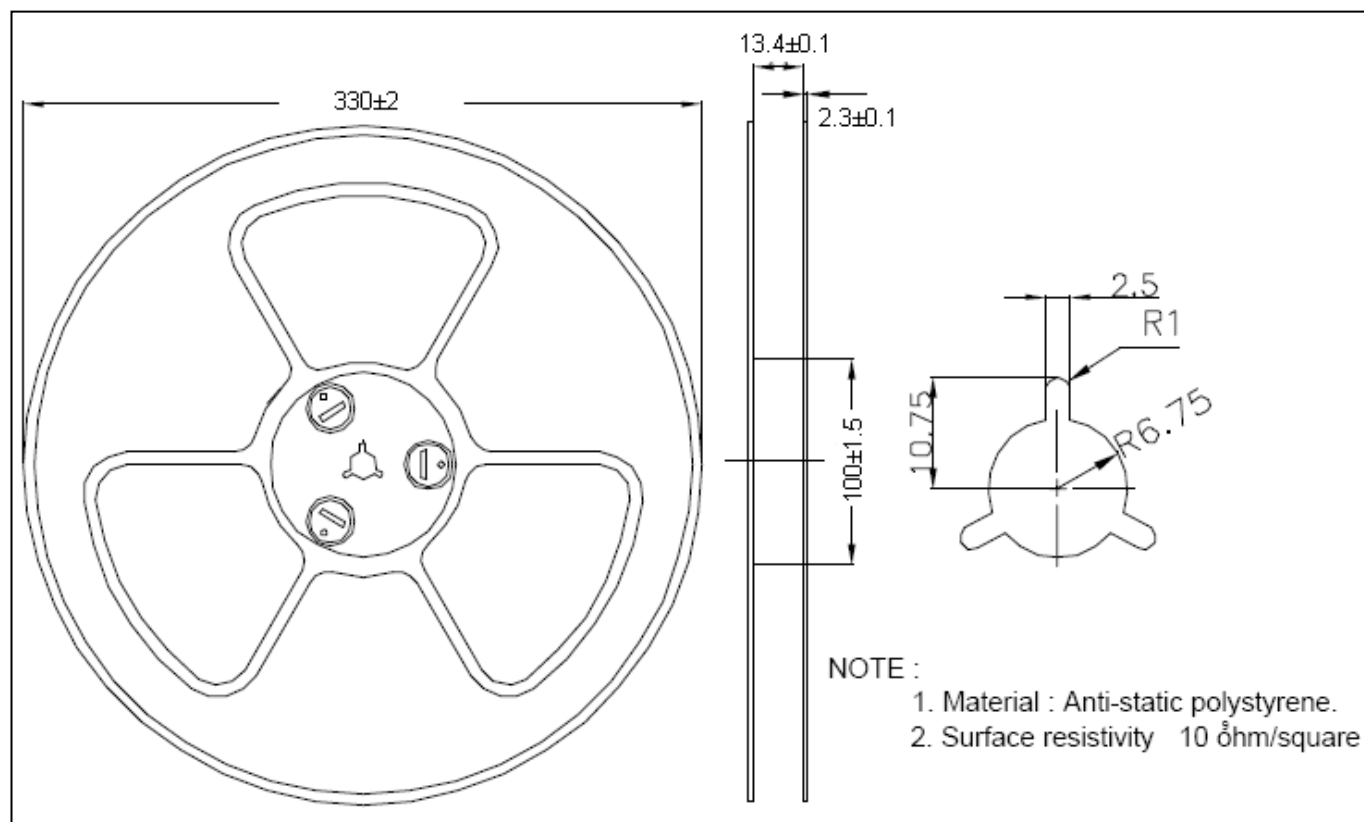
Single Pulse Maximum Power Dissipation



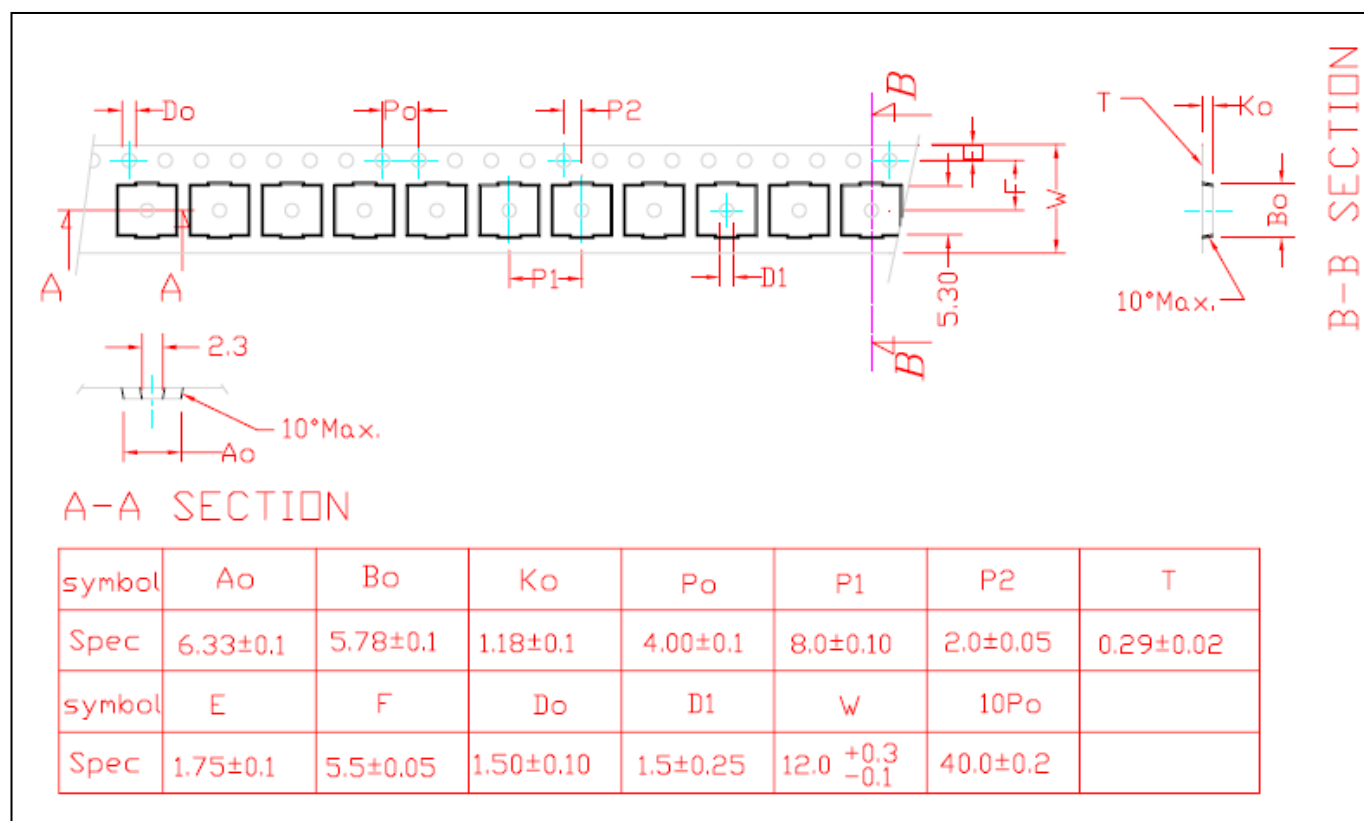
Transient Thermal Response Curves



Reel Dimension



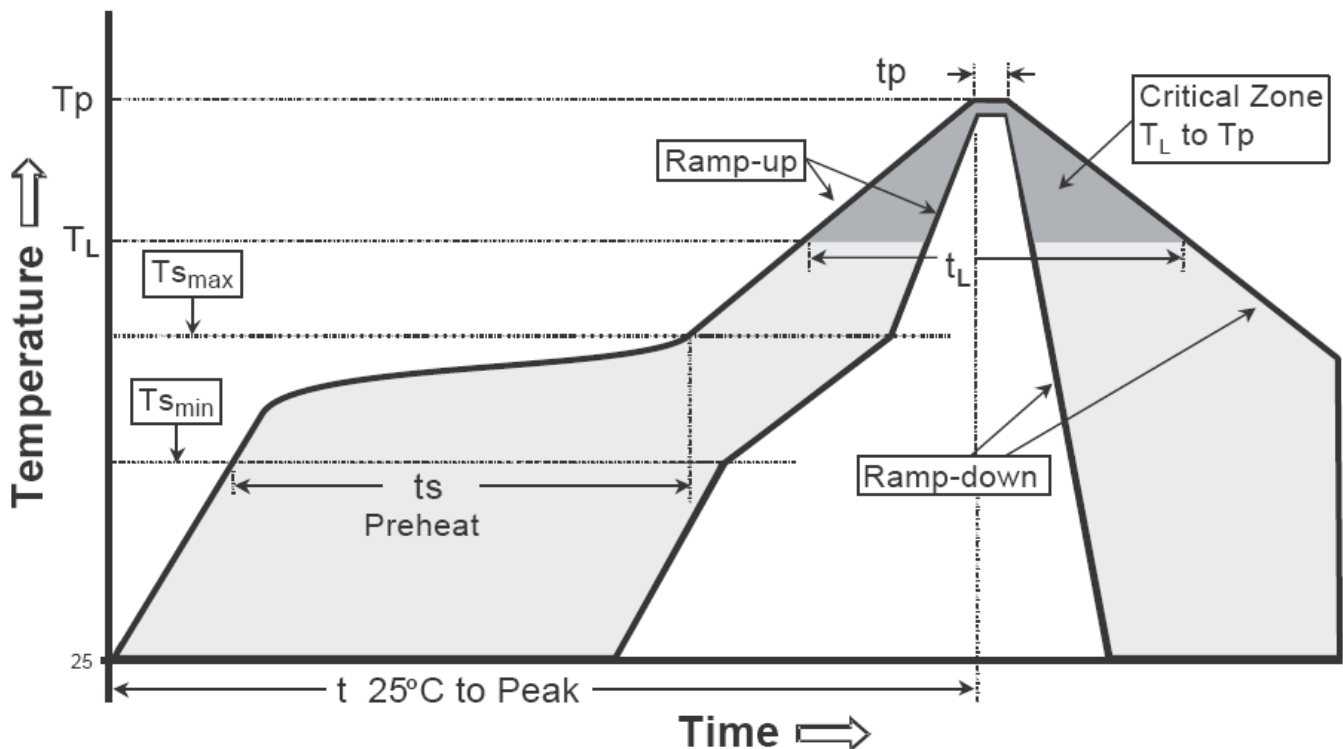
Carrier Tape Dimension



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

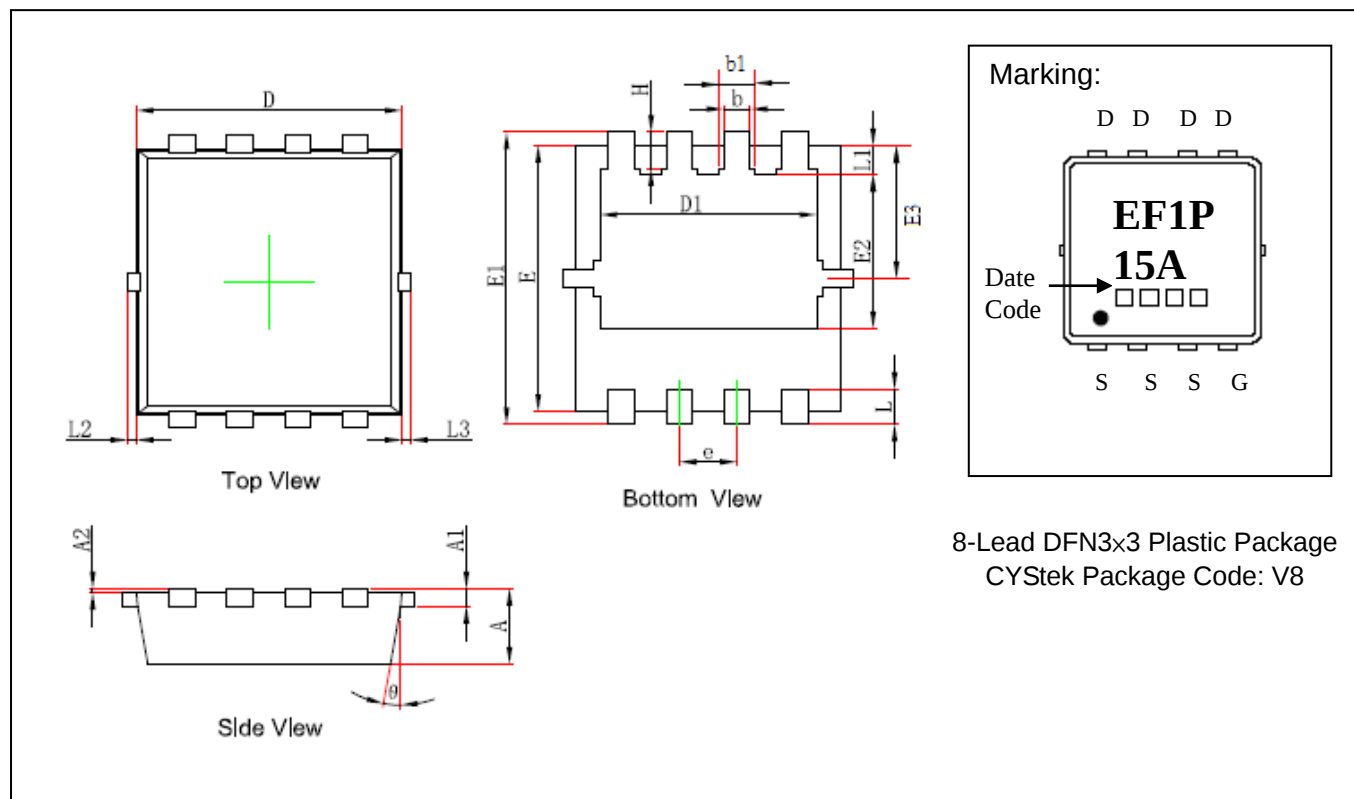
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(Ts min)	100°C	150°C
-Temperature Max(Ts max)	150°C	200°C
-Time(ts min to ts max)	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (TL)	183°C	217°C
- Time (tL)	60-150 seconds	60-150 seconds
Peak Temperature(TP)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

DFN3x3 Dimension



*: Typical

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.605	0.850	0.026	0.033	b	0.200	0.400	0.008	0.016
A1	0.152	REF	0.006	REF	b1	0.500	REF	0.020	REF
A2	0.000	0.050	0.000	0.002	e	0.550	0.750	0.022	0.030
D	2.900	3.100	0.114	0.122	L	0.300	0.500	0.012	0.020
D1	2.300	2.600	0.091	0.102	L1	0.180	0.480	0.007	0.019
E	2.900	3.100	0.114	0.122	L2	0.000	0.100	0.000	0.004
E1	3.150	3.450	0.124	0.136	L3	0.000	0.100	0.000	0.004
E2	1.535	1.935	0.060	0.076	H	0.315	0.515	0.012	0.020
E3	1.500	REF	0.059	REF	θ	9°	13°	9°	13°

Notes: 1.Controlling dimension: millimeters.

2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.

3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

Important Notice:

- All rights are reserved. Reproduction in whole or in part is prohibited without the prior written approval of CYStek.
- CYStek reserves the right to make changes to its products without notice.
- CYStek **semiconductor products are not warranted to be suitable for use in Life-Support Applications, or systems.**
- CYStek assumes no liability for any consequence of customer product design, infringement of patents, or application assistance.

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for [MOSFET](#) category:

Click to view products by [CYSTECH](#) manufacturer:

Other Similar products are found below :

[614233C](#) [648584F](#) [MCH3443-TL-E](#) [MCH6422-TL-E](#) [FDPF9N50NZ](#) [FW216A-TL-2W](#) [FW231A-TL-E](#) [APT5010JVR](#) [NTNS3A92PZT5G](#)
[IRF100S201](#) [JANTX2N5237](#) [2SK2464-TL-E](#) [2SK3818-DL-E](#) [FCA20N60_F109](#) [FDZ595PZ](#) [STD6600NT4G](#) [FSS804-TL-E](#) [2SJ277-DL-E](#)
[2SK1691-DL-E](#) [2SK2545\(Q,T\)](#) [D2294UK](#) [405094E](#) [423220D](#) [MCH6646-TL-E](#) [TPCC8103,L1Q\(CM](#) [367-8430-0972-503](#) [VN1206L](#)
[424134F](#) [026935X](#) [051075F](#) [SBVS138LT1G](#) [614234A](#) [715780A](#) [NTNS3166NZT5G](#) [751625C](#) [873612G](#) [IRF7380TRHR](#)
[IPS70R2K0CEAKMA1](#) [RJK60S3DPP-E0#T2](#) [RJK60S5DPK-M0#T0](#) [APT5010JVFR](#) [APT12031JFLL](#) [APT12040JVR](#) [DMN3404LQ-7](#)
[NTE6400](#) [JANTX2N6796U](#) [JANTX2N6784U](#) [JANTXV2N5416U4](#) [SQM110N05-06L-GE3](#) [SIHF35N60E-GE3](#)