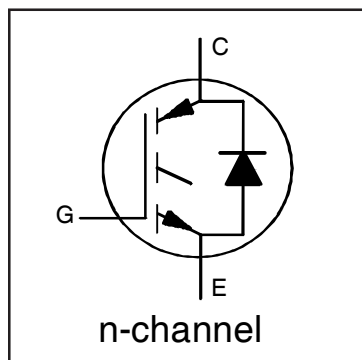


INSULATED GATE BIPOLAR TRANSISTOR WITH ULTRAFAST SOFT RECOVERY DIODE

Features

- Low $V_{CE(on)}$ Trench IGBT Technology
- Low Switching Losses
- 5 μ s SCSOA
- Square RBSOA
- 100% of The Parts Tested for I_{LM} ①
- Positive $V_{CE(on)}$ Temperature Coefficient.
- Ultra Fast Soft Recovery Co-pak Diode
- Tighter Distribution of Parameters
- Lead-Free, RoHS Compliant



$$V_{CES} = 600V$$

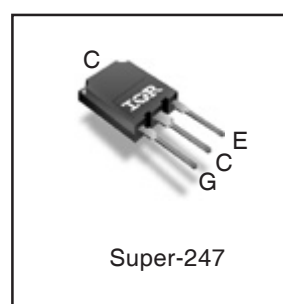
$$I_{C(Nominal)} = 120A$$

$$t_{SC} \geq 5\mu s, T_{J(max)} = 175^{\circ}C$$

$$V_{CE(on)} \text{ typ.} = 1.70V$$

Benefits

- High Efficiency in a Wide Range of Applications
- Suitable for a Wide Range of Switching Frequencies due to Low $V_{CE(ON)}$ and Low Switching Losses
- Rugged Transient Performance for Increased Reliability
- Excellent Current Sharing in Parallel Operation
- Low EMI



G	C	E
Gate	Collector	Emitter

Absolute Maximum Ratings

	Parameter	Max.	Units
V_{CES}	Collector-to-Emitter Voltage	600	V
$I_C @ T_C = 25^{\circ}C$	Continuous Collector Current	240 ^③	A
$I_C @ T_C = 100^{\circ}C$	Continuous Collector Current	160 ^③	
$I_{NOMINAL}$	Nominal Current	120	
I_{CM}	Pulse Collector Current, $V_{GE} = 15V$	360	
I_{LM}	Clamped Inductive Load Current, $V_{GE} = 20V$ ①	480	
$I_F @ T_C = 25^{\circ}C$	Diode Continuous Forward Current	240	
$I_F @ T_C = 100^{\circ}C$	Diode Continuous Forward Current	160	V
I_{FM}	Diode Maximum Forward Current ②	480	
V_{GE}	Continuous Gate-to-Emitter Voltage	± 20	W
	Transient Gate-to-Emitter Voltage	± 30	
$P_D @ T_C = 25^{\circ}C$	Maximum Power Dissipation	750	$^{\circ}C$
$P_D @ T_C = 100^{\circ}C$	Maximum Power Dissipation	375	
T_J	Operating Junction and	-55 to +175	$^{\circ}C$
T_{STG}	Storage Temperature Range		
	Soldering Temperature, for 10 sec.	300 (0.063 in. (1.6mm) from case)	

Thermal Resistance

	Parameter	Min.	Typ.	Max.	Units
$R_{\theta JC}$ (IGBT)	Thermal Resistance Junction-to-Case-(each IGBT) ④	—	—	0.20	$^{\circ}C/W$
$R_{\theta JC}$ (Diode)	Thermal Resistance Junction-to-Case-(each Diode) ④	—	—	0.63	
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink (flat, greased surface)	—	0.24	—	
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient (typical socket mount)	—	—	40	

Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)CES}$	Collector-to-Emitter Breakdown Voltage	600	—	—	V	$V_{GE} = 0V, I_C = 100\mu A$ ③
$\Delta V_{(BR)CES}/\Delta T_J$	Temperature Coeff. of Breakdown Voltage	—	0.27	—	V/°C	$V_{GE} = 0V, I_C = 4.0mA$ (25°C-175°C)
$V_{CE(on)}$	Collector-to-Emitter Saturation Voltage	—	1.70	2.05	V	$I_C = 120A, V_{GE} = 15V, T_J = 25^\circ\text{C}$
		—	2.15	—		$I_C = 120A, V_{GE} = 15V, T_J = 150^\circ\text{C}$
		—	2.20	—		$I_C = 120A, V_{GE} = 15V, T_J = 175^\circ\text{C}$
$V_{GE(th)}$	Gate Threshold Voltage	4.0	—	6.5	V	$V_{CE} = V_{GE}, I_C = 5.6mA$
$\Delta V_{GE(th)}/\Delta T_J$	Threshold Voltage temp. coefficient	—	-17	—	mV/°C	$V_{CE} = V_{GE}, I_C = 5.6mA$ (25°C - 175°C)
g_{fe}	Forward Transconductance	—	77	—	S	$V_{CE} = 50V, I_C = 120A$
I_{CES}	Collector-to-Emitter Leakage Current	—	1.0	150	μA	$V_{GE} = 0V, V_{CE} = 600V$
		—	2.3	—	mA	$V_{GE} = 0V, V_{CE} = 600V, T_J = 175^\circ\text{C}$
V_{FM}	Diode Forward Voltage Drop	—	2.4	3.0	V	$I_F = 120A$
		—	1.9	—		$I_F = 120A, T_J = 175^\circ\text{C}$
I_{GES}	Gate-to-Emitter Leakage Current	—	—	±400	nA	$V_{GE} = \pm 20V$

Switching Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max. ⑥	Units	Conditions
Q_g	Total Gate Charge (turn-on)	—	240	360	nC	$I_C = 120A$
Q_{ge}	Gate-to-Emitter Charge (turn-on)	—	70	105		$V_{GE} = 15V$
Q_{gc}	Gate-to-Collector Charge (turn-on)	—	90	135		$V_{CC} = 400V$
E_{on}	Turn-On Switching Loss	—	5750	7990	μJ	$I_C = 120A, V_{CC} = 400V, V_{GE} = 15V$
E_{off}	Turn-Off Switching Loss	—	3430	4360		$R_G = 4.7\Omega, L = 66\mu H, T_J = 25^\circ\text{C}$
E_{total}	Total Switching Loss	—	9180	12350		Energy losses include tdi & diode reverse recovery
$t_{d(on)}$	Turn-On delay time	—	80	100	ns	$I_C = 120A, V_{CC} = 400V, V_{GE} = 15V$
t_r	Rise time	—	70	125		$R_G = 4.7\Omega, L = 66\mu H, T_J = 25^\circ\text{C}$
$t_{d(off)}$	Turn-Off delay time	—	190	220		
t_f	Fall time	—	40	60		
E_{on}	Turn-On Switching Loss	—	7740	—		
E_{off}	Turn-Off Switching Loss	—	4390	—	μJ	$I_C = 120A, V_{CC} = 400V, V_{GE} = 15V$
E_{total}	Total Switching Loss	—	12130	—		$R_G = 4.7\Omega, L = 66\mu H, T_J = 175^\circ\text{C}$
		—		—		Energy losses include tdi & diode reverse recovery
$t_{d(on)}$	Turn-On delay time	—	80	—	ns	$I_C = 120A, V_{CC} = 400V, V_{GE} = 15V$
t_r	Rise time	—	75	—		$R_G = 4.7\Omega, L = 66\mu H$
$t_{d(off)}$	Turn-Off delay time	—	230	—		$T_J = 175^\circ\text{C}$
t_f	Fall time	—	55	—		
C_{ies}	Input Capacitance	—	7750	—	pF	$V_{GE} = 0V$
C_{oes}	Output Capacitance	—	550	—		$V_{CC} = 30V$
C_{res}	Reverse Transfer Capacitance	—	225	—		$f = 1.0MHz$
RBSOA	Reverse Bias Safe Operating Area	FULL SQUARE				$T_J = 175^\circ\text{C}, I_C = 480A$ $V_{CC} = 480V, V_p = 600V$ $R_G = 4.7\Omega, V_{GE} = +20V$ to 0V
SCSOA	Short Circuit Safe Operating Area	5	—	—	μs	$V_{CC} = 400V, V_p = 600V$ $R_G = 4.7\Omega, V_{GE} = +15V$ to 0V
E_{rec}	Reverse Recovery Energy of the Diode	—	500	—	μJ	$T_J = 175^\circ\text{C}$
t_{rr}	Diode Reverse Recovery Time	—	130	—	ns	$V_{CC} = 400V, I_F = 120A$
I_{rr}	Peak Reverse Recovery Current	—	36	—	A	$V_{GE} = 15V, R_G = 4.7\Omega, L = 100\mu H$

Notes:

- ① $V_{CC} = 80\% (V_{CES}), V_{GE} = 20V, L = 66\mu H, R_G = 4.7\Omega$, tested in production $I_{LM} \leq 400A$.
- ② Pulse width limited by max. junction temperature.
- ③ Refer to AN-1086 for guidelines for measuring $V_{(BR)CES}$ safely.
- ④ R_θ is measured at T_J of approximately 90°C.
- ⑤ Calculated continuous current based on maximum allowable junction temperature. Package IGBT current limit is 195A. Package diode current limit is 120A. Note that current limitations arising from heating of the device leads may occur.
- ⑥ Maximum limits are based on statistical sample size characterization.

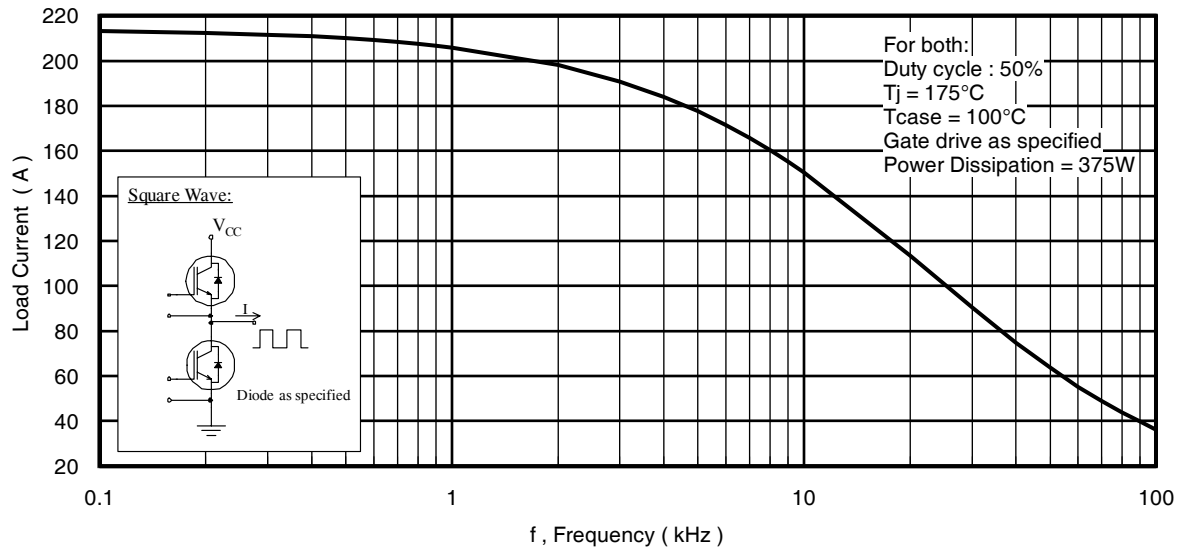


Fig. 1 - Typical Load Current vs. Frequency
(Load Current = I_{RMS} of fundamental)

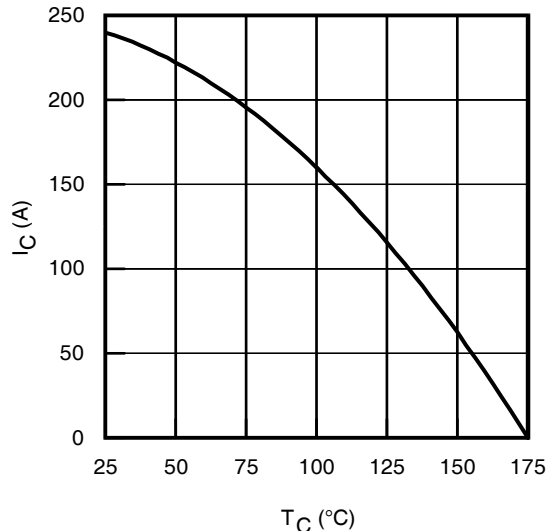


Fig. 2 - Maximum DC Collector Current vs. Case Temperature

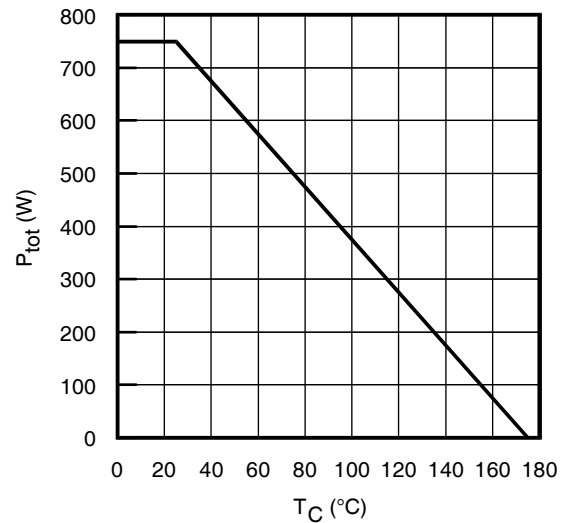


Fig. 3 - Power Dissipation vs. Case Temperature

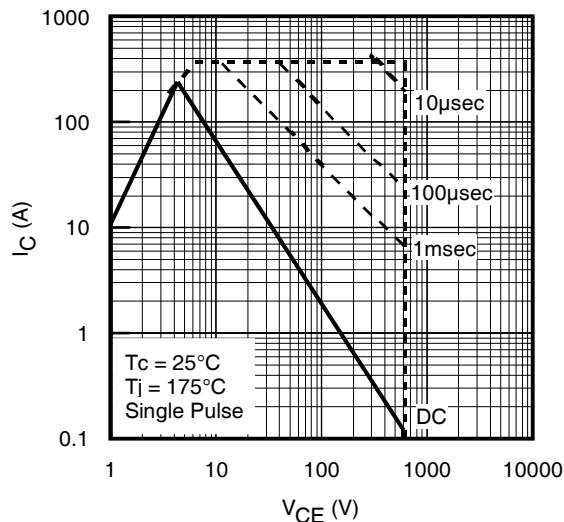


Fig. 4 - Forward SOA
 $T_C = 25^{\circ}\text{C}$, $T_J \leq 175^{\circ}\text{C}$; $V_{\text{GE}} = 15\text{V}$

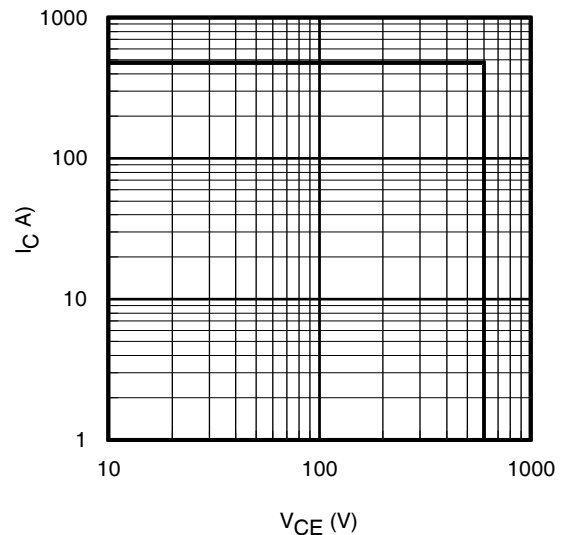


Fig. 5 - Reverse Bias SOA
 $T_J = 175^{\circ}\text{C}$; $V_{\text{GE}} = 20\text{V}$

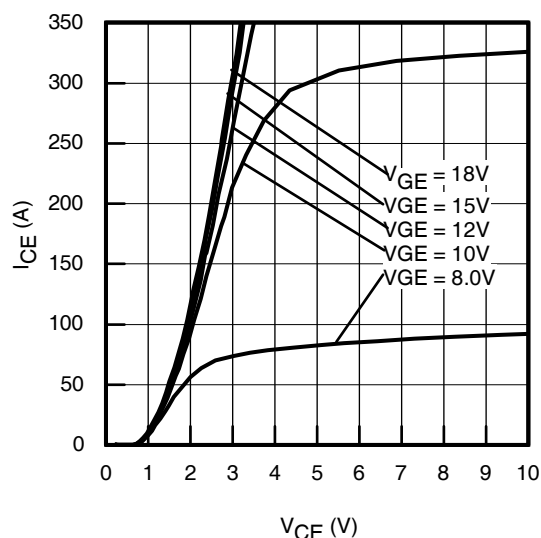


Fig. 6 - Typ. IGBT Output Characteristics
T_J = -40°C; t_p = 80μs

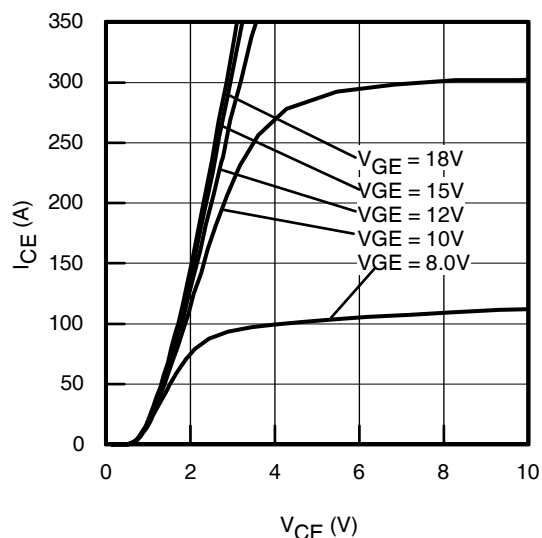


Fig. 7 - Typ. IGBT Output Characteristics
T_J = 25°C; t_p = 80μs

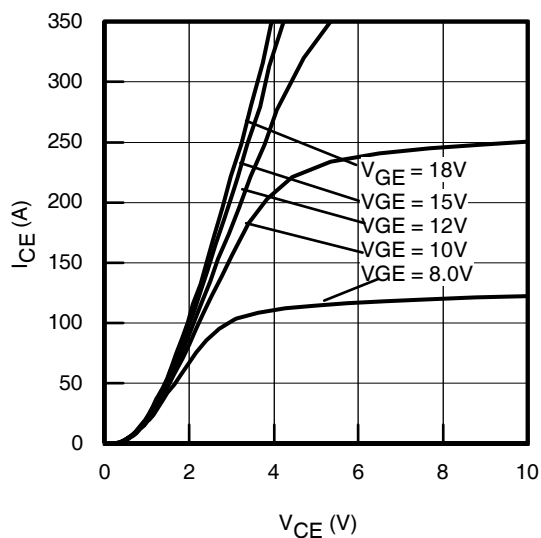


Fig. 8 - Typ. IGBT Output Characteristics
T_J = 175°C; t_p = 80μs

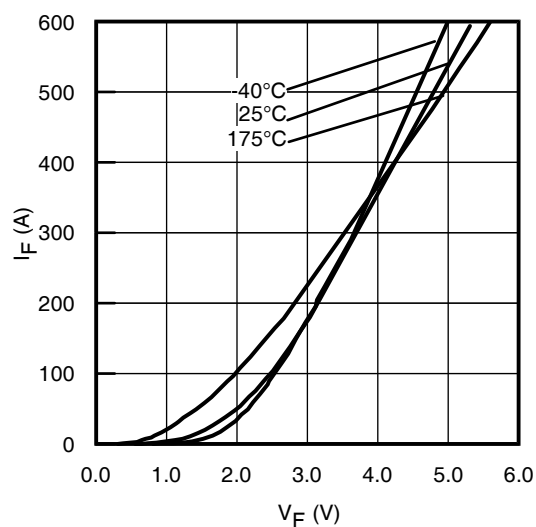


Fig. 9 - Typ. Diode Forward Characteristics
t_p = 80μs

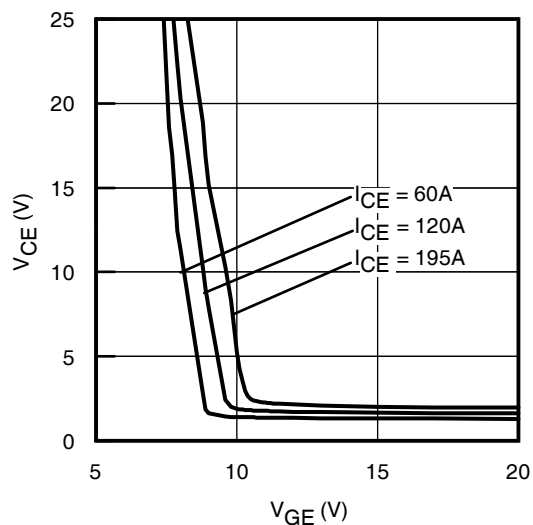


Fig. 10 - Typical V_{CE} vs. V_{GE}
T_J = -40°C

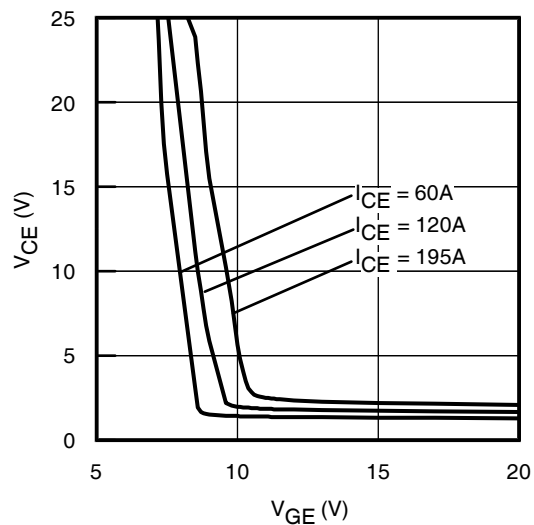


Fig. 11 - Typical V_{CE} vs. V_{GE}
T_J = 25°C

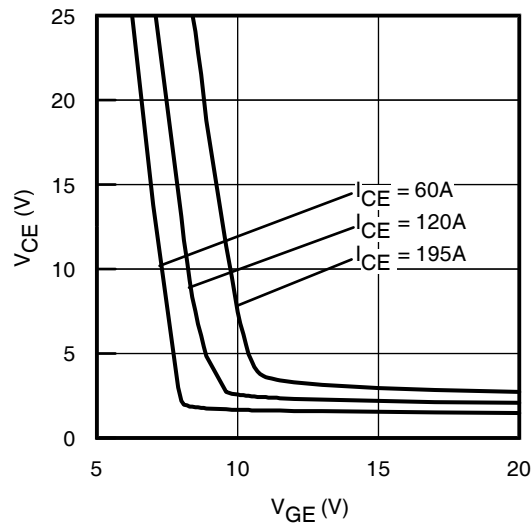


Fig. 12 - Typical V_{CE} vs. V_{GE}
 $T_J = 175^\circ\text{C}$

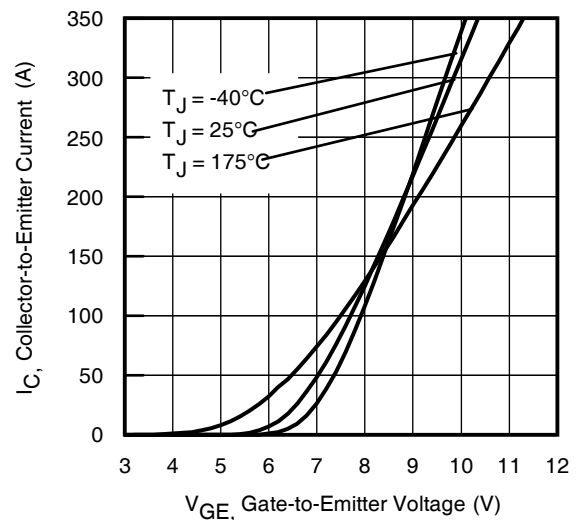


Fig. 13 - Typ. Transfer Characteristics
 $V_{CE} = 50\text{V}$; $t_p = 10\mu\text{s}$

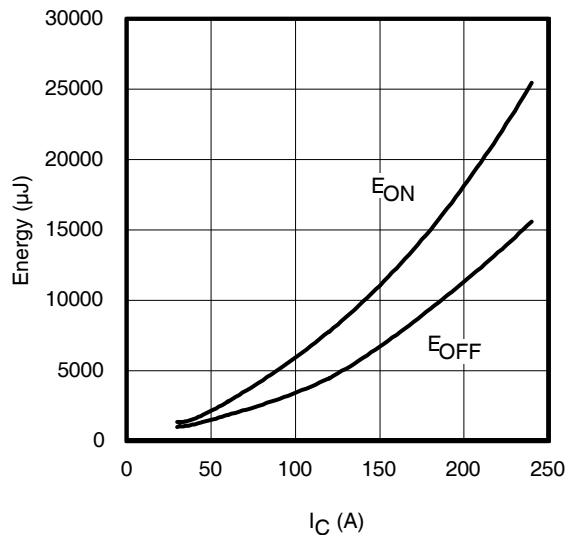


Fig. 14 - Typ. Energy Loss vs. I_C
 $T_J = 175^\circ\text{C}$; $L = 66\mu\text{H}$; $V_{CE} = 400\text{V}$; $R_G = 4.7\Omega$; $V_{GE} = 15\text{V}$

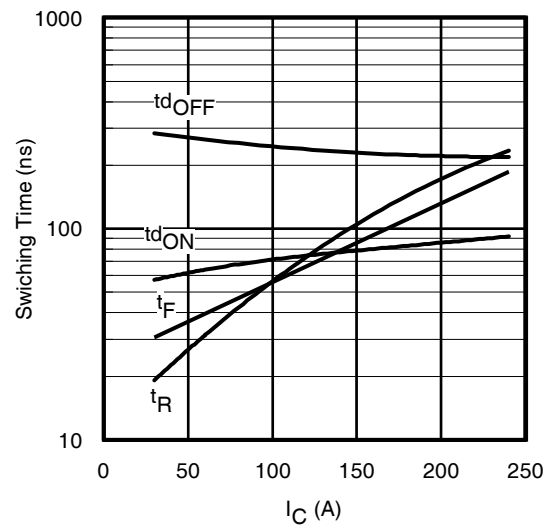


Fig. 15 - Typ. Switching Time vs. I_C
 $T_J = 175^\circ\text{C}$; $L = 66\mu\text{H}$; $V_{CE} = 400\text{V}$; $R_G = 4.7\Omega$; $V_{GE} = 15\text{V}$

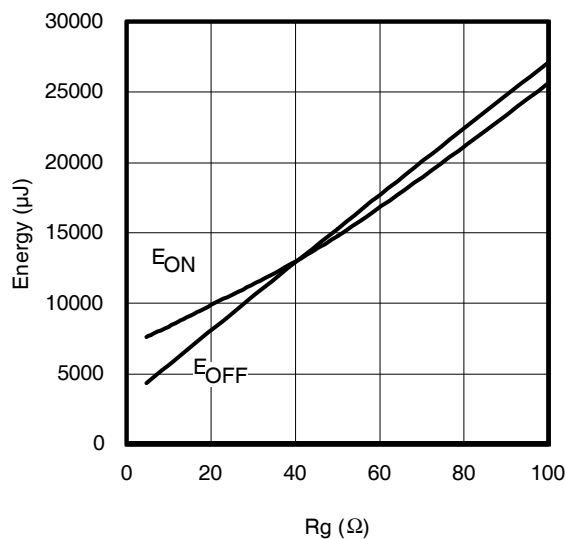


Fig. 16 - Typ. Energy Loss vs. R_G
 $T_J = 175^\circ\text{C}$; $L = 66\mu\text{H}$; $V_{CE} = 400\text{V}$; $I_{CE} = 120\text{A}$; $V_{GE} = 15\text{V}$

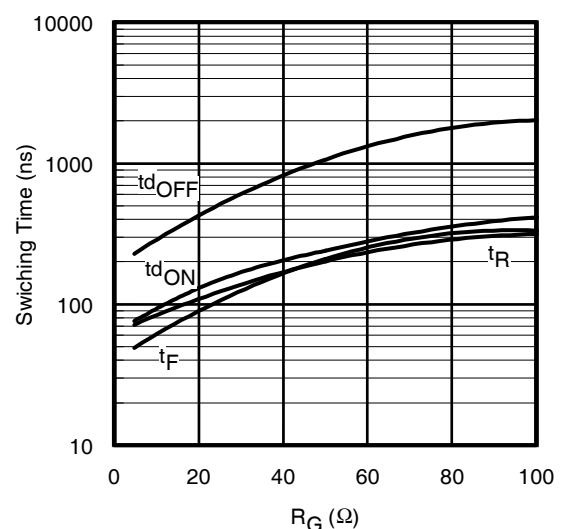


Fig. 17 - Typ. Switching Time vs. R_G
 $T_J = 175^\circ\text{C}$; $L = 66\mu\text{H}$; $V_{CE} = 400\text{V}$; $I_{CE} = 120\text{A}$; $V_{GE} = 15\text{V}$

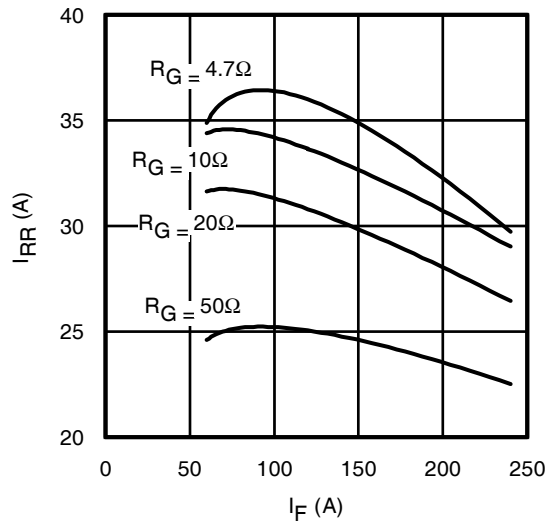


Fig. 18 - Typ. Diode I_{RR} vs. I_F
 $T_J = 175^\circ\text{C}$

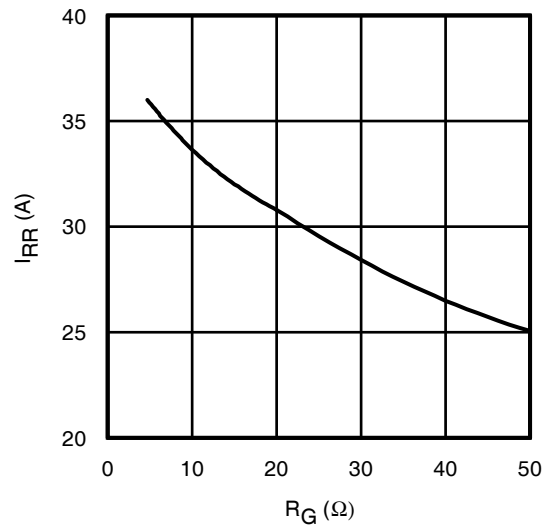


Fig. 19 - Typ. Diode I_{RR} vs. R_G
 $T_J = 175^\circ\text{C}$

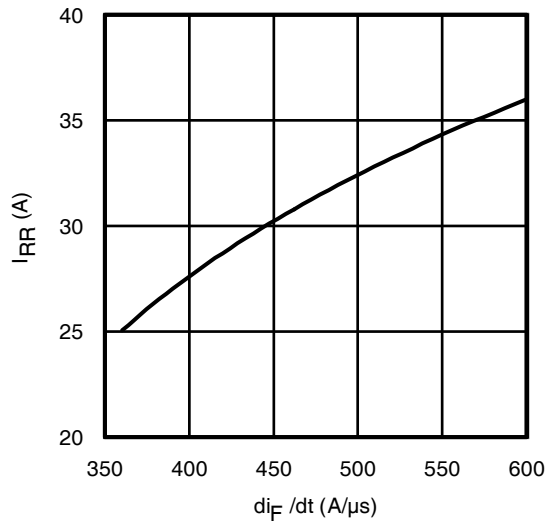


Fig. 20 - Typ. Diode I_{RR} vs. di_F/dt
 $V_{CC} = 400\text{V}$; $V_{GE} = 15\text{V}$; $I_F = 120\text{A}$; $T_J = 175^\circ\text{C}$

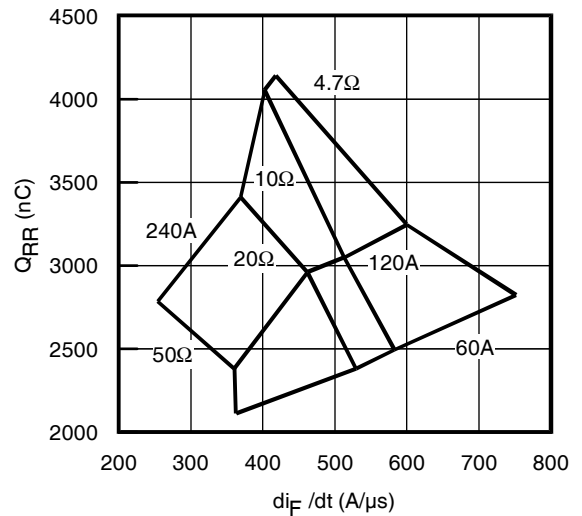


Fig. 21 - Typ. Diode Q_{RR} vs. di_F/dt
 $V_{CC} = 400\text{V}$; $V_{GE} = 15\text{V}$; $T_J = 175^\circ\text{C}$

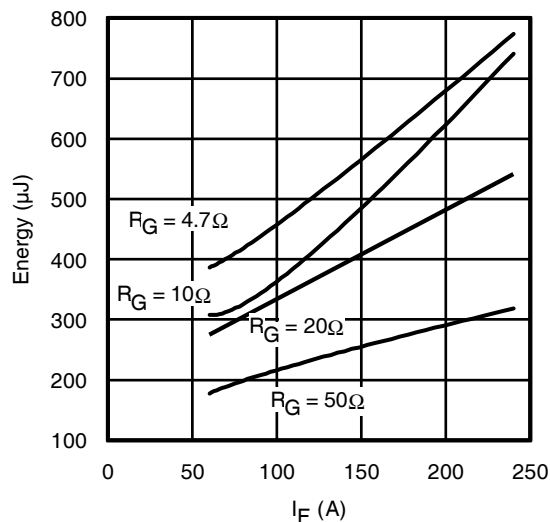


Fig. 22 - Typ. Diode E_{RR} vs. I_F
 $T_J = 175^\circ\text{C}$

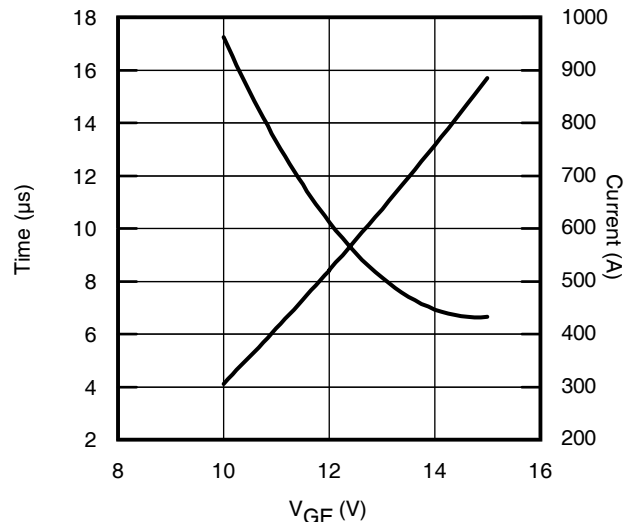


Fig. 23 - V_{GE} vs. Short Circuit Time
 $V_{CC} = 400\text{V}$; $T_C = 25^\circ\text{C}$

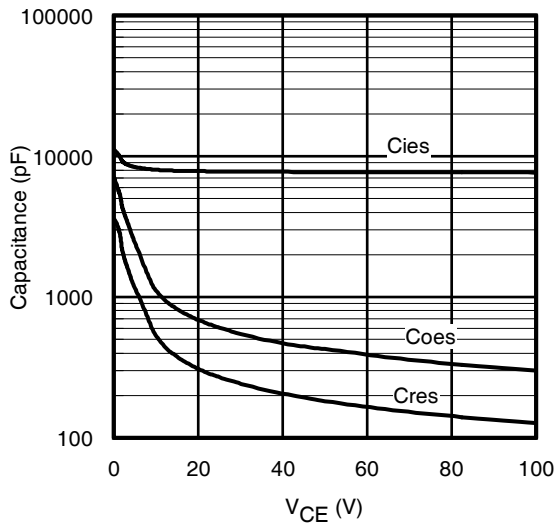


Fig. 24 - Typ. Capacitance vs. V_{CE}
 $V_{GE} = 0V$; $f = 1MHz$

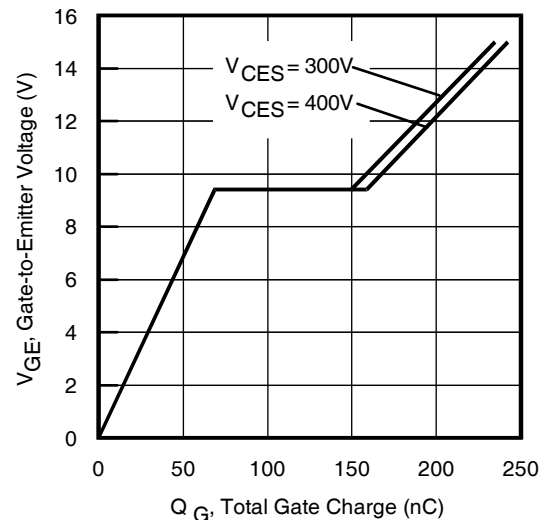


Fig. 25 - Typical Gate Charge vs. V_{GE}
 $I_{CE} = 120A$; $L = 100\mu H$

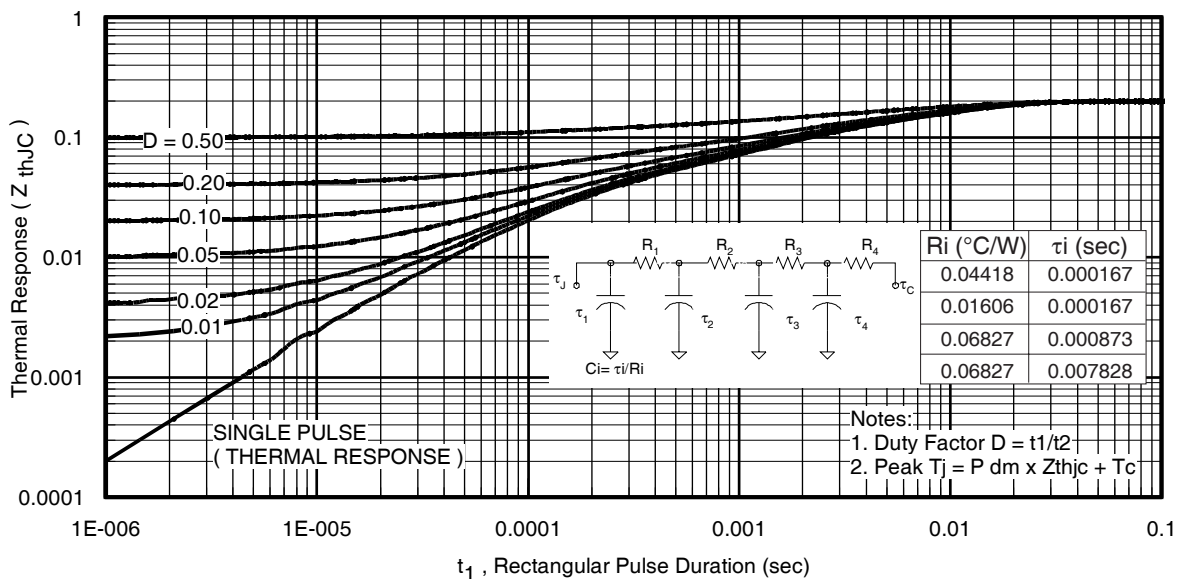


Fig. 26. Maximum Transient Thermal Impedance, Junction-to-Case (IGBT)

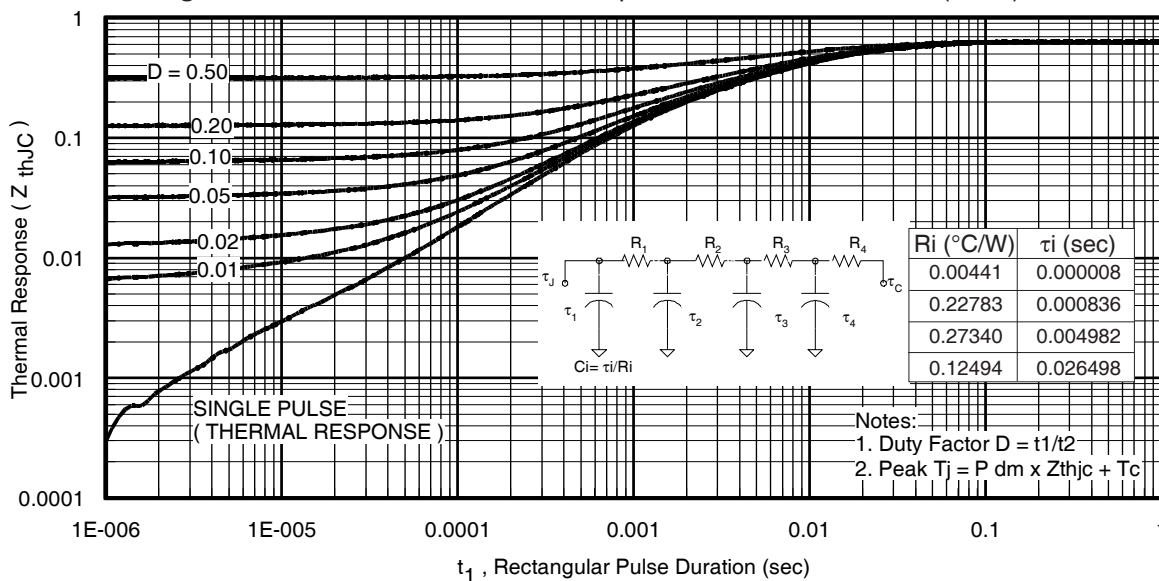
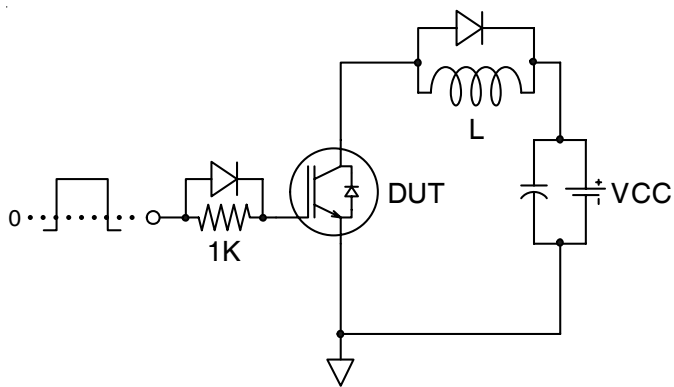
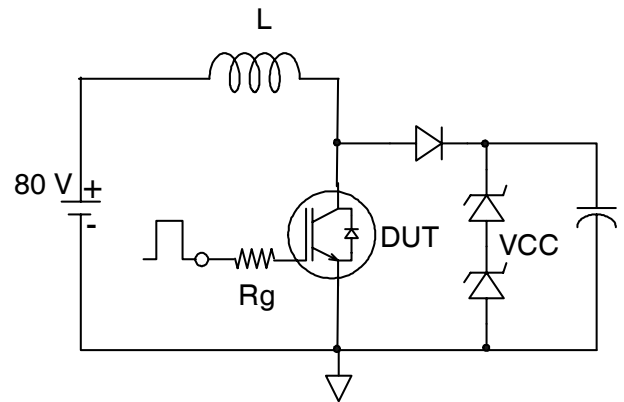
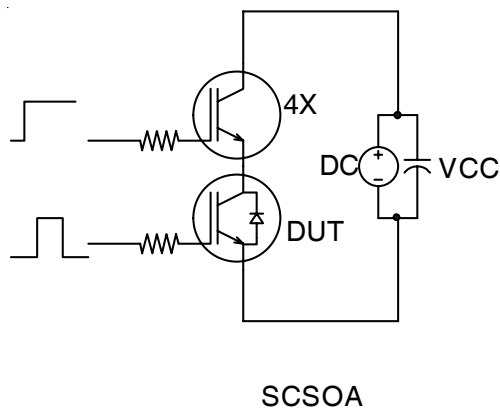
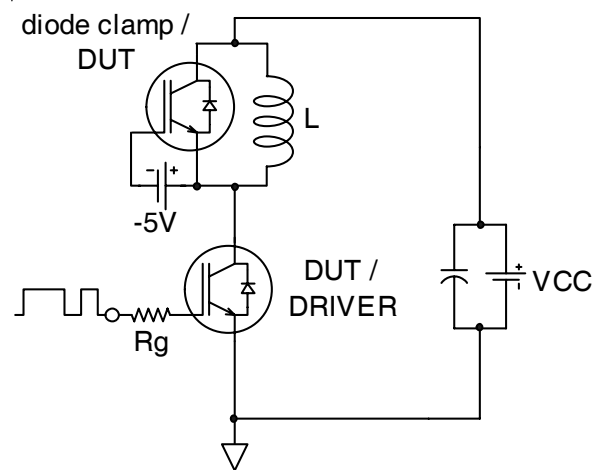
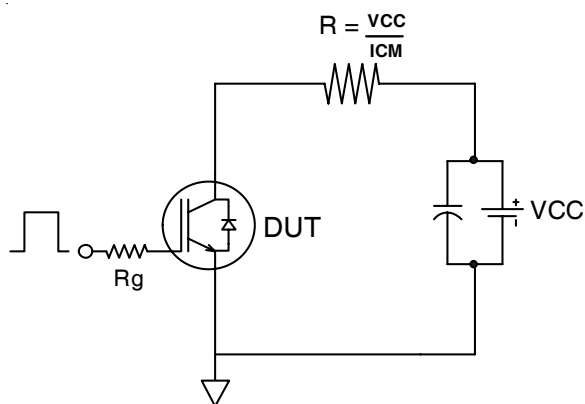
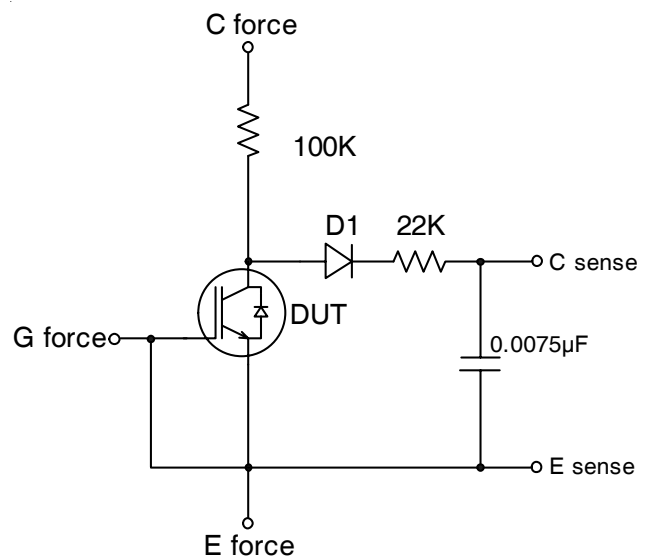


Fig. 27. Maximum Transient Thermal Impedance, Junction-to-Case (DIODE)


Fig.C.T.1 - Gate Charge Circuit (turn-off)

Fig.C.T.2 - RBSOA Circuit

Fig.C.T.3 - S.C. SOA Circuit

Fig.C.T.4 - Switching Loss Circuit

Fig.C.T.5 - Resistive Load Circuit

Fig.C.T.6 - BVCES Filter Circuit

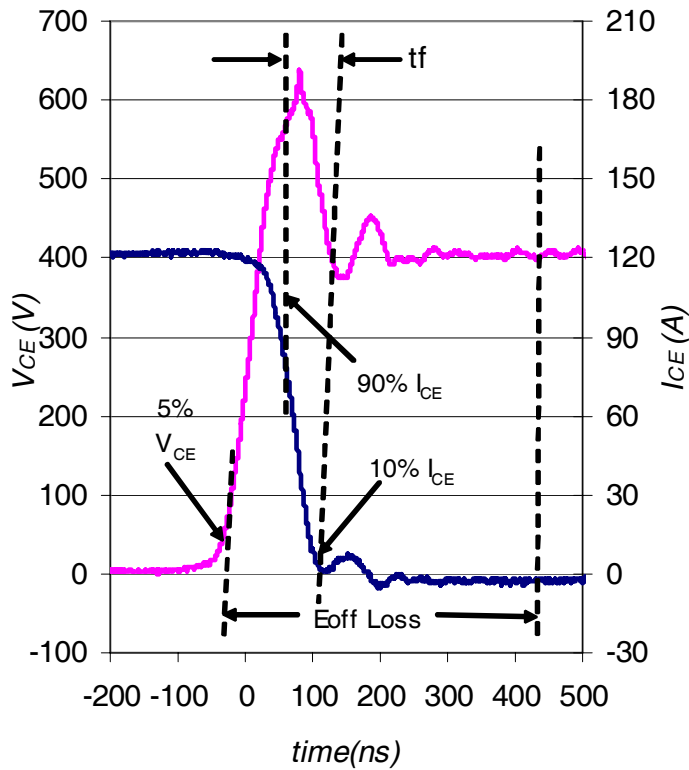


Fig. WF1 - Typ. Turn-off Loss Waveform
@ $T_J = 175^\circ\text{C}$ using Fig. CT.4

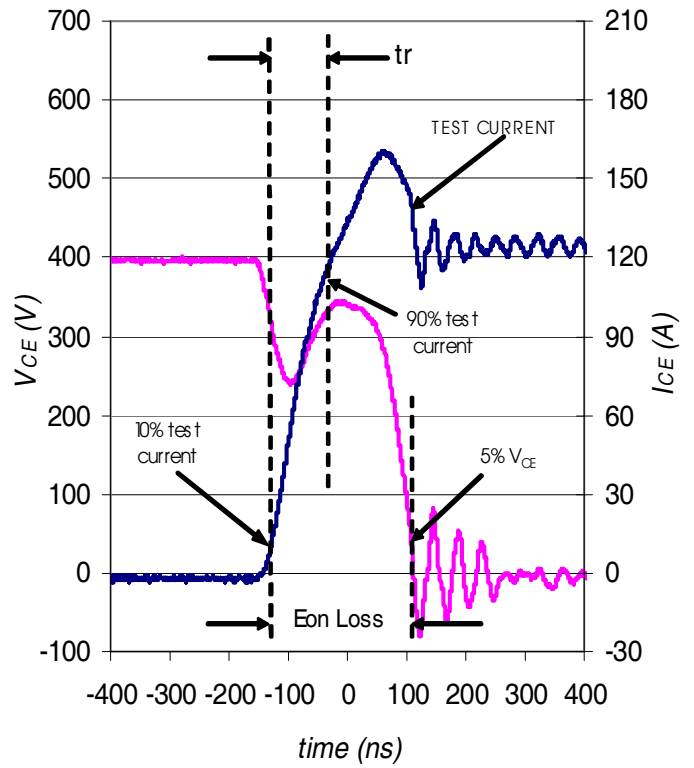


Fig. WF2 - Typ. Turn-on Loss Waveform
@ $T_J = 175^\circ\text{C}$ using Fig. CT.4

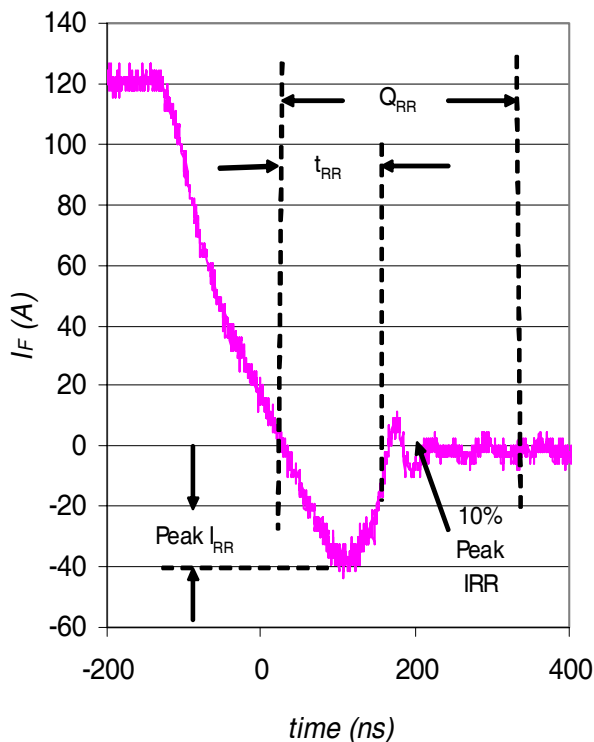


Fig. WF3 - Typ. Diode Recovery Waveform
@ $T_J = 175^\circ\text{C}$ using Fig. CT.4

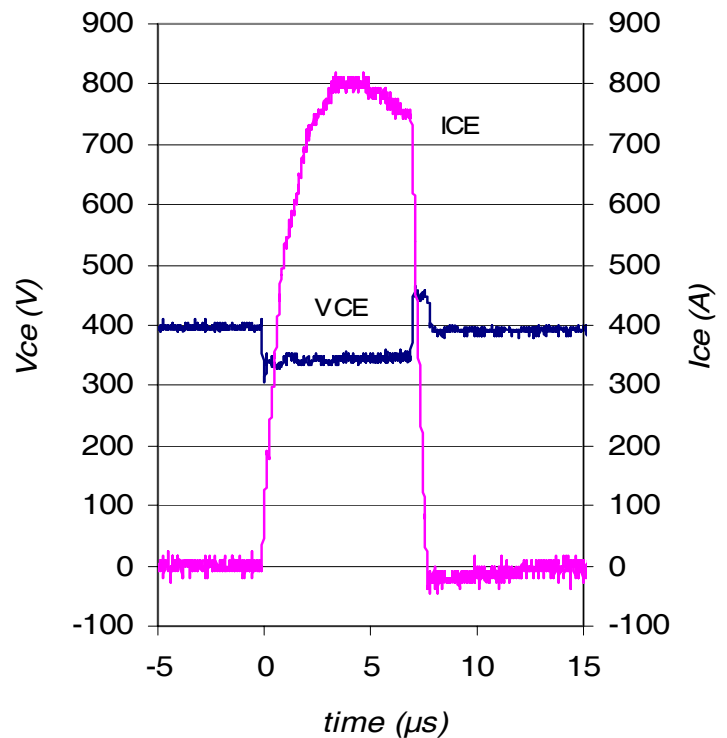
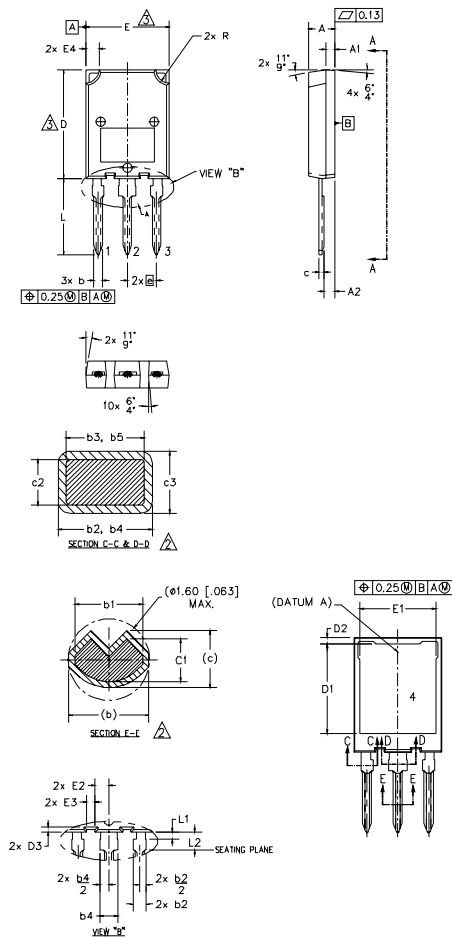


Fig. WF4 - Typ. S.C. Waveform
@ $T_J = 25^\circ\text{C}$ using Fig. CT.3

Case Outline and Dimensions — Super-247



NOTES:

1. DIMENSIONING AND TOLERANCING AS PER ASME Y14.5M-1994
2. DIMENSIONS b1, b3, b5, c1 & c3 APPLY TO BASE METAL ONLY.
3. DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.127 [.005"] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTER EXTREMES OF THE PLASTIC BODY.
4. ALL DIMENSIONS SHOWN IN MILLIMETERS.
5. CONTROLLING DIMENSION: MILLIMETER.
6. OUTLINE CONFORMS TO JEDEC OUTLINE TO-274AA

SYM BOL	DIMENSIONS				NOTES
	MILLIMETERS		INCHES		
	MIN.	MAX.	MIN.	MAX.	
A	4.50	5.50	.177	.217	
A1	1.45	2.15	.057	.085	
A2	1.65	2.35	.065	.093	
b	1.45	1.60	.054	.063	
b1	1.40	1.50	.055	.059	2
b2	2.00	2.40	.079	.094	
b3	1.95	2.35	.077	.093	2
b4	3.00	3.15	.118	.124	
b5	2.95	3.35	.116	.132	2
c	1.10	1.30	.043	.051	
c1	0.90	1.10	.035	.043	2
c2	0.65	0.85	.026	.033	
c3	0.50	0.70	.020	.028	2
D	19.80	20.80	.780	.819	3
D1	15.50	16.10	.610	.634	
D2	0.70	1.30	.028	.051	
D3	0.75	1.25	.030	.049	
E	15.10	16.10	.594	.634	3
E1	13.30	13.90	.524	.547	
E2	2.25	2.70	.089	.109	
E3	1.20	1.70	.047	.067	
E4	2.00	3.00	.079	.118	
e	5.45 BSC		.215	BSC	
L	13.80	14.80	.535	.583	
L1	1.00	1.60	.039	.063	
L2	3.85	4.25	.152	.167	
R	2.00	3.00	.079	.118	

LEAD ASSIGNMENTS

MOSFET

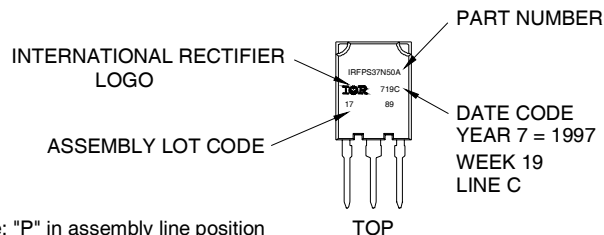
1. GATE
2. DRAIN
3. SOURCE
4. DRAIN

IGBT

1. GATE
2. COLLECTOR
3. EMITTER
4. COLLECTOR

Super-247 (TO-274AA) Part Marking Information

EXAMPLE: THIS IS AN IRFPS37N50A WITH
ASSEMBLY LOT CODE 1789
ASSEMBLED ON WW 19, 1997
IN THE ASSEMBLY LINE "C"



Note: "P" in assembly line position indicates "Lead-Free"

Note: For the most current drawing please refer to IR website at <http://www.irf.com/package/>

International
IR Rectifier

IR WORLD HEADQUARTERS: 101 N. Sepulveda Blvd., El Segundo, California 90245, USA
To contact International Rectifier, please visit <http://www.irf.com/whoto-call/>

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[FGB3236_F085](#) [APT13GP120BDQ1G](#) [APT25GN120BG](#) [APT25GR120S](#) [APT30GN60BDQ2G](#) [APT30GN60BG](#) [APT30GP60BG](#)
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