



NTE5693/NTE5695/NTE5697 NTE56022/NTE56024/NTE56026 TRIAC, 40 Amp

Description:

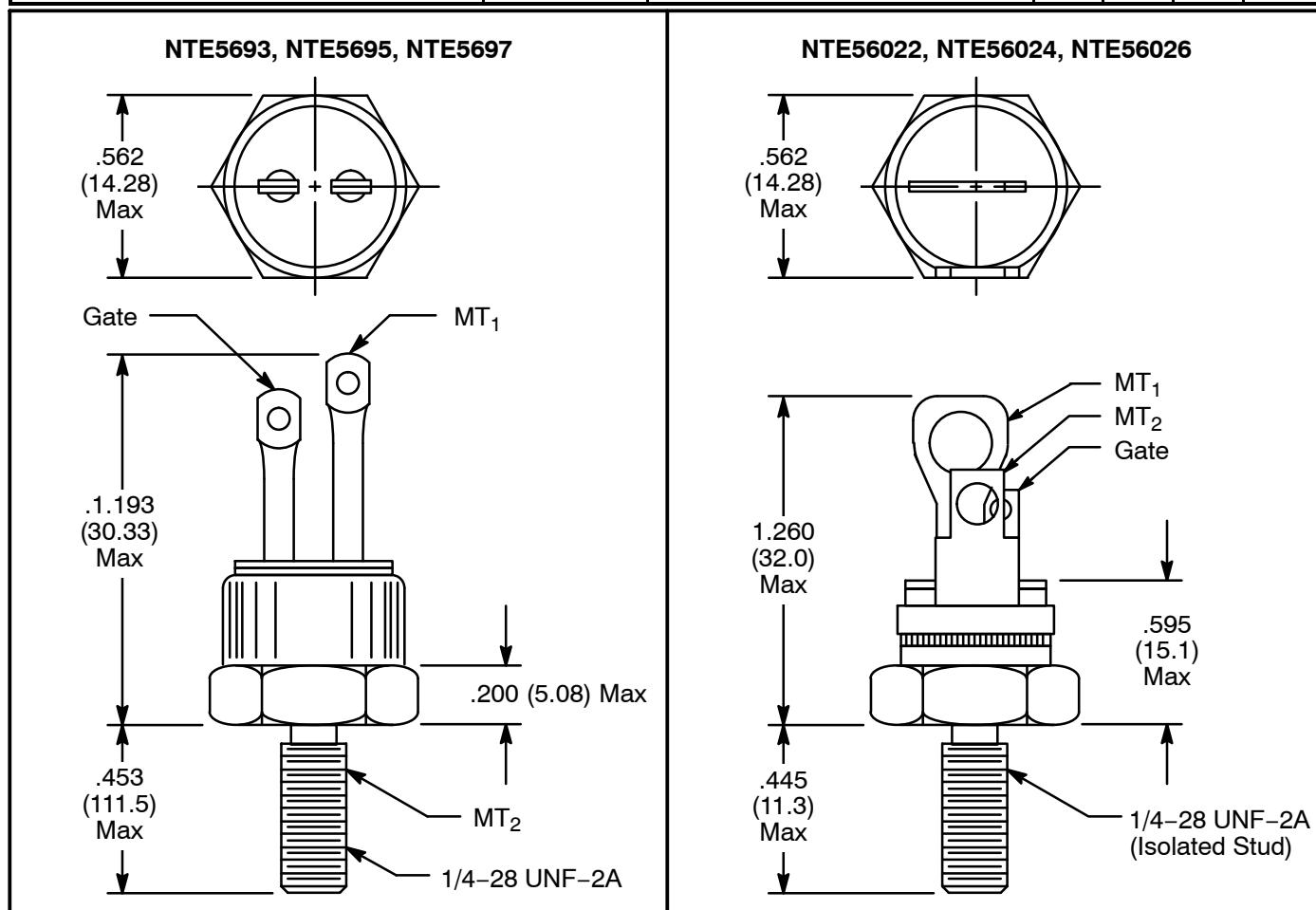
This NTE 40 Amp series medium power TRIACs are bi-directional triode thyristors which may be switched from off-state to conduction for either polarity of applied voltage with positive or negative gate triggering. These devices are designed for control of AC loads in applications such as lighting, heating and motor speed control, as well as static switching relays. These devices are available in a standard TO48 package (NTE5693, NTE5695, and NTE5697) or an Isolated TO48 package NTE56022, NTE56024, and NTE56026).

Absolute Maximum Ratings:

Repetitive Peak Off-State Voltage ($T_J = +100^\circ\text{C}$), V_{DRM}	
NTE5693, NTE56022 200V
NTE5695, NTE56024 400V
NTE5697, NTE56026 600V
Repetitive Peak Reverse Voltage ($T_J = +100^\circ\text{C}$), V_{RRM}	
NTE5693, NTE56022 200V
NTE5695, NTE56024 400V
NTE5697, NTE56026 600V
RMS On-State Current ($T_C = +75^\circ\text{C}$, 360° Continuous), $I_{\text{T(RMS)}}$ 40A
Peak Surge (Non-Repetitive) On-State Current, I_{TSM}	
One-Cycle, 60Hz 400A
One-Cycle, 50Hz 350A
Peak Gate-Trigger Current (3° s Max), I_{GTM} 12A
Peak Gate-Power Dissipation ($I_{\text{GT}} \leq I_{\text{GTM}}$ for 3° s Max), P_{GM} 40W
Average Gate-Power Dissipation, $P_{\text{G(AV)}}$ 750mW
Operating Junction Temperature Range, T_{opr} -40° to $+100^\circ\text{C}$
NTE56026 ONLY -65° to $+110^\circ\text{C}$
Storage Temperature Range, T_{stg} -40° to $+150^\circ\text{C}$
NTE56026 ONLY -65° to $+150^\circ\text{C}$
Thermal Resistance, Junction-to-Case, R_{thJC}	
NTE5693, NTE5695, NTE5697 1.8°C/W
NTE56022, NTE56024 2.1°C/W
NTE56026 1°C/W

Electrical Characteristics: (At Maximum Ratings and Indicated Case Temperatures unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Peak Off-State Current	I _{DROM}	T _J = +100°C, Gate Open, V _{DROM} = Max Rating	-	-	4	mA
Maximum On-State Voltage	V _T	T _C = +25°C, I _T = 100A (Peak)	-	-	2.5	V
DC Holding Current	I _{HOLD}	T _C = +25°C, Gate Open	-	-	60	mA
Critical rate-of-Rise of Off-State Voltage NTE5693, NTE56022	Critical dv/dt	T _C = +100°C, At V _{DROM} , Gate Open	-	200	-	V/ ^o s
NTE5695, NTE56024			-	150	-	V/ ^o s
NTE5697, NTE56026			-	100	-	V/ ^o s
Critical Rate-of-Rise of Commutation Voltage	Commutating dv/dt	T _C = +75°C, Gate Unenergized, v _D = V _{DROM} , T = I _{T(RMS)} , Commutating di/dt = 16A/ms	3	-	-	V/ ^o s
DC Gate-Trigger Current (T ₂₊ , Gate+; T ₂₋ , Gate-) Quads I-II	I _{GT}	T _C = +25°C, v _D = 12VDC, R _L = 30≤	-	-	50	mA
(T ₂₊ , Gate-; T ₂₋ , Gate+) Quads II-IV			-	-	80	mA
DC Gate-Trigger Voltage	V _{GT}	T _C = +25°C, v _D = 12VDC, R _L = 30≤	-	-	2.5	V
Gate-Controlled Turn-On Time	t _{gt}	T _C = +25°C, v _D = V _{DROM} , I _{GT} = 300mA, t _r = 0.1 ^o s, I _T = 10A(Peak)	-	3	-	^o s



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