

CMOS Analog Switches**General Description**

Maxim's IH5048-IH5051 analog switches are designed for applications requiring low leakage. They feature extremely low on resistance (30Ω typical) as well as quiescent power-supply current below $1\mu A$. Switch control inputs are fully compatible with both CMOS and TTL logic.

These switches are plug-in upgrades for the original manufacturer's devices, with improved specifications for analog-signal range and switch on and off times. They are also pin-compatible with the IH5040 family of analog switches. The IH5048 series is supplied in 16-pin DIP and SO packages.

Applications

- Precision Sample-and-Hold Circuits
- Transducer and Sensor Switching
- Low-Level Signal Conditioning
- Battery-Powered Instrumentation
- Programmable-Gain Amplifiers

Features

- ◆ Low Charge Injection (10pC Typ)
- ◆ Quiescent Current Below 1mA
- ◆ TTL and CMOS Compatible
- ◆ Low On Resistance (25Ω Max for IH5048A)
- ◆ Latchup-Proof Construction

Ordering Information

PART	TEMP. RANGE	PIN-PACKAGE
IH5048CPE	0°C to +70°C	16 Plastic DIP
IH5048CWE	0°C to +70°C	16 Wide SO
IH5048CJE	0°C to +70°C	16 CERDIP
IH5048C/D	0°C to +70°C	Dice*
IH5048MJE	-55°C to +125°C	16 CERDIP**
IH5048ACPE	0°C to +70°C	16 Plastic DIP
IH5048ACWE	0°C to +70°C	16 Wide SO
IH5048ACJE	0°C to +70°C	16 CERDIP
IH5048AC/D	0°C to +70°C	Dice*
IH5048AMJE	-55°C to +125°C	16 CERDIP**

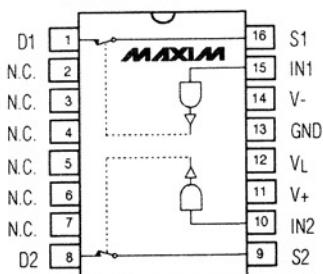
Ordering Information continued on last page.

* Contact factory for dice specifications.

** Contact factory for availability and processing to MIL-STD-883.

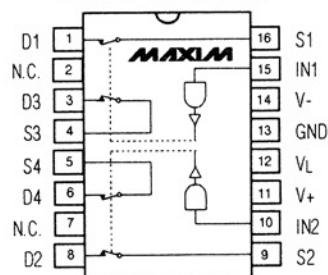
Pin Configurations & Switching-State Diagrams

TOP VIEW

**IH5048A - Dual SPST
IH5048 - Dual SPST**

DIP/SO

LOGIC	SWITCH
0	OFF
1	ON

IH5049 - Dual SPST

DIP/SO

LOGIC	SWITCH
0	OFF
1	ON

Pin Configurations & Switching-State Diagrams are continued on last page.

IH5048-IH5051

CMOS Analog Switches

ABSOLUTE MAXIMUM RATINGS

V ₊ to V ₋	36V	Continuous Power Dissipation (T _A = +70°C)
V ₊ to V _D	30V	Plastic DIP (derate 10.53mW/°C above +70°C) 842mW
V _D to V ₋	30V	Wide SO (derate 20.00mW/°C above +70°C) 1600mW
V _D to V _S	±28V	CERDIP (derate 10.00mW/°C above +70°C) 800mW
V _L to V ₋	33V	Operating Temperature Ranges:
V _L to V _{IN}	30V	IH50_C_IH50_AC 0°C to +70°C
V _L to GND	20V	IH50_M_IH50_AM -55°C to +125°C
V _{IN} to GND	20V	Storage Temperature Range -65°C to +150°C
Current (any terminal)	30mA	Lead Temperature (soldering, 10sec) +300°C
Digital Inputs	(V ₊ + 0.3V) to (V ₊ - 38V)	
V _S or V _D (Note 1)	-0.3V to (V ₊ + 0.3V)	

Note 1: Signals on S, D, and digital inputs that exceed V₋ or V₊ will be clamped by internal diodes. Limit forward diode current to 30mA maximum.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

(V₊ = 15V, V₋ = -15V, V_L = 5V, T_A = +25°C, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS			IH50_M IH50_AM			IH50_C IH50_AC			UNITS		
					-55°C	+25°C	+125°C	0°C	+25°C	+70°C			
Input Logic Current	I _{IN(ON)}	V _{IN} = 2.4V			±1	±1	±10	±1	±1	±10	μA		
	I _{IN(OFF)}	V _{IN} = 0.8V			±1	±1	±10	±1	±1	±10			
Input Logic Low	V _{IL}				0.8	0.8	0.8	0.8	0.8	0.8	V		
Input Logic High	V _{IH}				2.4	2.4	2.4	2.4	2.4	2.4	V		
Drain-Source On Resistance	r _{DS(ON)}	I _S = 10mA, V _D = ±10V	IH5048A only		25	25	35	30	30	45	Ω		
			All others		40	40	60	45	45	75			
Channel-to-Channel r _{DS(ON)} Match	Δr _{DS(ON)}				8 (typ)			8 (typ)			Ω		
Minimum Analog Signal Handling Capability	V _{ANALOG}				±14	±14	±14	±14	±14	±14	V		
Switch-Off Leakage Current	I _D /I _{S(OFF)}	V _{ANALOG} = -10V to 10V			±1	±100		±5	±100		nA		
Switch-On Leakage Current	I _{D(ON)} + I _{S(ON)}	V _D = V _S = -10V to 10V			±2	±200		±10	±200		nA		
Switch-On Time	t _{ON}	Figure 1			400			600			ns		
Switch-Off Time	t _{OFF}	Figure 1			200			300			ns		
Charge Injection	Q _{INJ}	Figure 2 (Note 2)			10 (typ)			10 (typ)			pC		
Minimum Off-Isolation Rejection Ratio	O _{IRR}	Figure 3, C _L < 5pF			54 (typ)			50 (typ)			dB		

CMOS Analog Switches

ELECTRICAL CHARACTERISTICS (continued)

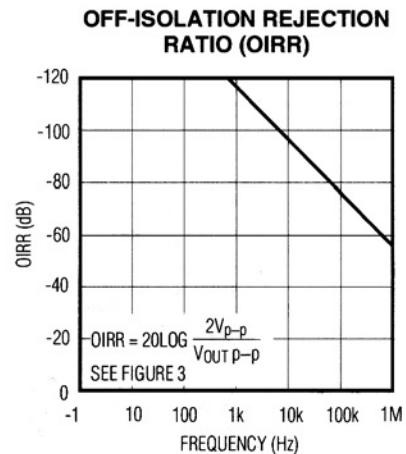
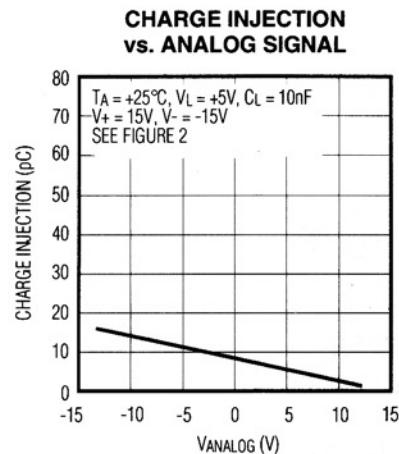
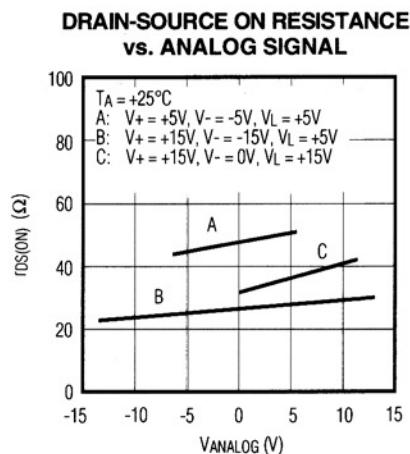
(V₊ = 15V, V₋ = -15V, V_L = 5V, T_A = +25°C, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	IH50_M IH50_AM			IH50_C IH50_AC			UNITS
			-55°C	+25°C	+125°C	0°C	+25°C	+70°C	
V+ Quiescent Current	I _{+Q}	V _{IN} = 0V or 5V	1	1	10	10	10	100	μA
V- Quiescent Current	I _{-Q}	V _{IN} = 0V or 5V	-1	-1	-10	-10	-10	-100	μA
+5V Quiescent Current	I _{LQ}	V _{IN} = 0V or 5V	1	1	10	10	10	100	μA
Ground Quiescent Current	I _{GND}	V _{IN} = 0V or 5V	1	1	10	10	10	100	μA
Minimum Channel-to-Channel Cross-Coupling Rejection Ratio	CCRR	One channel off (Note 2)	54 (typ)			50 (typ)			dB

Note 2: Not production tested.

IH5048-IH5051

Typical Operating Characteristics



Test Circuits

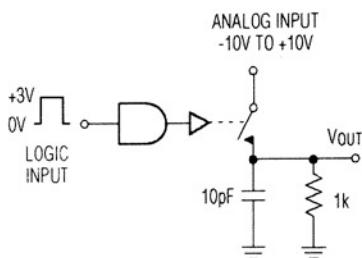


Figure 1. Switching Time

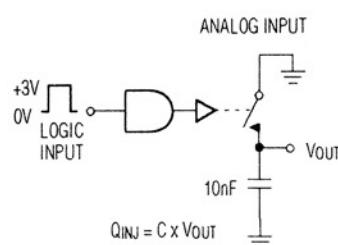


Figure 2. Charge Injection

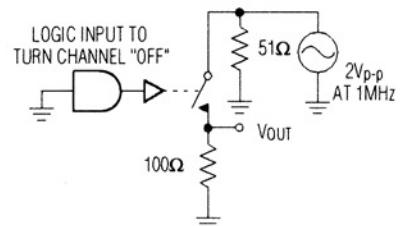
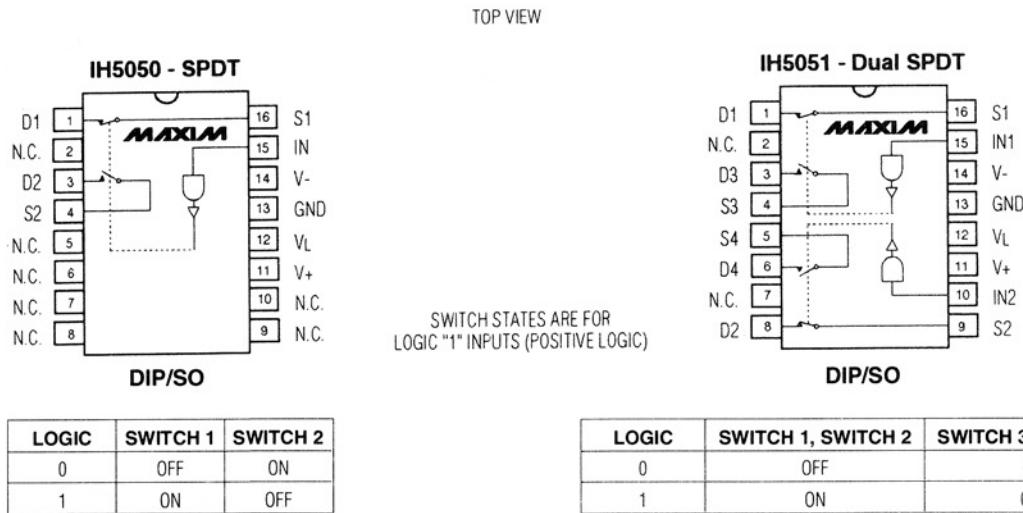


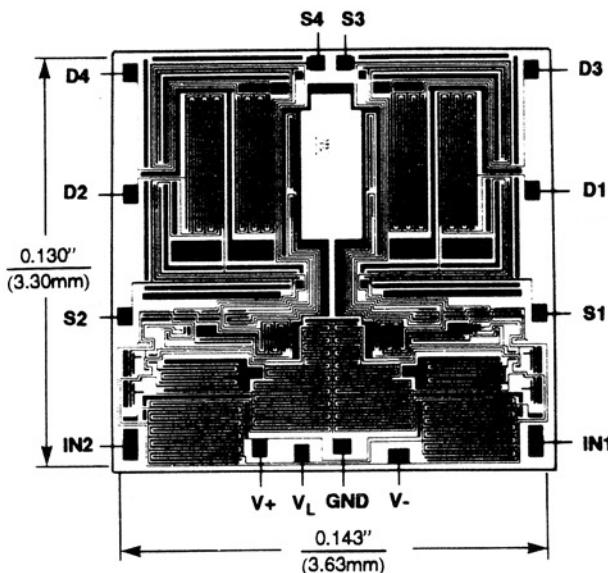
Figure 3. Off-Isolation Rejection Ratio

CMOS Analog Switches

Pin Configurations & Switching-State Diagrams (continued)



Chip Topography



Ordering Information (continued)

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IH5050CWE	0°C to +70°C	16 Wide SO
IH5050CJE	0°C to +70°C	16 CERDIP
IH5050C/D	0°C to +70°C	Dice*
IH5050MJE	-55°C to +125°C	16 CERDIP**
IH5051CPE	0°C to +70°C	16 Plastic DIP
IH5051CWE	0°C to +70°C	16 Wide SO
IH5051ACJE	0°C to +70°C	16 CERDIP
IH5051AC/D	0°C to +70°C	Dice*
IH5051MJE	-55°C to +125°C	16 CERDIP**

* Contact factory for dice specifications.

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