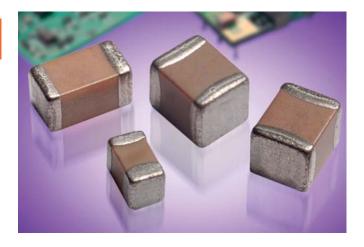
COG (NP0) Dielectric

General Specifications



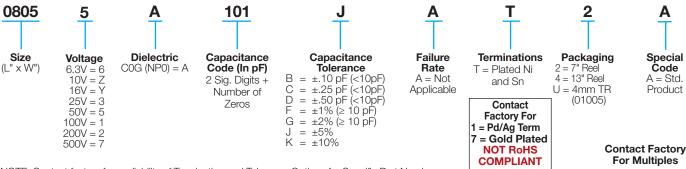


COG (NPO) is the most popular formulation of the "temperature-compensating," EIA Class I ceramic materials. Modern COG (NPO) formulations contain neodymium, samarium and other rare earth oxides.

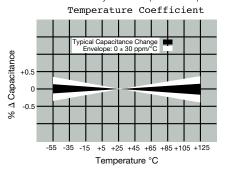
COG (NP0) ceramics offer one of the most stable capacitor dielectrics available. Capacitance change with temperature is 0 \pm 30ppm/°C which is less than \pm 0.3% Δ C from -55°C to +125°C. Capacitance drift or hysteresis for COG (NP0) ceramics is negligible at less than \pm 0.05% versus up to \pm 2% for films. Typical capacitance change with life is less than \pm 0.1% for COG (NP0), one-fifth that shown by most other dielectrics. COG (NP0) formulations show no aging characteristics.

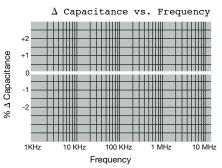
PART NUMBER (see page 2 for complete part number explanation)

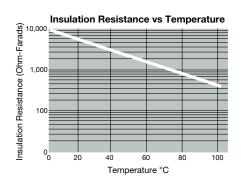


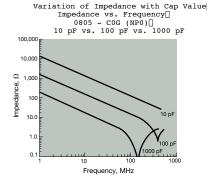


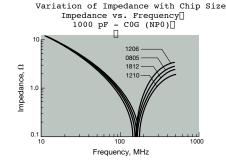
NOTE: Contact factory for availability of Termination and Tolerance Options for Specific Part Numbers. Contact factory for non-specified capacitance values.











Variation of Impedance with Ceramic Formu Impedance vs. Frequency[] 1000 pF - COG (NPO) vs X7R[] 0805[] X7R NPO 1000 Frequency, MHz



C0G (NP0) Dielectric



Specifications and Test Methods

	ter/Test	NP0 Specification Limits	Measuring Conditions							
Operating Temp		-55°C to +125°C	Temperature Cycle Chamber							
Capac	itance	Within specified tolerance	Freq.: 1.0 MHz ± 10% for cap ≤ 1000 pF							
)	<30 pF: Q≥ 400+20 x Cap Value	1.0 kHz \pm 10% for cap > 1000 pF							
		≥30 pF: Q≥ 1000	Voltage: 1.0Vrms ± .2V							
Insulation	Resistance	100,000MΩ or 1000MΩ - μF,	Charge device with rated voltage for							
		whichever is less	60 ± 5 secs @ room temp/humidity Charge device with 300% of rated voltage for							
Dielectric	Strength	No breakdown or visual defects	1-5 seconds, w/charge and discharge current limited to 50 mA (max)							
			Note: Charge device with 150% of rated voltage for 500V devices.							
	Appearance	No defects	Deflection: 2mm							
Resistance to Flexure Stresses	Capacitance Variation	±5% or ±.5 pF, whichever is greater	Test Time: 30 seconds							
	Q	Meets Initial Values (As Above)								
	Insulation Resistance	≥ Initial Value x 0.3	90 mm —							
Solder	rability	≥ 95% of each terminal should be covered with fresh solder	Dip device in eutectic solder at $230 \pm 5^{\circ}$ C for 5.0 ± 0.5 seconds							
	Appearance	No defects, <25% leaching of either end terminal								
Resistance to Solder Heat	Capacitance Variation	≤ ±2.5% or ±.25 pF, whichever is greater	Dip device in eutectic solder at 260°C for 60 seconds. Store at room temperature for 24 ± 2 hours before measuring electrical properties.							
	Q	Meets Initial Values (As Above)								
	Insulation Resistance	Meets Initial Values (As Above)								
	Dielectric Strength	Meets Initial Values (As Above)								
Thermal Shock	Appearance	No visual defects	Step 1: -55°C ± 2° 30 ± 3 minutes							
	Capacitance Variation	≤ ±2.5% or ±.25 pF, whichever is greater	Step 2: Room Temp ≤ 3 minutes							
	Q	Meets Initial Values (As Above)	Step 3: +125°C ± 2° 30 ± 3 minutes							
	Insulation Resistance	Meets Initial Values (As Above)	Step 4: Room Temp ≤ 3 minutes							
	Dielectric Strength	Meets Initial Values (As Above)	Repeat for 5 cycles and measure after 24 hours at room temperature							
Load Life	Appearance	No visual defects								
	Capacitance Variation	≤ ±3.0% or ± .3 pF, whichever is greater	Charge device with twice rated voltage in test chamber set at 125°C ± 2°C for 1000 hours (+48, -0).							
	Q (C=Nominal Cap)	≥ 30 pF: Q≥ 350 ≥10 pF, <30 pF: Q≥ 275 +5C/2 <10 pF: Q≥ 200 +10C								
	Insulation Resistance	≥ Initial Value x 0.3 (See Above)	Remove from test chamber and stabilize at room temperature for 24 hours before measuring.							
	Dielectric Strength	Meets Initial Values (As Above)								
Load Humidity	Appearance	No visual defects								
	Capacitance Variation	≤ ±5.0% or ± .5 pF, whichever is greater	Store in a test chamber set at 85°C ± 2°C/85% ± 5% relative humidity for 1000 hours (+48, -0) with rated voltage applied.							
	Q	≥ 30 pF: Q≥ 350 ≥10 pF, <30 pF: Q≥ 275 +5C/2 <10 pF: Q≥ 200 +10C								
	Insulation Resistance	≥ Initial Value x 0.3 (See Above)	Remove from chamber and stabilize at room temperature for 24 ± 2 hours							
	Dielectric Strength	Meets Initial Values (As Above)	before measuring.							

C0G (NP0) Dielectric



Capacitance Range

PREFERRED SIZES ARE SHADED

				a a																			
SIZE	0101*	02	201	0402			0603					0805					1206						
Soldering	Reflow Only		w Only			Reflow/Wave					Reflow/Wave					Reflow/Wave							
Packaging mm	All Paper 0.40 ± 0.02		Paper ± 0.03	All Paper 1.00 ± 0.10		All Paper 1.60 ± 0.15					Paper/Embossed 2.01 ± 0.20					Paper/Embossed 3.20 ± 0.20							
(L) Length (in.)	(0.016 ± 0.0008) 0.20 ± 0.02	(0.024	± 0.001)				(0.063 ± 0.006) 0.81 ± 0.15				(0.079 ± 0.008) 1.25 ± 0.20					(0.126 ± 0.008) 1.60 ± 0.20							
(vv) vvidiri (in.)	(0.008 ± 0.0008)	(0.011	1 ± 0.001) (0.020 ± 0.004)			(0.032 ± 0.006)				(0.049 ± 0.008)					(0.063 ± 0.008)								
(t) Terminal mm (in.)	0.10 ± 0.04 (0.004 ± 0.0016)	(0.006	0.15 ± 0.05 0.25 ± 0.15 (0.006 ± 0.002) (0.010 ± 0.006)		06)	0.35 ± 0.15 (0.014 ± 0.006)				0.50 ± 0.25 (0.020 ± 0.010)					0.50 ± 0.25 (0.020 ± 0.010)								
Cap 0.5	16	25 A	50	16 C	25 C	50 C	16 G	25 G	50 G	100 G	200	16 J	25 J	50 J	100 J	200 J	16 J	25 J	50 J	100 J	200 J	500 J	
(pF) 1.0	B B	Α		С	С	С	G	G	G G	G		J	J	J	J	J	J	J	J	J	J	J	
1.2 1.5	В	A A	А	C C	C C	C C	G G	G G	G	G G		J	J	J	J	J J	J	J J	J J	J	J J	J	
1.8 2.2	B B	A A	A A	00	C	0	G G	G G	G G	G G		J	J	J	J	J	J	J J	J	J	J J	J	
2.7	B B	A A	A	C	C	C	G G	G G	G G	G G		J	J	J	J	J	J	J	J	J	J	J	
3.9	В	Α	A	С	С	С	G	G	G	G		J	J	J	J	J	J	J	J	J	J	J	
<u>4.7</u> 5.6	B B	A A	A	C	C	C	G G	G G	G G	G G		J	J	J	J	J	J	J	J	J	J	J	
6.8 8.2	B B	A A	A	C	C	C C	G G	G G	G G	G G		J J	J J	J	J J	Ĵ	J	J J	J J	J J	J J	J J	
10	В	Α	Α	С	С	С	G	G	G	G	G	J	J	J	J	J	J	J	J	J	J	J	
12 15	B B	A A	A A	C	C	C C	G G	G G	G G	G G	G G	J	J	J	J	J	J	J J	J	J J	J J	J J	
18 22	B B	A A	A	C	C	C	G G	G G	G G	G G	G G	J	J	J	J	J	J	J	J	J	J J	J	
27	В	Α	Α	С	С	С	G	G	G	G	G	J	J	J	J	J	J	J	J	J	J	J	
33 39	B B	A A	A A	C	C	C C	G G	G G	G G	G G	G G	J	J	J	J	J	J	J J	J	J	J J	J	
<u>47</u> 56	B B	A A	A A	C	C C	C C	G G	G G	G G	G G	G G	J	J	J	J	J	J	J J	J	J	J	J	
68	В	Α	Α	С	С	С	G	G	G	G	G	J	J	J	J	J	J	J	J	J	J	J	
100	B B	A	A	C	C	C	G G	G G	G G	G G	G G	J	J	J	J	J	J	J	J	J	J	J	
120 150				C	C	C C	G G	G G	G G	G G	G G	J J	J	J J	J	J	J	J J	J	J J	J J	J J	
180 220				C	C	C	G G	G G	G G	G G	G G	J	J	J	J	J	J	J	J	J	J	J	
270				С	С	С	G	G	G	G	G	J	J	J	J	J	J	J J	J	J	J J	M M	
330 390				00	C	С	G G	G G	G G	G G		J	J	J	J	J	J	J J	J	J	J J	M M	
470 560				C	C C	C C	G G	G G	G G	G G		J	J	J	J	J	J	J J	J	J	J	M M	
680				С	С	С	G	G	G	G		J	J	J	J	J	J	J	J	J	J	P	
820 1000				С	C	C	G G	G G	G G	G G		J	J	J	J	J	J	J	J	J	M Q		
1200 1500							G G	G G	G G			J J	J	J	J		J J	J J	J	J M	Q Q		
1800							G	G	G			J	J	J	N		J	J	М	M P	Q		
2200 2700							G G	G G	G G			N N	N N	N N	N N		J	J J	M M	P	Q Q		
3300 3900							G G	G G	G G			P P	P P	P P	N N		J J	J J	M M	P P	Q		
4700 5600							G	G	G			Р	P P	P P	N		J	J	M M	P P			
6800	'	•										P	Р	Р			M	M	M	Р			
8200 Cap 0.010	 	اسيع		\sim		<						P P	P P	P P			M P	M P	M P	P P			
(μF) 0.012 0.015			\sim)	.لل	ŢT																	
0.018		_		1	-	1																	
0.022 0.027			4	t 1																			
0.033																							
0.047																	-						
0.082																							
U.1 WVDC	16	25	50	16	25	50	16	25	50	100	200	16	25	50	100	200	16	25	50	100	200	500	
SIZE	0101*	02	201		0402				0603					0805					12	06			
Letter Max.	A B	2	C 0.56	0.	71	G 0.90		J).94	K 1.02		M 1.27	N 1.4	<u> </u>	P 1.52	1.		X 2.29		Y .54	Z 2.79			
	.013) (0.00		(0.022)	(0.0		(0.035)		.037)	(0.040		0.050)	(0.05		0.060)	(0.0		(0.090)		100)	(0.110)			
	PAPER											EMBOSSED											

PAPER and EMBOSSED available for 01005

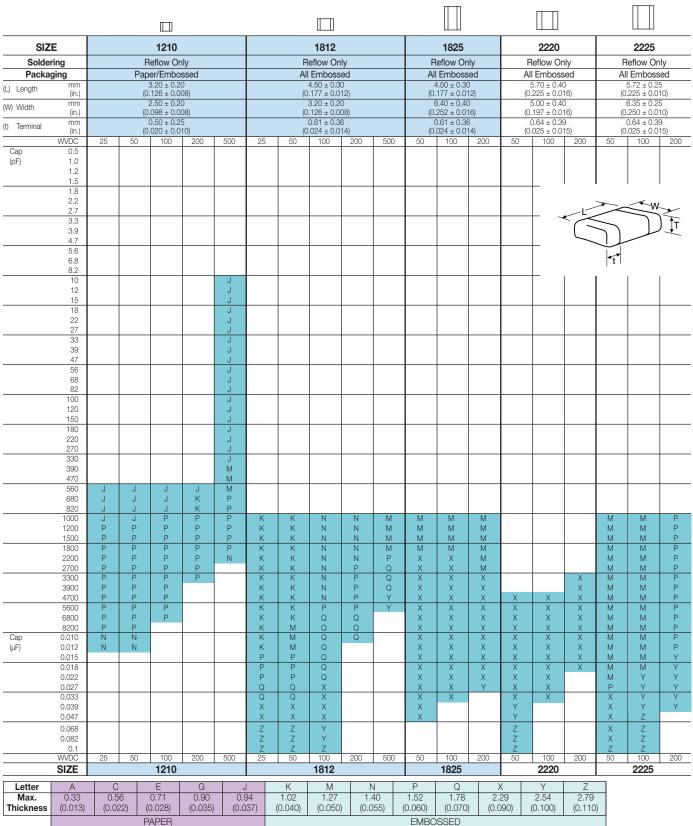


C0G (NP0) Dielectric



Capacitance Range

PREFERRED SIZES ARE SHADED



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CGA2B2C0G1H2R2C CGA2B2C0G1H3R3C CGA2B2C0G1H680J CGA2B2C0G1H6R8D CGA2B2X8R1H221K CGA2B2X8R1H472K

CGA3E1X7R1C474K