

CXDM4060P

**SURFACE MOUNT SILICON
P-CHANNEL
ENHANCEMENT-MODE
MOSFET**

**SOT-89 CASE****APPLICATIONS:**

- Load/Power switches
- Power supply converter circuits
- Battery powered portable equipment

MAXIMUM RATINGS: ($T_A=25^\circ\text{C}$)

	SYMBOL		UNITS
Drain-Source Voltage	V_{DS}	40	V
Gate-Source Voltage	V_{GS}	25	V
Continuous Drain Current (Steady State)	I_D	6.0	A
Maximum Pulsed Drain Current, $t_p=10\mu\text{s}$	I_{DM}	20	A
Power Dissipation	P_D	1.2	W
Operating and Storage Junction Temperature	T_J, T_{stg}	-55 to +150	$^\circ\text{C}$
Thermal Resistance	Θ_{JA}	104	$^\circ\text{C}/\text{W}$

ELECTRICAL CHARACTERISTICS: ($T_A=25^\circ\text{C}$ unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I_{GSSF}, I_{GSSR}	$V_{GS}=25\text{V}, V_{DS}=0$			100	nA
I_{DSS}	$V_{DS}=40\text{V}, V_{GS}=0$			1.0	μA
BV_{DSS}	$V_{GS}=0, I_D=250\mu\text{A}$	40			V
$V_{GS(\text{th})}$	$V_{GS}=V_{DS}, I_D=250\mu\text{A}$	1.0	2.0	3.0	V
V_{SD}	$V_{GS}=0, I_S=2.0\text{A}$			1.2	V
$r_{DS(\text{ON})}$	$V_{GS}=10\text{V}, I_D=6.0\text{A}$		48	65	$\text{m}\Omega$
$r_{DS(\text{ON})}$	$V_{GS}=4.5\text{V}, I_D=4.0\text{A}$		80	95	$\text{m}\Omega$
C_{rss}	$V_{DS}=25\text{V}, V_{GS}=0, f=1.0\text{MHz}$		61		pF
C_{iss}	$V_{DS}=25\text{V}, V_{GS}=0, f=1.0\text{MHz}$		750		pF
C_{oss}	$V_{DS}=25\text{V}, V_{GS}=0, f=1.0\text{MHz}$		56		pF
$Q_{g(\text{tot})}$	$V_{DS}=32\text{V}, V_{GS}=4.5\text{V}, I_D=6.0\text{A}$		6.5		nC
Q_{gs}	$V_{DS}=32\text{V}, V_{GS}=4.5\text{V}, I_D=6.0\text{A}$		3.2		nC
Q_{gd}	$V_{DS}=32\text{V}, V_{GS}=4.5\text{V}, I_D=6.0\text{A}$		2.7		nC
t_{on}	$ V_{DS}=20\text{V}, V_{GS}=10\text{V}, I_D=1.0\text{A} $		18		ns
t_{off}	$ R_G=3.0\Omega, R_L=20\Omega $		64		ns


www.centralsemi.com
DESCRIPTION:

The CENTRAL SEMICONDUCTOR CXDM4060P is a high current silicon P-Channel enhancement-mode MOSFET, designed for high speed pulsed amplifier and driver applications. This MOSFET features high current, low $r_{DS(\text{ON})}$, low threshold voltage, and low gate charge.

MARKING: FULL PART NUMBER**FEATURES:**

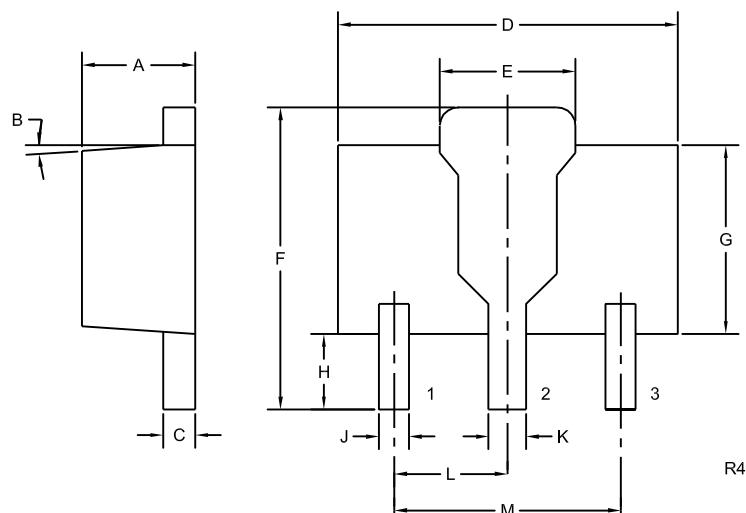
- Low $r_{DS(\text{ON})}$ (48m Ω TYP @ $V_{GS}=10\text{V}$)
- High current ($I_D=6.0\text{A}$)
- Logic level compatibility

CXDM4060P

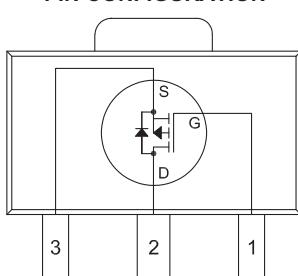
SURFACE MOUNT SILICON
P-CHANNEL
ENHANCEMENT-MODE
MOSFET



SOT-89 CASE - MECHANICAL OUTLINE



PIN CONFIGURATION



(Top View)
Tab is common to pin 2

LEAD CODE:

- 1) Gate
- 2) Drain
- 3) Source

MARKING: FULL PART NUMBER

SYMBOL	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.055	0.067	1.40	1.70
B	4°		4°	
C	0.014	0.018	0.35	0.46
D	0.173	0.185	4.40	4.70
E	0.064	0.074	1.62	1.87
F	0.146	0.177	3.70	4.50
G	0.090	0.106	2.29	2.70
H	0.028	0.051	0.70	1.30
J	0.014	0.019	0.36	0.48
K	0.017	0.023	0.44	0.58
L	0.059		1.50	
M	0.118		3.00	

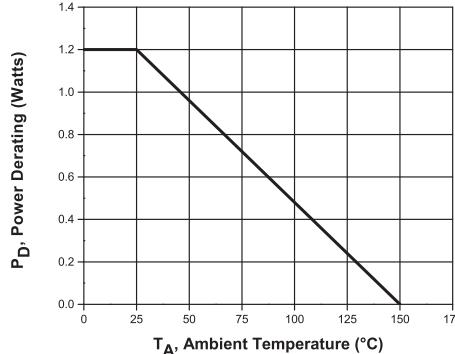
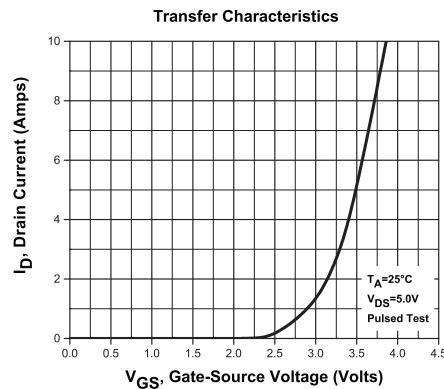
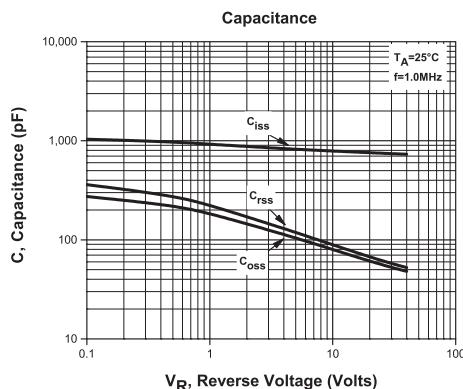
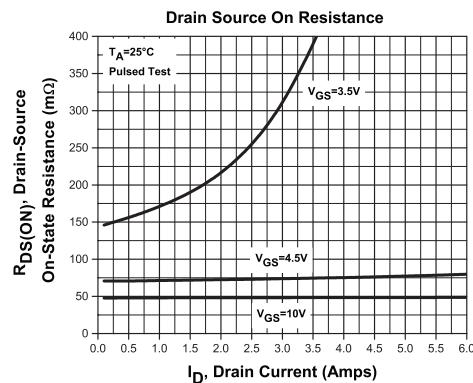
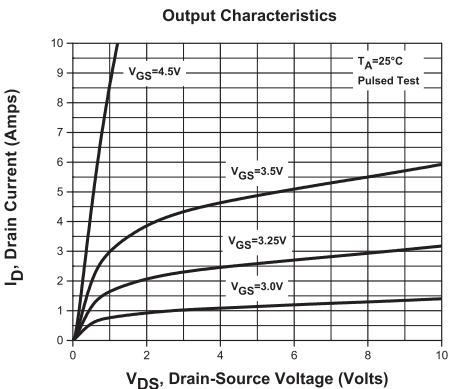
SOT-89 (REV: R4)

CXDM4060P

**SURFACE MOUNT SILICON
P-CHANNEL
ENHANCEMENT-MODE
MOSFET**



TYPICAL ELECTRICAL CHARACTERISTICS



R1 (28-March 2013)

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for MOSFET category:

Click to view products by Central Semiconductor manufacturer:

Other Similar products are found below :

[614233C](#) [648584F](#) [MCH3443-TL-E](#) [MCH6422-TL-E](#) [FDPF9N50NZ](#) [FW216A-TL-2W](#) [FW231A-TL-E](#) [APT5010JVR](#) [NTNS3A92PZT5G](#)
[IRF100S201](#) [JANTX2N5237](#) [2SK2464-TL-E](#) [2SK3818-DL-E](#) [FCA20N60_F109](#) [FDZ595PZ](#) [STD6600NT4G](#) [FSS804-TL-E](#) [2SJ277-DL-E](#)
[2SK1691-DL-E](#) [2SK2545\(Q,T\)](#) [D2294UK](#) [405094E](#) [423220D](#) [MCH6646-TL-E](#) [TPCC8103,L1Q\(CM](#) [367-8430-0972-503](#) [VN1206L](#)
[424134F](#) [026935X](#) [051075F](#) [SBVS138LT1G](#) [614234A](#) [715780A](#) [NTNS3166NZT5G](#) [751625C](#) [873612G](#) [IRF7380TRHR](#)
[IPS70R2K0CEAKMA1](#) [RJK60S3DPP-E0#T2](#) [RJK60S5DPK-M0#T0](#) [APT5010JVFR](#) [APT12031JFLL](#) [APT12040JVR](#) [DMN3404LQ-7](#)
[NTE6400](#) [JANTX2N6796U](#) [JANTX2N6784U](#) [JANTXV2N5416U4](#) [SQM110N05-06L-GE3](#) [SIHF35N60E-GE3](#)