



ON Semiconductor®

<http://onsemi.com>

LV4910T

Bi-CMOS LSI

Class-D Audio Power Amplifier

BTL 2W x 2ch

Overview

LV4910T is a stereo digital amplifier for portable equipment, for example notebook-PC, portable DVD and portable mini-speakers. It is characterized by the use of an original feedback technology to improve sound quality though it is Class-D amplifier, and does not need the LC filter in the output stage.

Features

- D-class high-efficiency amplifier
- Low pop sound at SW changeover
- Differential input type

Functions

- 2W stereo digital power amplifier
- Standby switch
- Mute switch
- Various protective circuits (over-current protective, thermal protective, and under-voltage circuits) incorporated

Specifications

Absolute Maximum Ratings at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	$V_{CC\ max}$		6	V
Allowable power dissipation	$P_d\ max$	as mounted on the substrate	1.05	W
Operating temperature	T_{opr}		-20 to +75	$^\circ\text{C}$
Storage temperature	T_{stg}		-40 to +150	$^\circ\text{C}$

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

LV4910T

Operating Conditions at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	V_{CC}		5	V
Operation supply voltage range	$V_{CC\text{ opg}}$		2.5 to 5.5	V
Recommended load resistance	R_L	Speaker	4	Ω

Electrical Characteristics $T_a = 25^\circ\text{C}$, $V_{CC} = 5\text{V}$, $f = 1\text{kHz}$, $R_L = 4\Omega$

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Standby current	I_{st}	Current at ST ON			1	μA
Current at no signal	I_{CCO1}	At LC filter-less		12	20	mA
Current at Mute	$I_{CCO\text{ mute}}$	At Mute of speaker		10	16	mA
Voltage gain	VG	$V_O = 0\text{dBm}$	21	23	25	dB
Channel balance	ΔVG	$V_O = 0\text{dBm}$	-1	0	1	dB
Output power	P_O	THD = 10%		2		W
Total harmonic distortion	THD	$P_O = 0.5\text{W}$, DIN AUDIO		0.4	0.7	%
Output noise voltage	V_{NO}	$R_g = 0$, DIN AUDIO		100	200	μV
Crosstalk	CT	$V_O = 0\text{dBm}$, TUN 1kHz		-60	-40	dB
Ripple rejection ratio	RR	$f_r = 100\text{Hz}$, $V_r = -10\text{dBm}$, TUN 100Hz		-40	-30	dB
Common mode rejection ratio	CMRR	$V_O = 0\text{dBm}$, DIN AUDIO		-60	-40	dB
Mute attenuation value	V_{OFF}	$V_O = 0\text{dBm}$, DIN AUDIO		-80	-70	dB
Oscillation frequency	FPWM			300		kHz
Standby ON voltage sensitivity	V_{PWROFF}	Standby ON start voltage			1	V
Standby OFF voltage sensitivity	V_{PWRON}	Standby OFF start voltage	3			V
Mute ON voltage sensitivity	V_{MUTEON}	Mute ON start voltage			0.5	V
Mute OFF voltage sensitivity	$V_{MUTEOFF}$	Mute OFF start voltage	2			V

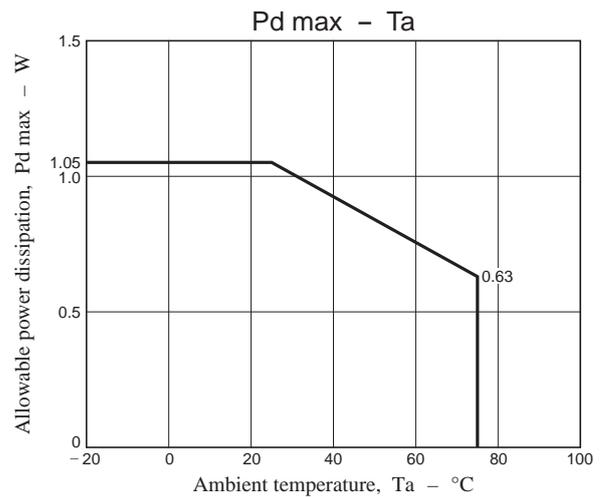
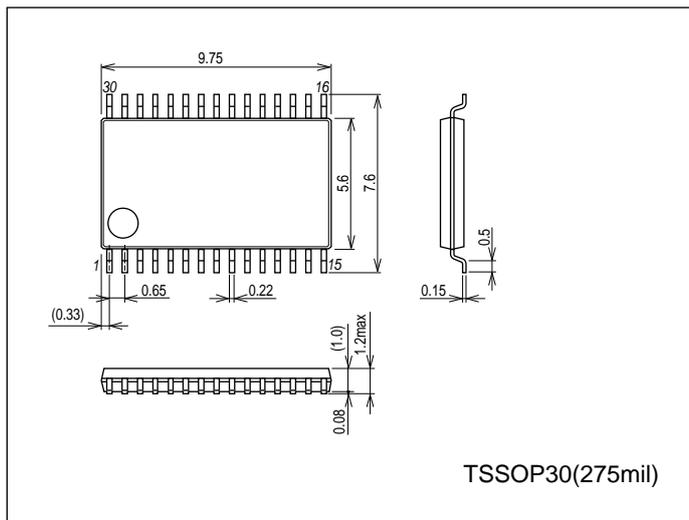
* Electrical characteristics vary depending on the substrate layout and selection of external parts.

For measurement of the above characteristics, the coil : 22 μH (Toko Kabushiki Kaisha made D63CB) is used.

Package Dimensions

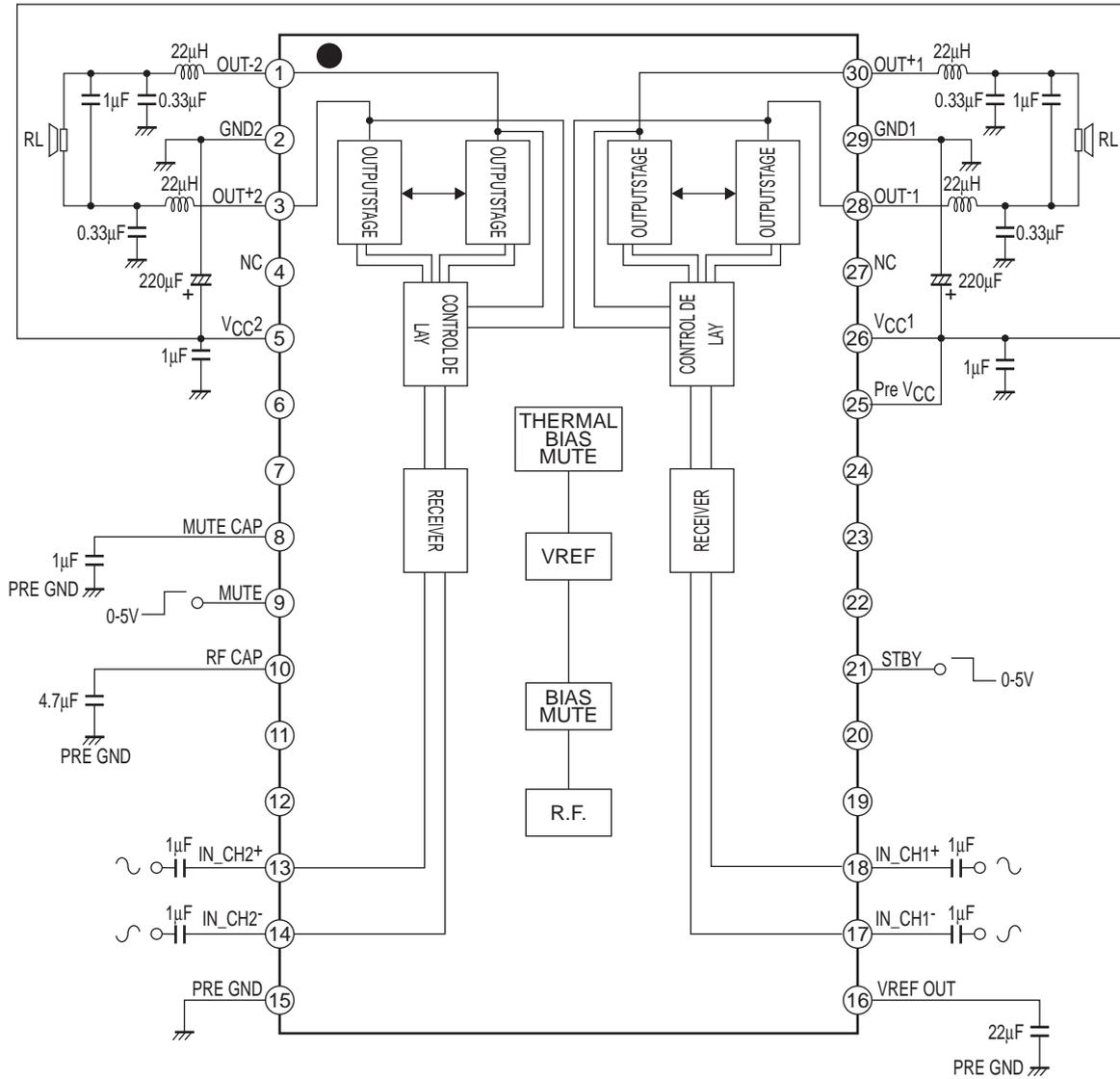
unit : mm (typ)

3259



LV4910T

Block Diagram



LV4910T

Pin Descriptions

Pin No.	Pin name	Pin voltage (V)	Pin description	Equivalent circuit
1 3 28 30	OUT-2 OUT+2 OUT-1 OUT+1	2.58	<ul style="list-style-type: none"> Power outputs 	
2	GND2	0		
4	NC		<ul style="list-style-type: none"> Non-connection 	
5	V _{CC2}	5		
6	NC		<ul style="list-style-type: none"> Non-connection 	
7	NC		<ul style="list-style-type: none"> Non-connection 	
8	MUTE CAP	4.9	<ul style="list-style-type: none"> Connection for the mute switch On/Off impulse noise reduction capacitor 	
9	MUTE		<ul style="list-style-type: none"> Mute On/Off switch 2 to 5.5V : Mute Off 0 to 0.7V : Mute On 	
10	RF CAP	2.6	<ul style="list-style-type: none"> Ripple filter reference 	
11	NC		<ul style="list-style-type: none"> Non-connection 	
12	NC		<ul style="list-style-type: none"> Non-connection 	
13 14 17 18	IN_ch2+ IN_ch2- IN_ch1- IN_ch1+	2.4	<ul style="list-style-type: none"> Signal input 	

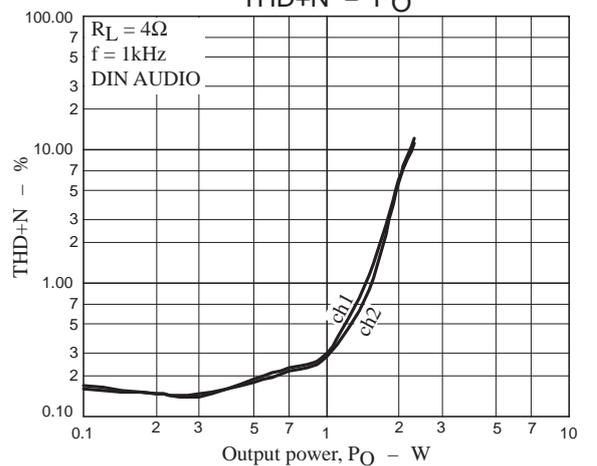
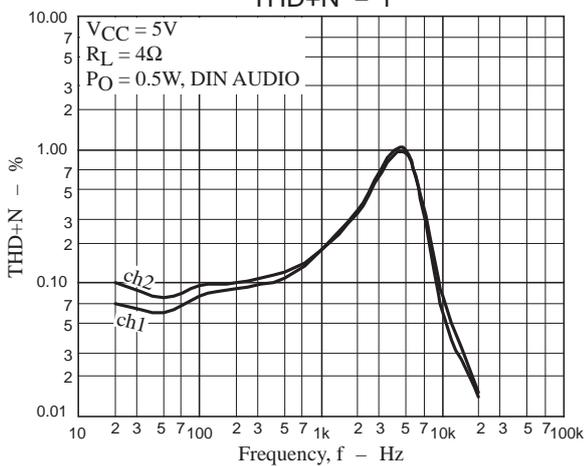
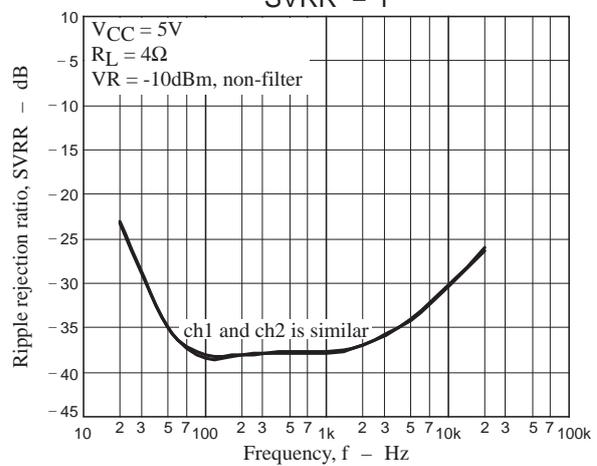
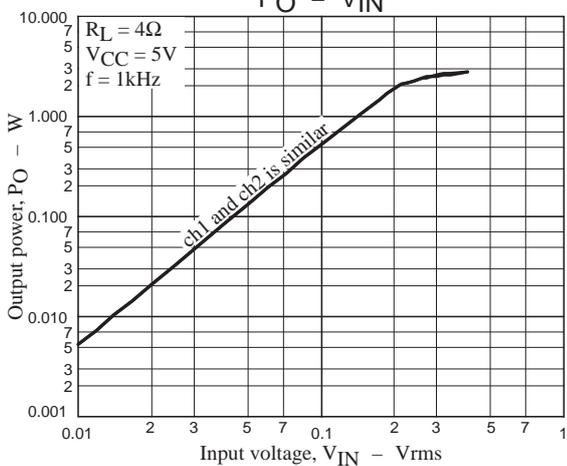
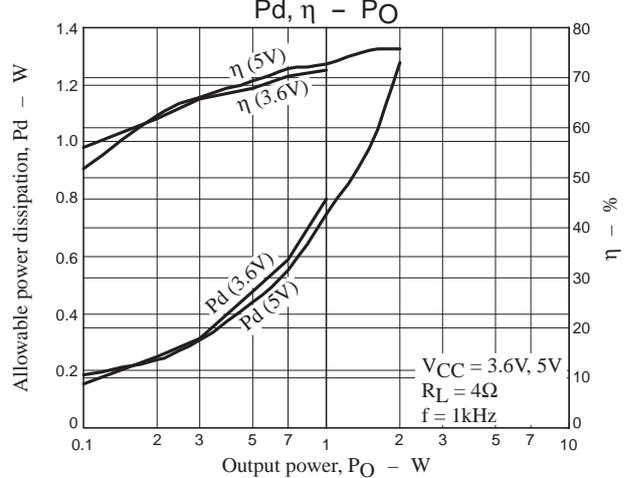
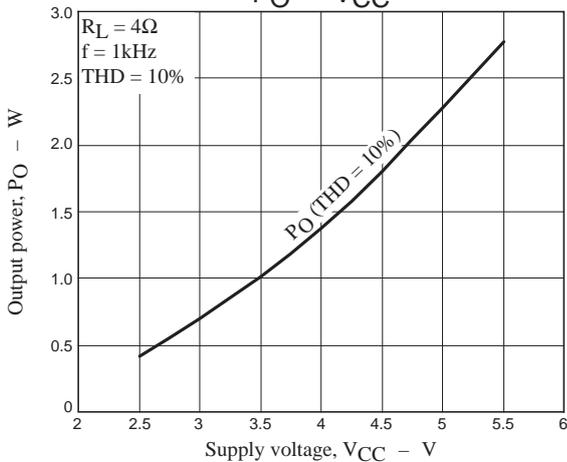
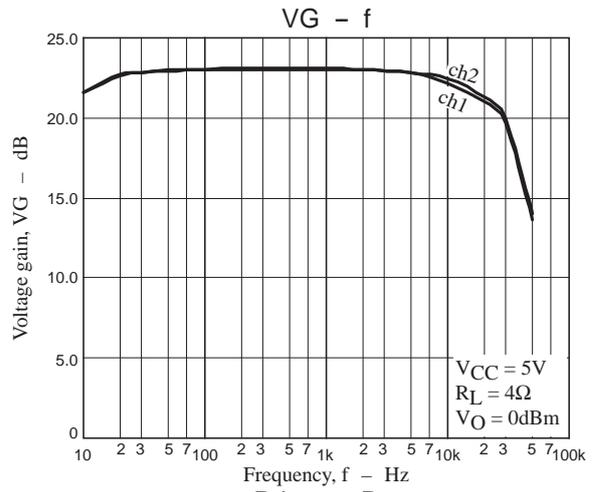
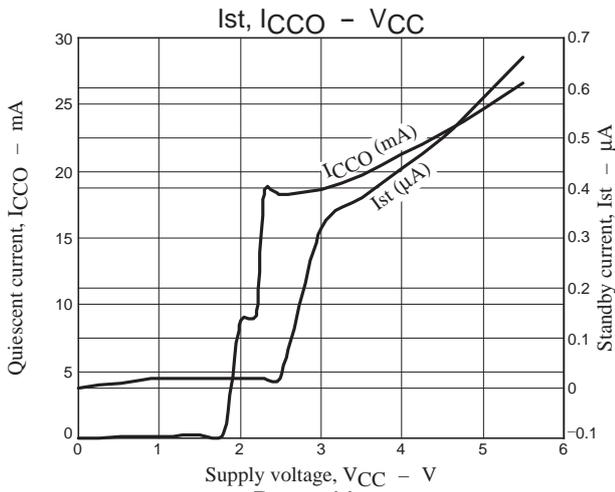
Continued on next page.

LV4910T

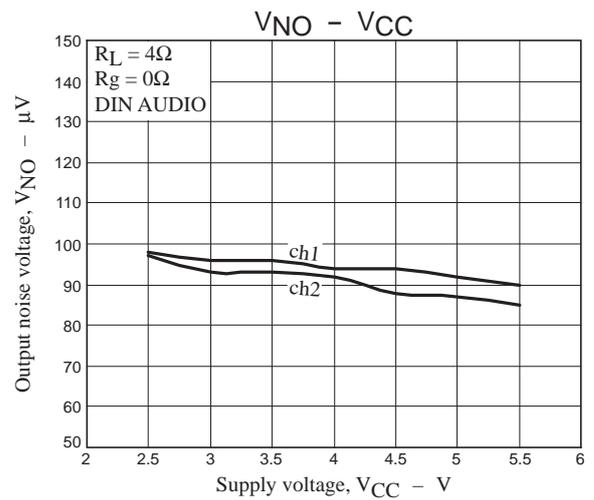
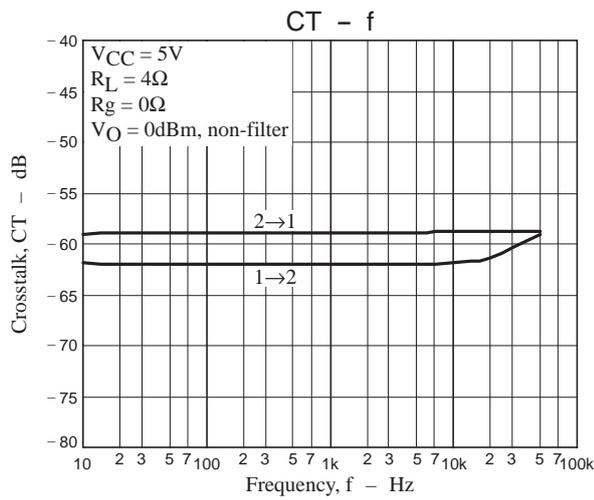
Continued from preceding page.

Pin No.	Pin name	Pin voltage (V)	Pin description	Equivalent circuit
15	PRE GND	0		
16	VREF OUT	2.55	<ul style="list-style-type: none"> • VREF amplifier reference 	
19	NC		<ul style="list-style-type: none"> • Non-connection 	
20	NC		<ul style="list-style-type: none"> • Non-connection 	
21	STBY		<ul style="list-style-type: none"> • STBY On/Off switch • 0 to 1V : Power Off • 3 to 5.5V : Power On 	
22	NC		<ul style="list-style-type: none"> • Non-connection 	
23	NC		<ul style="list-style-type: none"> • Non-connection 	
24	NC		<ul style="list-style-type: none"> • Non-connection 	
25	PRE V _{CC}	5		
26	V _{CC} 1	5		
27	NC		<ul style="list-style-type: none"> • Non-connection 	
29	GND1	0		

LV4910T



LV4910T



ON Semiconductor and the ON logo are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for [Audio Amplifiers](#) category:

Click to view products by [ON Semiconductor](#) manufacturer:

Other Similar products are found below :

[LV47002P-E](#) [MP7747DQ-LF-P](#) [AZ386MTR-E1](#) [NCP2811AFCT1G](#) [NCP2890AFCT2G](#) [NJM8068RB1-TE1](#) [NJW1194V-TE1](#) [LA4282-E](#)
[LA4814JA-AE](#) [LC706200CM](#) [SSM2377ACBZ-R7](#) [FDA2100LV](#) [TDA2541](#) [TDA7385H](#) [TDA7391LV](#) [TDA7575BPDTR](#) [TDA7718NTR](#)
[IS31AP2121-LQLS1](#) [IS31AP4915A-QFLS2-TR](#) [LA74309FA-BH](#) [421067X](#) [480263C](#) [NCP2820FCT2G](#) [STPA001](#) [TDA1515AQ](#) [TDA1520B](#)
[TDA1591T](#) [TDA2051H](#) [TDA4850](#) [TDA7391PDUTR](#) [TDA7563BH](#) [TDA7718B](#) [LA4425F-E](#) [LA4742-E](#) [TDA7391PDU](#)
[TDA7491MV13TR](#) [TDA749213TR](#) [TDA7563AH](#) [TDA7850H](#) [STK433-070GN-E](#) [E-TDA7391PDTR](#) [SSM2529ACBZ-R7](#) [SSM2518CBZ-](#)
[R7](#) [MAX9890BEBL+T](#) [MAX98303EWE+T](#) [MAX98358EWL+](#) [MAX98304DEWL+T](#) [MAX97220DETE+T](#) [TS4962MEIJT](#) [TS4990EIJT](#)