Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π–MOSV)

# 2SK2841

# Chopper Regulator, DC-DC Converter and Motor Drive Applications

• Low drain–source ON resistance :  $R_{DS (ON)} = 0.4 \Omega (typ.)$ • High forward transfer admittance :  $|Y_{fs}| = 8.0 S (typ.)$ • Low leakage current :  $I_{DSS} = 100 \mu A (max) (V_{DS} = 400 V)$ • Enhancement mode :  $V_{th} = 2.0 \text{ to } 4.0 \text{ V } (V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA})$ 

#### Absolute Maximum Ratings (Ta = 25°C)

Characteri	stics	Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	400	V
Drain-gate voltage (R	<sub>GS</sub> = 20 kΩ)	$V_{DGR}$	400	V
Gate-source voltage		V <sub>GSS</sub>	±30	V
Drain current	DC (Note 1)	I <sub>D</sub>	10	Α
	Pulse (Note 1)	I <sub>DP</sub>	40	Α
Drain power dissipatio	n (Tc = 25°C)	PD	80	W
Single pulse avalanche	e energy (Note 2)	E <sub>AS</sub>	360	mJ
Avalanche current		I <sub>AR</sub>	10	Α
Repetitive avalanche	energy (Note 3)	E <sub>AR</sub>	8	mJ
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C

1. GATE
2. DRAIN (HEAT SINK)
3. SOURCE

JEDEC TO-220AB

JEITA SC-46

TOSHIBA 2-10P1B

Weight: 2.0 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	1.56	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	83.3	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 5.85 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 10 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.

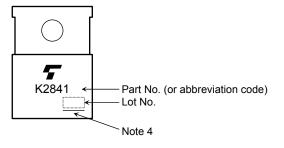
### **Electrical Characteristics (Ta = 25°C)**

Charac	eteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±25 V, V <sub>DS</sub> = 0 V		_	±10	μΑ
Gate-source bre	eakdown voltage	V (BR) GSS	I <sub>G</sub> = ±10 μA, V <sub>DS</sub> = 0 V	±30	_	_	V
Drain cut-off cur	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 400 V, V <sub>GS</sub> = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	400	_	_	V
Gate threshold v	oltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	4.0	V
Drain-source Ol	N resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 5.0 A	_	0.4	0.55	Ω
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 5.0 A	4.0	8.0	_	S
Input capacitano	е	C <sub>iss</sub>		_	1340	_	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	160	_	pF
Output capacitance		Coss			490	_	
Switching time	Rise time	t <sub>r</sub>	$V_{GS} = \frac{10V}{0V}$ $V_{OUT}$ $R_{L} = 40\Omega$ $V_{DD} = 200V$ $Duty \le 1\%, t_{W} = 10\mu s$	_	22	_	- ns
	Turn-on time	t <sub>on</sub>		_	60	_	
	Fall time	t <sub>f</sub>		1	32		
	Turn-off time	t <sub>off</sub>		1	140		
Total gate charge (gate-source plus gate-drain) Qg		Qg	V <sub>DD</sub> ≈ 320 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A		34		nC
Gate-source charge		Q <sub>gs</sub>			18		
Gate-drain ("miller") Charge		Q <sub>gd</sub>			16	_	

### Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	10	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	40	Α
Forward voltage (diode)	$V_{DSF}$	I <sub>DR</sub> = 10 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 10 A, V <sub>GS</sub> = 0 V	_	350	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> / dt = 100 A / μs	_	2.6	_	μC

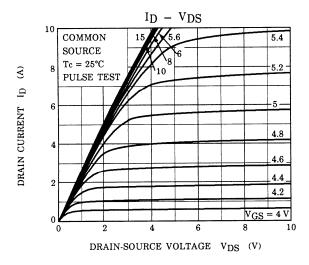
### Marking

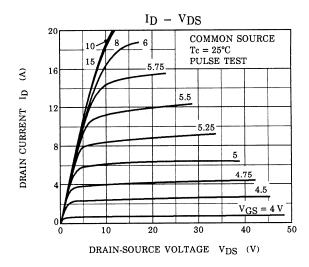


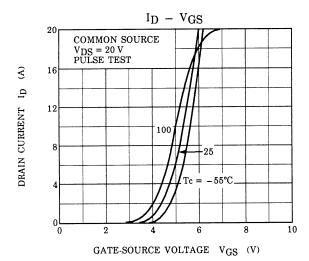
Note 4: A line under a Lot No. identifies the indication of product Labels.

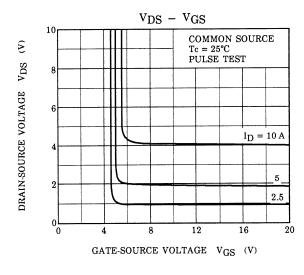
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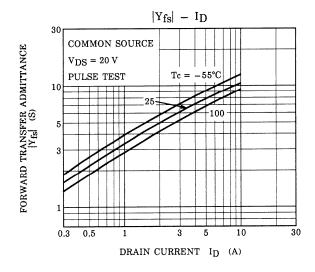
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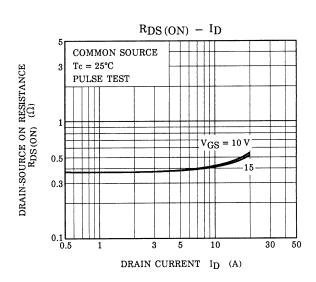




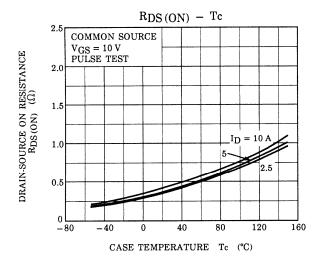


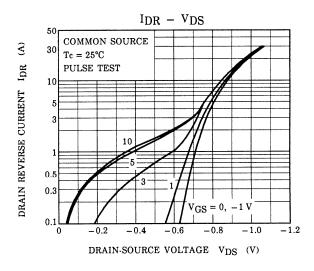


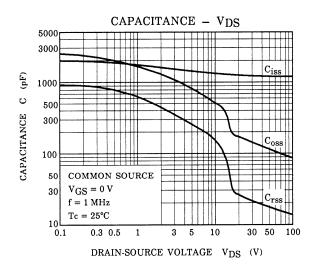


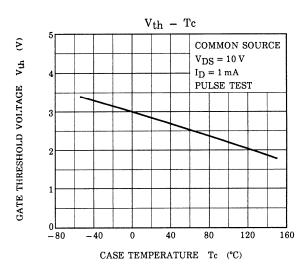


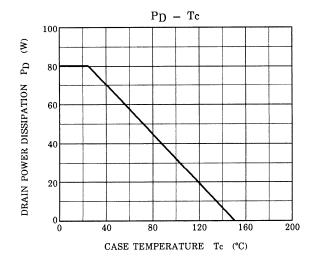
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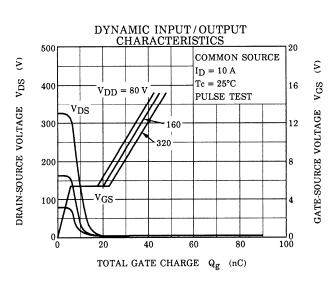


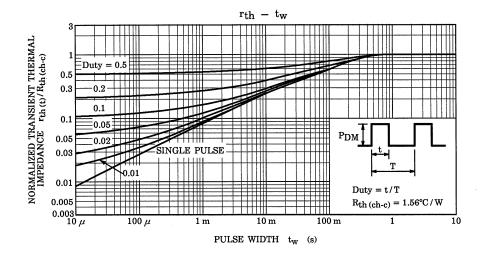


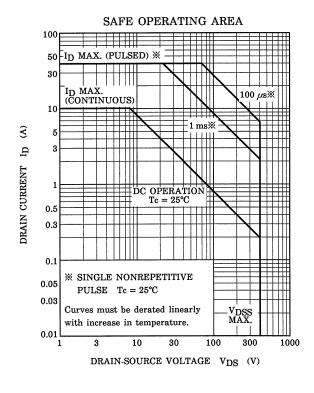


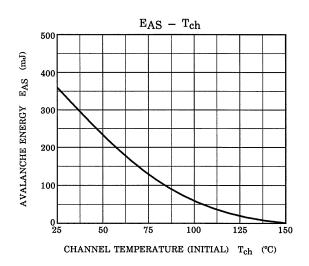


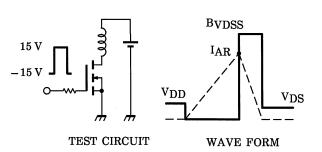












$$R_G$$
 = 25  $\Omega$   
 $V_{DD}$  = 90 V, L = 5.85 mH

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^{2} \cdot \left( \frac{BVDSS}{BVDSS - VDD} \right)$$

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